



## XILINX ISE SOFTWARE: CUSTOMER PROVEN

### ***On Ease of Use, Time-to Market, and Performance Benefits:***

*“Critical requirements for high performance visual computing are processing speed, high gate density for parallel operations and as much memory bandwidth as possible. Xilinx Virtex-II Pro FPGAs and ISE 5.1i software design tools provide us with the combination of speed, density and memory bandwidth, along with a superb development environment that enables Quantum3D to address the needs of the advanced visual simulation and training and embedded visual computing markets without having to use custom ASICs.”*

**- Ross Q. Smith, Founder/Executive V.P. of Marketing and Business Development Quantum3D**

*“Xilinx ISE 4.2i software tools’ advanced place and route technology kept runtimes short and performance high, allowing our design team to develop 1 Gigabit, 10 Gigabit, and OC-192c products all in a matter of months!”*

**- Jesse D. Beeson, Director of Engineering, IneoQuest Technologies**

*"We recognized that attention to physical characteristics of the design during synthesis was quickly becoming essential for large FPGAs. Using Synplicity’s Amplify which performs simultaneous placement and logic optimization and Xilinx ISE place & route we could quickly perform design iterations and get accurate timing and speed results right away. In addition, we found Xilinx ISE place & route run time was reduced to a fraction, due to the physical optimization during Synthesis"*

**- Thomas Zipper, Research and Development Engineer for Siemens ICN**

*“The combination of Xilinx high performance FPGAs and comprehensive ISE software tools provided us the low risk and feature-rich capability we needed to introduce a ground breaking networking product without employing custom ASICs.”*

**- Homayoun Valizadeh, VP of Engineering, Bivio Networks**

*“The ease of use, place and route time, quality of results, and timing driven layout of the Xilinx ISE software tools were essential to the success of our design. This software is truly phenomenal.”*

**- Dave Roth, Engineering Director, Allegro Networks**

*"The reconfigurable nature of Xilinx FPGAs offer time-to-market and design flexibility we just couldn't achieve with a traditional ASIC or any other programmable logic vendor. The intuitive ISE software allowed us to optimize designs for specific customer solutions. The combination of first class software and state of the art silicon made our decision an easy one."*

**- Ken Romano, Director, Hardware Engineering, Photon Vision Systems**

*“Xilinx ISE Foundation Software provided a ready-to-use solution combined with a powerful interface. The software was very fast and efficient and provided us with the ability to iterate new designs very rapidly. The software automates the creation of schematic and HDL symbols for inclusion within a design, making it easy to design complex circuits in minutes.”*

**- Stephen Caldera, Director, Hardware Engineering, Telica**

*“With NC-Sim, Synplify and the Xilinx ISE tools we have significantly sped-up our development and verification efforts, and we are able to provide customers of Intellitech's TEST-IP(TM) family of embedded test products with the highest quality designs”*

**- Mike Ricchetti, CTO, Intellitech Corporation**

*“We've had experience using other popular programmable logic design tools and prefer Xilinx ISE. The speed and seamless integration of the ISE Project Manager, entry, synthesis, place & route, and verification tools make the design process faster and easier. Xilinx ISE is a complete programmable design solution.”*

**- Tim Thomson, Chief Technologist, Kurzweil Music Systems**

*“Using Synopsys FPGA Compiler II with the Xilinx software has enabled us to achieve the required timing performance in our speed-critical projects targeting intelligent wireless solutions,”. “We used the retiming capability of FCII to increase the clock frequency of our design. The easy-to-use interfaces allowed us to get up to speed on the tool quickly. FCII also has the same HDL reader as Design Compiler, which simplified migrating our ASIC design to FPGA.”*

**- Matthew Wishek, Director of IC Engineering, CarrierComm**

## ***On Access to Software before Silicon***

*“Because we had access in advance to the preliminary information for the Xilinx Virtex-II Pro FPGAs, we were able to evaluate the architecture for our needs. We've verified and are encouraged that it matches very well with the architecture of our next generation of high capacity optical multiservice nodes and gateways.”*

**- Stefano Gastaldello, ASICs Lab Manager, Alcatel, Italy**

*“Having early access to the design tools for the Xilinx Virtex-II Pro FPGA and the preliminary information, we've begun early design work for a client project involving secure, high-speed video transmission. What we've seen in the Virtex-based product tools with the combination of the PowerPC processors and high-speed serial transceivers will accelerate time-to-market and reduce overall costs because of the functional integration we're leveraging in this new FPGA.”*

**- Chris Hallahan, VP of Sales and Marketing, Nuvation Engineering**

*“With the immersion of PowerPC cores, Xilinx has developed the optimal platform to implement our storage network designs. The Virtex-II Pro devices provide the critical elements of system-level design, including high-speed serial I/O, making it an ideal combination for our next generation products. Moreover, the unique architectural synthesis design environment allows us to make hardware and software tradeoffs throughout the design cycle.”*

**- Sandy Helton, Executive Vice President and CTO, SAN Valley**