AMD XILINX

Design Services Partner Profile

Xylon d.o.o. Company Overview

Partner Details

Partner Tier:	Premier
Headquarters Location:	Fallerovo Setaliste 22,Zagreb,,Croatia
Xilinx Alliance Partner Since:	4/11/2000

Key Contact Info

Ivana Vojnovic
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Regions Served

APAC, Africa, Central and South America, Eastern Europe and Russia, India, Japan, Middle East, North America Central, North America East, North America West, Western Europe

Supported Product Types

Yes	Design Services	Yes	Embedded Products
Yes	IP	No	EDA Tools
Yes	Boards		

Engineering Service Types

Yes	General Purpose Services	Preferred
Yes	Services in Support of Products Sold	
Yes	Services Leading to Contract Manufacturing	
Maximum Service Scope:	Turnkey / full product design	

Engagement Models Supported

Yes	SOW-Based Fixed Bid Projects	Preferred
Yes	Time and Materials	
Both	Captive Resources at Partner or Customer Premises	

Xilinx Design Tool Experience

Yes	Vivado	Yes	SDSoC
Yes	HLx	No	SDAccel

Industry Certifications Achieved   ISO 9001

Industry Certification Services Offered

Government Clearances   None

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Xylon d.o.o. Segment and Application Focus

Primary Focus	**Defense/Aerospace**	No Focus	Homeland Security
		No Focus	Electronic Warfare
		Primary Focus	ISR
		Secondary Focus	Space
		No Focus	Missiles & Munitions
		Primary Focus	Military Avionics
		No Focus	Commercial Avionics
		No Focus	SatComm
		No Focus	Wireless Milcom
		Primary Focus	Other A&D

Secondary Focus	**Audio, Video, Broadcast**	Secondary Focus	Encoders & Decoders
		Primary Focus	Monitors & Projection
		Primary Focus	Networking & Converters
		Secondary Focus	Audio Systems
		No Focus	Video Processing Cards
		Secondary Focus	Video Conferencing
		No Focus	Routers and Switches
		Primary Focus	Pro Cameras & Camcorders
		No Focus	Other Broadcast

Primary Focus	**Automotive & Transport**	Primary Focus	Driver Assist - Camera
		Secondary Focus	Driver Assist Radar/LIDAR
		Secondary Focus	Driver Info/Infotainment
		Primary Focus	Automated Driving
		Primary Focus	Other Automotive

Secondary Focus	**Consumer**	Secondary Focus	Digital Display
		Secondary Focus	Drones
		Secondary Focus	Multifunction Printers
		No Focus	Other Consumer

No Focus	**Datacenter**	No Focus	Storage
		No Focus	Compute
		No Focus	Networking

Primary Focus	**Industrial, Medical**	No Focus	Motor Control
		No Focus	Industrial Networking
		Primary Focus	Surveillance
		Primary Focus	Machine Vision
		Primary Focus	Medical Imaging
		Primary Focus	Medical / Clinical
		Secondary Focus	Other ISM

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Xylon d.o.o. Segment and Application Focus (continued)

Secondary Focus	**Test & Measurement**	Secondary Focus	Simulation / Emulation
		Primary Focus	T & M Instrumentation
		Secondary Focus	Semiconductor Test
		No Focus	Wired Communication Test
		No Focus	Wireless Communications Test
		No Focus	Other Test & Measurement

No Focus	**Wired Comms & Networks**	No Focus	Access Equipment
		No Focus	Long Haul/Transport
		No Focus	Wired Mobile Backhaul
		No Focus	OTN
		No Focus	Enterprise Equipment
		No Focus	Ethernet Interlaken Connectivity
		No Focus	Packet Processing
		No Focus	Other Wired Communications

No Focus	**Wireless Comms & Networks**	No Focus	Baseband
		No Focus	Wireless Backhaul
		No Focus	Connectivity & Switching
		No Focus	Radio
		No Focus	Other Wireless

Design Competencies

System Architecture	Primary Focus	UFDM certified, HW/SW partitioning, soft-CPUs and SoC/MPSoC CPUs, optimizations on size and timings, system modeling
RF and Analog Design	Secondary Focus	Power supplies, eventual ADCs, PHYs (i.e. MIPI), power supply monitors, video front-ends with operational amplifiers
FPGA Design and Integration	Primary Focus	Xylon works exclusively with Xilinx devices since 1995. We have used almost all FPGA and SoC families, starting from the XC3000.
FPGA Timing Closure	Primary Focus	Familiar with all Xilinx design guideliness for timing constraining, static timing analysis, and all techiques to achieve and timing goals.
FPGA Design Optimization	Primary Focus	Familiar with all techiques and coding styles (including partial reconfiguration) that assure the best optimization and timing closure results in different families.
Signal Integrity Design and Analysis	Secondary Focus	With Xilinx developed a proprietary Gbps multimedia serial link (VLINK) that works through an ordinary differential pair. Experienced with FPD-Link II, FPD-Link III, APIX, APIX2, Ethernet, MIPI, HiSpi, SATA, MOST....
Inhouse or Subcontracted:	Inhouse	
Typical Serial Speed:	3	
PCB Design & Layout	Primary Focus	Able to produce complex PCBs: BGA IC footprints, Gbps lines, up to 20 layers...Connections to different Mfg. Do PCB designs for customers within proof-of-concept and demo projects, as well as small volume Xylon HW products.
Inhouse or Subcontracted:	Inhouse	
Industrial and Mechanical Design	Secondary Focus	Xylon can design small mechanical products in order to complete in-house or customer developed electronics.
Inhouse or Subcontracted:	Both	
Industry Certification and Compliance Testing	Secondary Focus	Preparation of Xylon products for CE and FCC conformance testings.
Inhouse or Subcontracted:	Subcontracted	
Quality and Reliability Testing	Secondary Focus	Experienced in using Xilinx Soft Error Mitigation (SEM) IP core and some other Xilinx functional safety tools.
Inhouse or Subcontracted:	Inhouse	
Contract Manufacturing	Secondary Focus	Xylon has developed a number of different Xilinx-based development boards, FMC cards, camera modules and also can manufacture different add-on cards (mostly FMC) to support customer prototyping needs. Supported Xilinx TDPs,
Inhouse or Subcontracted:	Subcontracted	
Max Quantities Supported:	1000s	

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Digital Signal Processing	Primary Focus	Specialists for video and computer vision, DSP, algorithm development and optimizations for use in Xilinx devices.
Experience:	MicroBlaze, Zynq SoC and Zynq UltraScale MPSoC	
Embedded Processors	Primary Focus	Experiened with Xilinx soft-CPUs, PowerPC and ARM processing systems, as well as with many other embedded SoCs (Intel, MIPS, ARM...)
Experience:	PicoBlaze, MicroBlaze, Zynq, PowerPC, MIPS	
Embedded Porting / BSP Extension	Secondary Focus	Xylon provides software drivers for logicBRICKS IP cores and can develop software applications for different operating systems.
OS and RTOS Design Experience:	Android, Linux, QNX, Windows Embedded	
Embedded Application Development	Primary Focus	Xylon is capable to develop embedded applications for WEC OS, Linux OS, no-OS or RTOS.
Image Sensor Fusion	Primary Focus	Complete automotive surround view system solution. Customers are major OEMs and Tier1s. Heterogeneous video/thermal sensor fusion demo. Heterogeneous video/IMU units (accelerometer, gyroscope and magnetometer).
Homo or Heterogeneous:	Both	
Sensor Types Design Experience:	Accelerometer, Compass, GPS / Position, Gyro, Inclinometer, Other / Contact Partner, Thermal, Tilt, Video (SD, HD)	
Computer Vision	Primary Focus	Xylon provides solutions for face detection and tracking, emotions, gender and age estimators, driver monitoring systems, objects detections and recognition...
Machine Learning	Primary Focus	Xylon is an active memeber of the reVISION ecosystem, and works with the complete stack.
Framework Experience:	Caffe	
Network Model Experience:	AlexNet, FCN, GoogLeNet, ResNet, SSD, SqueezeNet, VGG	