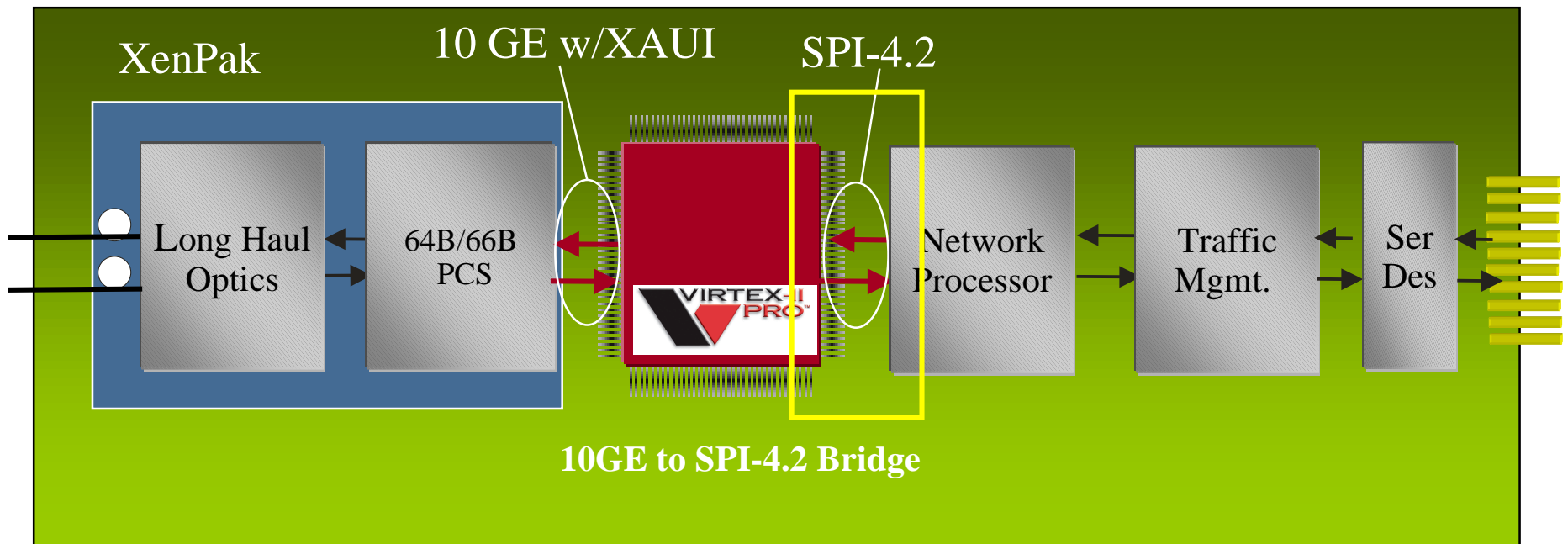


SPI-4.2 Application Example 1

10 Gigabit Line Card Design Example



SPI-4.2 Application Example 2

Xilinx SPI-4.2 will typically be used on network line card performing Link Layer function interfacing to an ASSP performing the PHY function

