CORE Generator™ Components

The Xilinx CORE Generator System delivers a library of both parameterizable and point solution LogiCORE™ IP cores with detailed data sheet specifications. LogiCORE IP are designed and supported by Xilinx.

Seamless integration

The CORE Generator can be accessed from within the ISE design environment or as a stand-alone tool. Easy access to optimized cores helps you get the results you need faster and easier than ever before.

Generating a Core

Use the keyword search function to find the core you want, then enter your core parameters and then click on the Generate button. The output is an optimized CORE for the targeted FPGA device family that includes the following files.

- A tailored Xilinx implementation netlist
- VHDL or Verilog instantiation code
- VHDL or Verilog wrapper for simulation support
- A symbol for schematic capture tools

Up-front FPGA resource estimates help you determine the best FPGA for your project.

Documentation

Easy access to datasheet specifications is provided for each IP core. Most system level cores also come with helpful User Guides and sample designs to get you up and running quickly.

New Cores and Updates

New cores and updates not already bundled with ISE can be installed automatically along with the requisite Service Packs using the ISE WebUpdate tool, or downloaded from the Xilinx Download Center.

Xilinx Core Generator

Achieve high performance results while cutting your design time