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High-Performance DSP — Executing to Plan

Welcome to the second edition of Xilinx® *DSP Magazine*. In the last issue we outlined five pillars that underline our vision in DSP: market focus, design methodology, tailored solutions, ecosystem, and awareness. Since publishing that issue, we have delivered many exciting tailored solutions, such as starter and co-processing kits for video and imaging and JTRS development platforms for software-defined radio.

With the acquisition of AccelChip and its MATLAB-to-RTL synthesis tools, we have also increased our investment in providing you with the most capable and easiest to use design methodology solutions. We are also continuing to work with our core DSP partners like Texas Instruments and The MathWorks to deliver complementary solutions, as unveiled in our recent Serial RapidIO interoperability announcement at TI's Developer Conference in February.

For this second edition of *DSP Magazine* we welcome DSP industry icon Will Strauss of Forward Concepts with snippets from his latest DSP industry research report. His insights regarding shifts in the DSP industry are provided in his article, "FPGAs for DSP: A Fast-Growing Market." In addition, our partners Avnet, Lyrtech, The MathWorks, and Nuvation highlight their latest innovations for our XtremeDSP™ platforms. Our own experts provide tutorials on implementing floating-point DSP, optimizing filter design, and achieving high-bandwidth simulations, among others.

I'm sure you'll find our second edition of *DSP Magazine* informative and inspiring as we endeavor to help you unlock the full capabilities of Xilinx reconfigurable signal processing. Enjoy the read!



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