

On-Demand Webcasts

Xilinx® on-demand webcasts are 60-minute recorded broadcasts featuring interactive technical presentations, product demonstrations, and question-and-answer sessions presented by our silicon and software experts on Xilinx technology, the industry, or both. All on-demand webcasts are available for viewing within 24 hours of the original live session.

The entire webcast library is on the Web at www.xilinx.com/events/webcasts.htm.

Quick Start Your Embedded Design with the MicroBlaze 32-bit Soft Processor

With Derek Palmer of Xilinx

With ever-growing requirements of embedded designs, system architects today are facing a common dilemma – how to push the limits of performance and functionality while still being highly economical, both with respect to system cost and power consumption. They often have to settle with silicon solutions that are neither cost-efficient nor sufficiently optimized for the targeted application. Furthermore, the lack of comprehensive development tools often leads to increased design cycles. Xilinx MicroBlaze™-based designs provide a compelling solution to this dilemma.

Topics in this webcast include:

- Accelerating time to market. Build a system in minutes with the easy-to-use Embedded Development Kit (EDK) v8.2i. The webcast highlights the advanced capabilities and user-friendliness of EDK.
- High-performance embedded system. Powering the system is the MicroBlaze soft processor, a 32-bit RISC engine designed for a variety of computationally intensive embedded applications.



It is supported in both Xilinx Spartan™ and Virtex™ FPGAs.

- Highly customized design. MicroBlaze technology provides ultimate flexibility, giving you control of a number of features such as cache sizes, interfaces, and execution units. Additionally, you can further customize the design for unique customer requirements using an array of ready-to-use peripherals and configuration options.

This webcast is on the Web at http://seminar2.techonline.com/sl/xilinx_sep1906.

New Debug Techniques for FPGA and Embedded Processing-Based Design

With Larry McKeogh of Xilinx and Brad Frieden of Agilent Technologies

Visibility of microprocessor execution in the context of the surrounding system is a critical element of an effective debugging strategy for systems implemented with embedded processors. New advances in FPGA-based embedded processor measure-

ment cores and associated measurement tools from Xilinx and Agilent have eliminated the traditional limitations incurred with buried cache and pipelining, offering good visibility behind caches while using a minimum number of FPGA pins.

This joint Xilinx/Agilent webcast explores techniques on how to debug and validate FPGA designs, including those that implement the PowerPC™ hard processor or MicroBlaze soft processor.

In this webcast, you will learn:

- Proactive techniques for creating debuggable hardware/software systems
- How to avoid traditional limitations on system visibility
- How to use sophisticated tools and cores to capture complex system execution information
- How to collect and analyze trace execution to find and fix bugs faster

This webcast is on the Web at <http://www.techonline.com/learning/webinar/100343>.