

MicroBlaze – The Industry's Most Flexible Embedded Processing Solution

Finding a processor to meet performance, feature, and cost targets can be very challenging in today's competitive environment. With the continued advances in Xilinx FPGA technology features, increased performance and higher density devices, scalable processor systems can meet your ever changing processing needs. A flexible processor system that's easy-to-use, area-efficient, optimized for cost-sensitive designs, and able to support you well into the future is delivered by the award-winning Xilinx MicroBlaze™ solution.

The Xilinx MicroBlaze processor, named to EDN's Hot 100 Products of 2005, is a 32-bit RISC "soft" processor core. Because the processor is a soft core, you can choose from any combination of highly customizable features that will bring your products to market faster, extend your product's life cycle, and avoid processor obsolescence.



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The MicroBlaze Advantage

Xilinx unleashes the potential of embedded FPGA designs with the award-winning MicroBlaze soft processor solution. The MicroBlaze core is a 32-bit RISC Harvard architecture soft processor core with 32 general purpose registers, ALU, and a rich instruction set optimized for embedded applications. It supports both on-chip block RAM and/or external memory. With the MicroBlaze soft processor solution, you have complete flexibility to select any combination of peripherals, memory and interface features that you need to give you the best system performance at the lowest cost on a single FPGA.

Hardware Acceleration using Fast Simplex Link

The MicroBlaze Fast Simplex Link (FSL) lets you connect hardware co-processors to accelerate time-critical algorithms. The FSL channels are dedicated point-to-point data streaming interfaces. Each FSL channel provides a low latency interface to the processor pipeline making them ideal for extending the processor's execution unit with custom hardware accelerators.

Floating-Point Unit Support

MicroBlaze introduces an integrated single precision, IEEE-754 compatible Floating Point Unit (FPU) option optimized for embedded applications such as industrial control, automotive, and office automation. The MicroBlaze FPU provides designers with a processor tailored to execute both integer and floating point operations.

Hardware Configurability

The MicroBlaze processor solution provides a high level of configurability to tailor the processor sub-system to the exact needs of the target embedded application. Configurable features such as the barrel shifter, divider, multiplier, instruction and data caches, FPU, FSL interfaces, hardware debug logic, and the hardware exceptions, provide great flexibility but does not add to the cost if they are not used.

Award-Winning Platform Studio Tool Suite

The Embedded Development Kit (EDK) is an all encompassing solution for designing embedded programmable systems. This pre-configured kit includes the award-winning Platform Studio™ Tool Suite, the MicroBlaze soft processor core as well as all the documentation and soft peripheral IP that you require for designing FPGA-based embedded processor systems.

Embedded Development Kit and Platform Studio Tool Suite

For development, Xilinx offers the Embedded Development Kit (EDK), which is the common design environment for both MicroBlaze and PowerPC-based embedded systems. The EDK is a set of microprocessor design tools and common software platforms, such as device drivers and protocol stacks. The EDK includes the Platform Studio tool suite, the MicroBlaze core, and a library of peripheral IP cores.

Using these tools, design engineers can define the processor subsystem hardware and configure the software platform, including generating a Board Support Package (BSP) for a variety of development boards. Platform Studio Software Development Kit (SDK) is based on the Eclipse open-source C development tool kit and includes a full-featured development environment and a feature-rich GUI debugger. The MicroBlaze processor is supported by the GNU compiler and debugger tools. The debugger connects the MicroBlaze via JTAG. For debugging visibility and control over the embedded system, design engineers can add the ChipScope Pro™ verification tools from Xilinx, which are integrated into the hardware/software debug capabilities of the EDK.

Device Family	Max Clock Frequency	Max Dhrystone 2.1 Performance
Virtex-5	210 MHz	240 DMIPS
Virtex-4	160 MHz	184 DMIPS
Spartan-3	100 MHz	92 DMIPS

Note: Virtex benchmark uses MicroBlaze v5.00 core and Spartan benchmark uses MicroBlaze v4.00 core configurations respectively. Processor performance and size will vary with configuration options. Size is about 800 to 2600 LUTs.

MicroBlaze Hardware Options and Configurable Blocks

Hardware Functions

- Hardware Barrel Shifter
- Hardware Divider
- Machine Status Set and Clear Instructions
- Hardware Exception Support
- Processor Version Register
- Floating-Point Unit (FPU)
- Hardware Multiplier
- Hardware Debug Logic

Cache Options

- Configurable size 2kB - 64 kB
- Configurable cacheable range
- 4 or 8 word cache lines

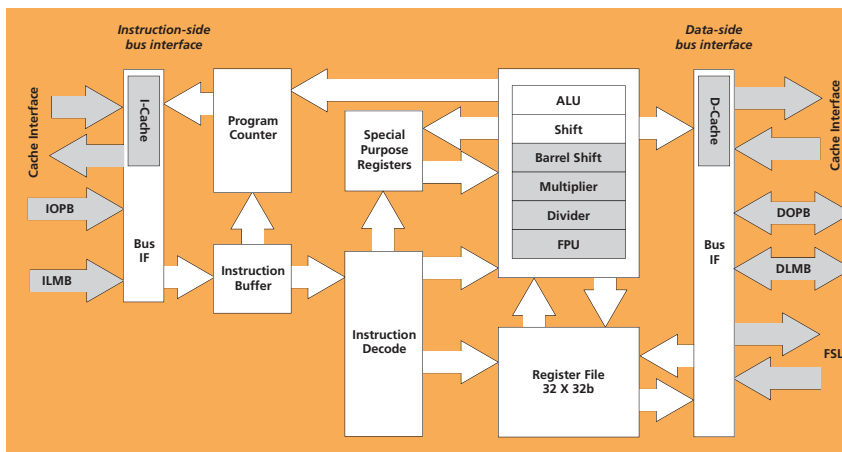
Bus Infrastructure

- On-Chip Peripheral Bus (OPB) for interfacing to peripherals
- Local Memory Bus (LMB) for fast local access memory
- Fast Simplex Link (FSL) for interfacing to co-processors

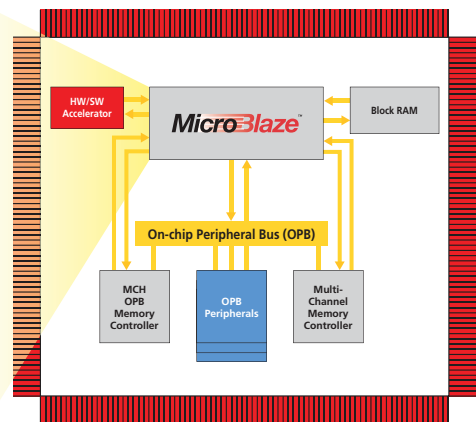
Take the Next Step

Visit our website www.xilinx.com/microblaze for more information .
To order your Embedded Development Kit visit www.xilinx.com/edk.

MicroBlaze Processor Block Diagram



Processor Sub-system Block Diagram



□ Basic Processor Functions ■ Configurable Functions ■ Designer Defined Blocks ■ Peripherals – Xilinx or 3rd Party or Designer Defined

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