

## Artix-7 FPGAs

Optimized for Lowest Cost and Power with Small Form-Factor Packaging for Highest Volume Applications (1.0V, 0.9V)

		Part Number	XC7A100T	XC7A200T	XC7A350T
Logic Resources	Slices		15,850	33,650	56,250
	Logic Cells		101,440	215,360	360,000
	CLB Flip-Flops		126,800	269,200	450,000
Memory Resources	Maximum Distributed RAM (Kbits)		1,188	2,888	4,638
	Block RAM/FIFO w/ ECC (36Kbits each)		135	365	515
	Total Block RAM (Kbits)		4,860	13,140	18,540
Clock Resources	CMTs (1 MMCM + 1 PLL)		6	10	12
I/O Resources	Maximum Single-Ended I/O <sup>(4)</sup>		300	500	600
	Maximum Differential I/O Pairs <sup>(4)</sup>		144	240	288
Embedded Hard IP Resources	DSP48E1 Slices		240	740	1,040
	PCI Express <sup>(1)</sup>		1	1	1
	Agile Mixed Signal (AMS) / XADC		1	1	1
	Configuration AES / HMAC Blocks		1	1	1
	GTP 5.4 / 6.6 Gb/s Transceivers		8	16	16
Speed Grades	Commercial		-1, -2	-1, -2	-1, -2
	Extended		-2L, -3	-2L, -3	-2L, -3
	Industrial		-1, -2	-1, -2	-1, -2
Configuration	Configuration Memory (Mbits)		29.3	62.4	96.1
		Package <sup>(3)</sup>	Dimensions (mm)	Available User I/O: 3.3V SelectIO™ Pins (GTP Transceivers)	
		CSG324	15 x 15	210 (0)	
		FTG256	17 x 17	170 (0)	
		SBG484	19 x 19	285 (4)	
Footprint Compatible	FGG484 <sup>(2)</sup>	23 x 23	285 (4)		
	FBG484 <sup>(2)</sup>	23 x 23		285 (4)	285 (4)
Footprint Compatible	FGG676 <sup>(2)</sup>	27 x 27	300 (8)		
	FBG676 <sup>(2)</sup>	27 x 27		400 (8)	400 (8)
	FFG1156 <sup>(2)</sup>	35 x 35		500 (16)	600 (16)

CSG: 0.8mm Wire-bond chip-scale; FTG: 1.0mm Wire-bond fine-pitch; SBG: 0.8mm Lidless flip-chip; FGG: 1.0mm Wire-bond fine-pitch; FBG: 1.0mm Lidless flip-chip; FFG: 1.0mm Flip-chip fine-pitch

XMP086 (v3.1.1)

- Notes: 1. Supports PCI Express Base 2.1 specification at Gen1 and Gen2 data rates.  
 2. Leaded package options available.  
 3. Device migration is available within the Artix-7 family for like packages but is not supported between other 7 series families.

Important: Verify all data in this document with the device data sheets found at [www.xilinx.com/7](http://www.xilinx.com/7)