



DOMAIN-SPECIFIC METHODOLOGY
FOR TARGETED DESIGN PLATFORMS

› Steep Digital Design Challenges

- Reducing design cycles to meet aggressive deadlines
- Achieving greater performance, lower cost, and reduced power
- Addressing growing design complexity and domain-specific requirements
- Addressing the demands for product differentiation

› The ISE Design Suite

- Achieve greater designer productivity
- Focus on design differentiation
- Shrink time to production
- Attain breakthrough performance, power, and cost benefits

Complex Design Made Logical

Whether you're new to FPGAs or a veteran "power" user, the ISE® Design Suite 11 enables the fastest time to design completion with Xilinx Targeted Design Platforms. This is the first FPGA design suite available in four different configurations aligned to your preferred methodology—logic, embedded, DSP, or system design.

Complete Methodology for Targeted Design Platforms

Xilinx Targeted Design Platforms provide embedded, DSP, and hardware designers alike with access to a wide array of silicon devices supported by open standards, common design flows, IP, and run-time platforms. The ISE Design Suite brings it all together with domain-specific design environments, while enabling design teams to meet their power and performance goals with Xilinx CPLDs and FPGAs, including the new Virtex®-6 and Spartan®-6 families.

Focus on the Design, Not the Tools

Instead of addressing the problem in terms of individual fixed tools, the ISE Design Suite targets the required or preferred methodology for users to better address their specific needs with the appropriate design flow. At this level, the design language may vary from HDL (VHDL/Verilog) to C, C++, or MATLAB® software from The MathWorks. Likewise, design abstraction moves up from the block or component level to the system level. The result is a methodology and complete design flow tailored to each user profile that provides complete design creation, design implementation, and design verification.

Integrating System-Level Design

In addition, the ISE Design Suite provides a tighter connection between embedded and DSP flows to enable integration of designs that contain embedded, DSP, IP, and user blocks in one system. To better serve users familiar with differing design environments, the new ISE Design Suite 11 provides specific accommodations for designers ranging from the pushbutton user to the ASIC designer.

Visit www.xilinx.com/ise to download a free 30-day evaluation, or to learn more about the Xilinx Targeted Design Platforms.

ISE Design Suite: Logic Edition

The ISE Design Suite: Logic Edition delivers a complete solution for logic and connectivity design, providing the front-to-back base methodology and IP. The Logic Edition includes Xilinx-exclusive tools and technologies for design entry, synthesis, implementation, and verification to help achieve optimal design results in the shortest time.

For more information visit www.xilinx.com/ise-logic.

ISE Design Suite: Embedded Edition

The ISE Design Suite: Embedded Edition delivers all of the features, technologies, and IP found in the Logic Edition plus additional tools, including the IP, required for FPGA designs with embedded PowerPC® hard processor and/or MicroBlaze™ soft processor cores. Designers have access to the Xilinx Platform Studio (XPS) tool suite, which delivers a graphical environment and command line support for developing hardware platforms for embedded applications.

For more information visit www.xilinx.com/ise-embedded.

ISE Design Suite: DSP Edition

The ISE Design Suite: DSP Edition addresses the unique needs of the DSP design methodology. In addition to the tools, technologies, and IP in the Logic Edition, the DSP Edition includes System Generator for DSP and specialized IP required for DSP development. With System Generator for DSP, developers with little FPGA design experience can quickly create production-quality FPGA implementations of DSP algorithms in a fraction of traditional RTL development times.

For more information visit www.xilinx.com/ise-dsp.

ISE Design Suite: System Edition

The ISE Design Suite: System Edition includes all of the tools, technologies, and IP from the Logic, DSP, and Embedded Editions. The System Edition delivers a flexible solution for the user who requires a unique methodology that allows powerful DSP functionality in an embedded design or who wants to dramatically extend DSP designs through embedded processor capabilities.

For more information visit www.xilinx.com/ise-system.

FEATURE COMPARISON TABLE

FEATURES	ISE WEBPACK™ (DEVICE LIMITED)	LOGIC EDITION	EMBEDDED EDITION	DSP EDITION	SYSTEM EDITION
ISE Foundation with ISE Simulator (ISim)	●	●	●	●	●
PlanAhead™ Design and Analysis Tool	●	●	●	●	●
ChipScope™ Pro and the ChipScope Pro Serial I/O Toolkit		●	●	●	●
Embedded Development Kit* (EDK)			●		●
Software Development Kit* (SDK)			●		●
Xilinx DSP Bundle* with the System Generator for DSP				●	●

*Available as a standalone product

Corporate Headquarters

Xilinx, Inc.
2100 Logic Drive
San Jose, CA 95124
USA
Tel: 408-559-7778
www.xilinx.com

Europe

Xilinx Europe
One Logic Drive
Citywest Business Campus
Saggart, County Dublin
Ireland
Tel: +353-1-464-0311
www.xilinx.com

Japan

Xilinx K.K.
Art Village Osaki Central Tower 4F
1-2-2 Osaki, Shinagawa-ku
Tokyo 141-0032 Japan
Tel: +81-3-6744-7777
japan.xilinx.com

Asia Pacific Pte. Ltd.

Xilinx, Asia Pacific
5 Changi Business Park
Singapore 486040
Tel: +65-6407-3000
www.xilinx.com



© Copyright 2009 Xilinx, Inc. XILINX, the Xilinx logo, Virtex, Spartan, ISE and other designated brands included herein are trademarks of Xilinx in the United States and other countries. All other trademarks are the property of their respective owners.

Printed in the U.S.A. PN 2212