

		Virtex-4 XQ FPGAs
	Part Number	XQ4V5X55
	Core Voltage	1.2V
Logic Resources	Slices ⁽¹⁾	24,576
	Logic Cells	55,296
	CLB Flip-Flops	49,152
Memory Resources	Maximum Distributed RAM (Kbits)	384
	Block RAM/FIFO w/ECC (18kbits each)	320
	Total Block RAM (Kbits)	5,760
Clock Resources	Digital Clock Manager (DCM)	8
I/O Resources	Maximum Single-Ended I/Os	640
	Maximum Differential I/O Pairs	320
	Digitally Controlled Impedance	Yes
Embedded Hard IP Resources	DSP Slices	512
	18 x 18 Multipliers	—
	RocketIO™ Transceivers	—
	PowerPC® Processor Blocks	—
Miscellaneous	Speed Grades	-10
	Configuration Memory (Mbits)	22.7
	Manufacturing Grades	M
	Package	FF1148

Notes: 1. Each slice comprises two 4-input logic function generators (LUTs), two storage elements, wide-function multiplexers, and carry logic.