

		Virtex-4 XQ FPGAs				
		Part Number	XQ4VLX25	XQ4VLX60	XQ4VLX100	XQ4VLX160
		Core Voltage	1.2V	1.2V	1.2V	1.2V
Logic Resources	Slices ⁽¹⁾	10,752	26,624	49,152	67,584	
	Logic Cells	24,192	59,904	110,592	152,064	
	CLB Flip-Flops	21,504	53,248	98,304	135,168	
Memory Resources	Maximum Distributed RAM (Kbits)	168	416	768	1,056	
	Block RAM/FIFO w/ECC (18kbits each)	72	160	240	288	
	Total Block RAM (Kbits)	1,296	2,880	4,320	5,184	
Clock Resources	Digital Clock Manager (DCM)	8	8	12	12	
I/O Resources	Maximum Single-Ended I/Os	448	640	960	960	
	Maximum Differential I/O Pairs	224	320	480	480	
	Digitally Controlled Impedance	Yes	Yes	Yes	Yes	
Embedded Hard IP Resources	DSP Slices	48	64	96	96	
	18 x 18 Multipliers	—	—	—	—	
	RocketIO™ Transceivers	—	—	—	—	
	PowerPC® Processor Blocks	—	—	—	—	
Miscellaneous	Speed Grades	-10	-10	-10	-10	
	Configuration Memory (Mbits)	4.8	17.7	30.7	40.3	
	Manufacturing Grades	M	M	I	I	
	Package	SF363, FF668	FF668, FF1148	FF1148	FF1148	

Notes: 1. Each slice comprises two 4-input logic function generators (LUTs), two storage elements, wide-function multiplexers, and carry logic.