Xilinx
Productivity
Advantage

Xilinx XPA Program

Incorporating Xilinx Design Productivity Solutions

Xilinx offers several Design Productivity Solution options to help our customers migrate their designs to our FPGA Hardware Platforms efficiently. Using the Xilinx Productivity Advantage (XPA) program, customers can obtain customized bundles that include Design Tools, Training, IP, Design Services and more~ all tailored to meet specific customer site and corporate needs. Your Xilinx Sales Representative and Applications Engineering team will assist in determining which of these solutions should be included in an XPA to increase your productivity and shorten your time to market. This can include a detailed review of your design environment and recommendations to better integrate Xilinx solutions into your design flow for maximum productivity. To learn more visit **www.xilinx.com/support/gsd/xpa_program.htm**

ISE Design Suite (Xilinx Design Tools)

The ISE Design Suite is a critical component of Xilinx overall product offering. The ISE Design Suite offer's a comprehensive design environment providing the tools necessary to efficiently meet all the design requirements for targeting Xilinx FPGA Platforms.

ISE Design Suite 11 includes several significant enhancements in areas such as run-time, power optimization and quality of results. Additionally, ISE 11 includes domain specific tools bundled together for specific "Targeted Design Platforms". These bundles include Xilinx tools and reference IP targeted to meet the needs of Logic Design, DSP, and Embedded applications. These are aligned with the Xilinx Targeted Design Platforms, Domain's, and Market Specific product offerings.

Providing these domain specific tool environments helps streamline the process of getting a design started and eliminates hurdles later in the project, where the user realizes they are lacking a piece of the design flow for simulation, on-chip debug, etc. Rather than selecting a basic set of design tools such as ISE Foundation and supplementing it with an array of development tool options, users can now identify the specific domain methodology and enhance with IP and services best suited to their design needs specific to a Targeted Design Platform. ISE Design Suite is now offered either with "floating" or "node locked" licenses to optimize design efficiency. To learn more visit **www.xilinx.com/products/design_resources/design_tool/index.htm**

Intellectual Property (IP)

Xilinx offers a wide variety of ready to use IP optimized for use in our latest platform offerings. The majority of our IP are included in our ISE Design Suite and are parametrizable to enable you to meet your specific design goals. Examples of these include I/O blocks, processor blocks, peripherals, DSP and math functions. We also offer "specialty" IP cores specific for vertical markets including wireless, video, automotive, and consumer applications. PCI, SRIO, and CPRI are a few specific examples. Visit our web site or contact your local Xilinx sales representative, for the comprehensive list. To learn more visit **www.xilinx.com/ipcenter/index.htm**

XILINX®

Xilinx Development Boards and Starter Kits

There are several development boards and starter kits available from Xilinx and our partners which can help you "jump start" your projects. Visit our web site for a complete listing. To learn more visit **www.xilinx.com/products/devboards/index.htm**

Xilinx Engineering Services

Xilinx offers a range of Engineering Services, including Titanium, a short-term, highly focused, task oriented on-site service, Xilinx Design Services (XDS), which does full turn-key, full-custom, high-complexity FPGA designs, and QuickStart!, a combination of customized training and consulting services. To learn more visit **www.xilinx.com/gsd/index.htm**

Titanium Dedicated Engineering

Titanium Dedicated Engineering seeks to improve design productivity and accelerate time to market by deploying dedicated engineers to address critical segments of your overall design and development cycle. Titanium engineers can carry out architecture reviews, help in IP evaluation, design integration, timing closure, embedded system development, high speed FPGA and board design including SI analysis, design optimization, area/power optimization, etc. Titanium engineering is deployed on demand, and is typically a short term engagement (1-2 weeks). Utilizing Titanium, customers can focus on potential problem areas during the design cycle and take steps to mitigate critical issues, helping ensure on time delivery to production. Titanium is billed hourly with expenses. To learn more visit **www.xilinx.com/support/services/titanium/index.htm**

Xilinx Design Services (XDS)

With an ISO 9001 certified process and emphasis on program management the XDS team of engineers deliver full-turnkey FPGA design starting from a high level requirement document. The team will work with the customer engineering team to create specifications, design documents and test plans and then code, verify and target a design to a Xilinx FPGA. The team has had a very high percentage of 'first-time-right' designs, and can provide services to design a part of an FPGA, multiple FPGAs on a board, or even a complete system with board, FPGA, BSP, device drivers, etc. XDS services are typically a Fixed Price, with some segments requiring more flexible resource model being Time and Materials. To learn more visit **www.xilinx.com/xds/xds_portfolio/index.htm**

QuickStart!

QuickStart! offers customers a week of focused training and consulting to help your team kick start a Xilinx project. QuickStart! is a fixed price offering. To learn more visit **www.xilinx.com/quickstart/index.htm**

Xilinx Educational Services

Xilinx provides targeted, high-quality education services designed by experts in programmable logic design, and delivered by Xilinx-qualified trainers. We offer instructor-led classes through our authorized training provider (ATP) network. We also offer on-line and e-learning modules for distance education on our tools and products. Examples of our robust class offering include: Introduction to FPGAs, Designing for Performance, Designing with Multi-Gigabit Serial Transceivers, DSP Design using System Generator, Advanced VHDL, and Designing FPGAs for ASIC designers. To learn more visit **www.xilinx.com/support/education-home.htm**

Obtaining an XPA

Contact your local Xilinx Sales Representative to get more details about the XPA program and how it can be customized to meet your specific needs. This will include an assessment of your tool design flow and training needs to ensure maximum efficiency in migrating your designs into Xilinx hardware platforms.

Your Sales Representative can be located at: **www.xilinx.com/company/sales/ww_reps.htm**

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