ChipScope™ Pro

Lower Verification Times by up to 50%

Debug often consumes the most time and cost in the design cycle

- HDL verification is inadequate for high-density designs
- Other ASIC/FPGA solutions require large numbers of dedicated pins and deliver limited signal access
- Correlating logic and embedded processor signals is complicated
- Hardware solutions are expensive

ChipScope Pro delivers an efficient and unique verification solution

- Capture and view any internal FPGA signal including those associated with embedded processors
- Enabled through low-profile, configurable software cores
- No additional FPGA pins required
- Debug while the FPGA is on the board, interacting with the rest of the system

ChipScope Pro delivers real-time logic verification that outperforms ASIC or competing FPGA offerings. First, you insert low-profile logic, bus, virtual I/O, or Agilent Trace cores into your design or netlist. After device programming, these cores monitor and capture internal FPGA signals in real time, and send that data back to the ChipScope Pro software logic analyzer for analysis and debug.

The ChipScope Pro Serial IO Toolkit delivers a fast and easy way to evaluate and measure high-speed FPGA serial IO channel operation at a fraction of the cost of comparable hardware systems. An add on option to ChipScope Pro, it uses the IBERT core to enable you to test in real-time to a variety of high-speed serial standards including PCI Express, Serial RapidIO, Gigabit Ethernet, XAUI, Fibre Channel and more. Then save your serial IO channel settings to your design files for implementation.

ChipScope Pro and the ChipScope Pro Serial IO Toolkit deliver:

- Lower Project Costs
- Flexible and Efficient Debug
- Unparalleled Ease-of-Use
- Complete Integration

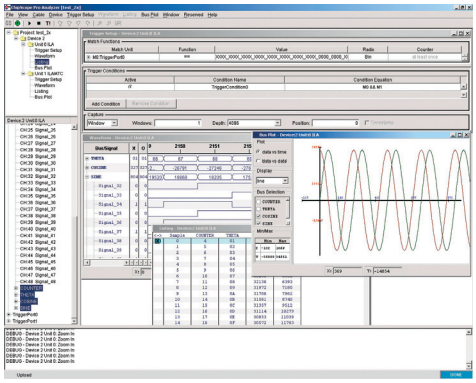
XILINX®

High-Performance Debug for Logic and Embedded Design

Verify and debug at up to 475 MHz operation, in-system, under real conditions. Capture any internal FPGA logic signal, including embedded processor busses on both soft-processor MicroBlaze™ designs, or hard processors like the IBM PowerPC™ 405.

Optional Direct Link to Your Agilent Test Equipment

ChipScope Pro optionally works directly with Agilent Techologies' FPGA Dynamic Probe to bring the advantages of real-time debug to your Agilent logic analyzer. Cross-correlate signal debugging inside the FPGA and on the board using your FPGA programming cable and Agilent test equipment.



ChipScope Pro Logic Analyzer

Full Operation with the Latest FPGA Technology

ChipScope Pro tools work with all Xilinx FPGAs, including the Virtex™-5 65nm FPGAs and Spartan™-3 low-cost FPGAs. ChipScope Pro and the ChipScope Pro Serial IO Toolkit work with any configuration of ISE™ including our free, downloadable ISE WebPACK and 60-day Evaluation versions.

Remote Debug and Verification

Client/Server support in ChipScope Pro tools enables users to debug Xilinx FPGAs remotely. Engineering teams can share and manage FPGA access across a network, reducing design time through more efficient resource utilization. Systems can be deployed and then accessed remotely for in-system verification whenever field changes become necessary.

Take the Next Step

Visit **www.xilinx.com/chipscopepro** to get more detailed information on ChipScope Pro or the ChipScope Pro Serial IO Toolkit, and to download your free Windows or Linux 60-day evaluation version. The tools are available for purchase from your local sales office and the Xilinx online store.

Corporate Headquarters

Xilinx, Inc.
2100 Logic Drive
San Jose, CA 95124
USA
Tel: 408-559-7778
Web: www.xilinx.com

Europe

Xilinx Europe
One Logic Drive
Citywest Business Campus
Saggart, County Dublin
Ireland
Tel: +353-1-464-0311
Web: www.xilinx.com

Japan

Xilinx K.K.
Art Village Osaki Central Tower 4F
1-2-2 Osaki, Shinagawa-ku
Tokyo 141-0032 Japan
Tel: +81-3-6744-7777
Web: japan.xilinx.com

Asia Pacific Pte. Ltd.

Xilinx, Asia Pacific
5 Changi Business Park
Singapore 486040
Tel: +65-6407-3000
Web: www.xilinx.com

XILINX®

www.xilinx.com

Printed in U.S.A. PN 2068