**Quote Sheet**

# Xilinx Introduces Zynq-7000 Family, Industry's First Extensible Processing Platform

**SAN JOSE, Calif., Mar. 1, 2011 –** A familiar programming and development environment supported by an industry-leading ecosystem of providers is a major pillar of the overall product solution that makes up the Zynq-7000 family. In addition to early access customers, Xilinx is working closely with members of the ARM Connected Community® and the Xilinx Alliance Program to provide well-adopted tool chains, compilers, debuggers, IP and other support for the Zynq-7000 family.

ARM

"A dual core Cortex™-A9 processor, implemented with ARM Artisan Physical IP and tightly integrated with 28nm FPGA fabric opens up a whole new spectrum of possibilities for embedded designers who need significant levels of performance in a single low-cost, low-power package," said Mike Muller, CTO, ARM. "The Zynq-7000 family enables the development of fully extensible systems, providing the end customer with a balance between fixed and flexible resources."

iVeia

"The Dual Cortex A9 processor system with NEON and floating point engine offers iVeia the high performance we need with the integration to reduce cost and power for our ultra compact form factors," said Michael Fawcett, CTO of iVeia. "The tight coupling between the processor system and programmable logic allows us to get the best performance for hardware acceleration."

Lauterbach

"Lauterbach's support of the Zynq-7000 family with the TRACE32 hardware assisted debugging and trace tool allows customers to preserve their design/debug environment while, at the same time, explore the capabilities of the Extensible Processing Platform," said Norbert Weiss, Sales and Marketing Director at Lauterbach Development Tools. "Our work with Xilinx builds on

years of collaboration between the two companies on FPGA-based embedded systems development solutions."

PetaLogix

"PetaLogix has a long relationship with Xilinx providing a complete Linux and Development Tools solution for the Xilinx MicroBlaze™ soft processor," said Dr. John Williams, PetaLogix founder and CEO. "Now with support for the ARM Cortex-A9 processor sub-system, PetaLogix will be able to offer the embedded designer support across both hard and soft microprocessor resources, in a unified and seamless design flow, with the out-of-the-box Linux platform support they've come to expect."

MathWorks

"Thousands of engineers are using Model-Based Design with Simulink® and MathWorks code generation technology to implement algorithms for both processors and FPGAs," said Ken Karnofsky, Senior Strategist at MathWorks. "MathWorks is committed to working with Xilinx to bring a Model-Based Design flow to the Zynq-7000 family that supports automatic code generation for both the ARM Cortex-A9 processor and the programmable logic."

Mentor Graphics

"Partnering with Xilinx to provide Sourcery G++ for the Zynq-7000 family's ARM Cortex-A9 cores extends our solutions to a broader class of embedded customer," said Mark Mitchell, Director of Embedded Tools at Mentor Graphics. "Our Eclipse-based integrated development environment, including CodeSourcery's professional-quality GNU tool chain, will give developers an easy way to build applications for Zynq-7000."

Micrium

"Micrium is looking forward to extending its support for Xilinx's MicroBlaze soft processor extending to the ARM Cortex A9-based processing subsystem within the Zynq-7000 family," said Jean Labrosse, Micrium's president and CEO. "Micrium offers comprehensive support for Xilinx's broad portfolio of embedded processing solutions, and takes pride in addressing the performance, integration, and reliability requirements inherent in embedded applications."

MontaVista

"MontaVista's MVL 6 development platform for Xilinx embedded solutions will now extend to the Zynq-7000 family," said Patrick J. MacCartee, Director of Product Management at MontaVista. "Our Extensible Processing Platform support opens up fantastic opportunities for our Meld development community to work with a processing platform that delivers new levels of performance and integration."

Wind River

"By integrating a high performance Cortex A9 MPCore processor with the flexibility of an FPGA fabric, Xilinx establishes a highly flexible foundation for tightly embedded applications," said Imran Yusuf, director of global alliances at Wind River. "When combined with Wind River's extensive and market-leading multi-core software portfolio, Wind River and Xilinx provide a flexible and configurable ARM based hardware and software platform on which our customers can innovate to address diverse needs across the embedded spectrum."

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