

ISE™ 8.1i – Powered by ISE Fmax Technology

Feature	ISE WebPACK™	ISE Foundation™
Platforms	Microsoft Windows 2000 / XP Red Hat Enterprise Linux 3 (32 bit)	Microsoft Windows 2000 / XP Sun Solaris 2.8 or 2.9 Red Hat Enterprise Linux 3 (32 & 64 bit)
Devices	Virtex™ Series Virtex: XC3V50 - XC3V600 Virtex-E: XC3V50E - XC3V600E Virtex-II: XC2V40 - XC2V500 Virtex-II Pro: XC2VP2 - XC2VP7 Virtex-4: LX: XC4VLX15, XC4VLX25 SX: XC4VSX25 FX: XC4VPX12 Virtex Q: XQV100 - XQV600 Virtex QR: XQVR300, XQVR600 Virtex E Q: XQV600E	ALL
Design Entry	Spartan™ Series Spartan-II/III: All Spartan-3: XC3S50 - XC3S1500 Spartan-3E: All Spartan-3L: XC3S1000L, XC3S1500L XA (Xilinx Automotive) Spartan-3: All CoolRunner™ XPLA3 CoolRunner-III™ CoolRunner-IIA XC9500™ Series Schematic Editor HDL Editor State Diagram Editor Xilinx CORE Generator System™ RTL & Technology Viewers PACE (Pinout & Area Constraint Editor) Architecture Wizards 3rd Party RTL Checker Support Xilinx System Generator for DSP	Spartan-II/III: All Spartan-3: All Spartan-3E: All Spartan-3L: All XA (Xilinx Automotive) Spartan-3: All All All Yes Yes Microsoft Windows only Yes Yes Yes Yes Sold as an Option Sold as an Option Yes Integrated Interface (EDIF Interface on Linux) Integrated Interface EDIF Interface EDIF Interface Integrated Interface EDIF Interface CPID (Microsoft Windows only)
Embedded System Design	Embedded Design Kit (EDK)	Sold as an Option
Synthesis	XST - Xilinx Synthesis Technology Mentor Graphics LeonardoSpectrum Mentor Graphics Precision RTL Mentor Graphics Precision Physical Synopsys DC-FPGA Compiler Synplify/Pro/Premier Synplify Amplify Physical Synthesis ABEL	Yes Integrated Interface (EDIF Interface on Linux) Integrated Interface EDIF Interface EDIF Interface Integrated Interface EDIF Interface CPID (Microsoft Windows only)

Implementation	Feature	ISE WebPACK™	ISE Foundation™
Programming	FloorPlanner		Yes
	PlanAhead™		Sold as an Option
	Timing Driven Place & Route		Yes
	Incremental Design		Yes
	Timing Improvement Wizard		Yes
	Xplorer		Yes
	IMPACT /System ACE™/ CableServer		Yes
	IBIS / STAMP / HSPICE* Models		Yes
	ELDO Models* (MGT only)		Yes
	ChipScope PRO™		Sold as an Option
Board Level Integration	Graphical Testbench Editor		Microsoft Windows only
	ISE Simulator Lite		Yes
	ISE Simulator		ISE Simulator is available as an Optional Design Tool for ISE Foundation only
	ModelSim® XE III Starter		Yes
	ModelSim XE III		Sold as an option
	Static Timing Analyzer		Yes
	FPGA Editor with Probe		Yes
	ChipViewer		Yes
	XPower (Power Analysis)		Yes
	3rd Party Equivalency Checking Support		Yes
Verification	SMARTModels for PowerPC™ and RocketIO™		Yes
	3rd Party Simulator Support		Yes

*HSPICE and ELDO Models are available at the Xilinx Design Tools Center at www.xilinx.com/ise

For more information, visit www.xilinx.com/ise

