

Intellectual Property Offerings

The Xilinx IP Center on the Web allows you to search for IP by function, type, vendor, or keywords.

The IP Center includes intellectual property from Xilinx and its third-party vendors. To access the IP solutions highlighted here, visit www.xilinx.com/ipcenter and type the keywords listed in the search box.

Source-Synchronous Interfaces

SPI-4 Phase 2 Interface Solutions (DO-DI-POSL4MC) Xilinx IP Core

The Xilinx® SPI-4 Phase 2 core provides a fully compliant packet-over-SONET/SDH (POS) solution, which can be quickly integrated into networking systems.

Through user-configurable options, the Xilinx SPI-4.2 core provides ultimate flexibility while seamlessly interoperating with industry-leading ASSPs to maximize the data transfer bandwidth. The Xilinx SPI-4.2 core is fully compliant with the OIF's System Packet Interface Level 4 (SPI-4) Phase 2 standard, as well as the Saturn Development Group's POS-PHY Level 4 (PL4) interface specification.

Type search keywords: **SPI-4 Phase 2**

Serial Connectivity

Virtex-5 RocketIO GTP Wizard

The Virtex™-5 RocketIO™ GTP Wizard automates the task of creating HDL wrappers to configure Virtex-5 RocketIO GTP transceivers. The wizard's customization GUI allows you to configure one or more GTP transceivers using pre-defined templates to support popular industry standards, or from scratch to support a wide variety of custom protocols.

Type search keywords: **GTP Wizard**



Virtex-5 Embedded Tri-Mode Ethernet MAC Wrapper

The CORE Generator™ tool supports the Virtex-5 Tri-Mode Ethernet Media Access Controller (MAC) Wrapper to automate the generation of HDL wrapper files for the tri-mode Ethernet MAC in Virtex-5 LXT devices. Preconfigured HDL wrappers, testbenches, and implement and simulation scripts are generated automatically based on user-defined options.

Type search keywords: **Virtex-5
Ethernet MAC**

Virtex-5 PCI Express Endpoint Block Wrapper

The Xilinx PCI Express Endpoint block wrapper integrates and interfaces to the on-chip PCI Express Endpoint block, supporting 1-lane, 2-lane, 4-lane, and 8-lane complete endpoint core implementations. In addition, a PCI Express Endpoint block development kit is also available. This solution is used in com-

munication, multimedia, server, storage, and mobile platforms and enables applications such as high-end medical imaging, graphics-intensive video games, DVD quality streaming video on the desktop, and 10 Gigabit Ethernet interface cards.

Type search keywords: **PCI Express Block**

Virtex-5 LXT PCI Express Block Plus LogiCORE Xilinx IP Core

The Xilinx PCI Express Plus LogiCORE IP integrates and interfaces to the PCI Express Endpoint block, supporting 1-lane, 4-lane, and 8-lane complete endpoint core implementations. In addition, a PCI Express development kit is also available. This solution is used in communication, multimedia, server, storage, and mobile platforms and enables applications such as high-end medical imaging, graphics-intensive video games, DVD quality streaming video on the desktop, and 10 Gigabit Ethernet interface cards.

Type search keywords: **PCI Express Plus**