

High-Volume Design Application Notes

These FPGA and CPLD application notes can help you design for high-volume markets.

Spartan-3 Generation FPGAs	
Audio, Video, and Image Processing	
XAPP485 – 1:7 Deserialization in Spartan-3E FPGAs at Speeds Up to 666 Mbps	www.xilinx.com/bvdocs/appnotes/xapp485.pdf
XAPP486 – 7:1 Serialization in Spartan-3E FPGAs at Speeds Up to 666 Mbps	www.xilinx.com/bvdocs/appnotes/xapp486.pdf
XAPP491 – Inverting LVDS Signals for Efficient PCB Layout in Spartan-3 Generation FPGAs	www.xilinx.com/bvdocs/appnotes/xapp491.pdf
XAPP930 – Color-Space Converter: RGB to YCrCb	www.xilinx.com/bvdocs/appnotes/xapp930.pdf
Digital Signal Processing	
XAPP634 – Analog Devices TigerSHARC Link	www.xilinx.com/bvdocs/appnotes/xapp634.pdf
XAPP753 – Interfacing Xilinx FPGAs to TI DSP Platforms Using the EMIF	www.xilinx.com/bvdocs/appnotes/xapp753.pdf
Communication and Networking	
XAPP551 – Viterbi Decoder Block Decoding – Trellis Termination and Tail Biting	www.xilinx.com/bvdocs/appnotes/xapp551.pdf
XAPP569 – Digital Up and Down Converters for the CDMA2000 and UMTS Base Stations	www.xilinx.com/bvdocs/appnotes/xapp569.pdf
XAPP942 – Reference System: OPB Ethernet MAC	www.xilinx.com/bvdocs/appnotes/xapp942.pdf
Memory Interface and Storage Elements	
XAPP229 – Wider Block Memories	www.xilinx.com/bvdocs/appnotes/xapp229.pdf
XAPP291 – Self-Addressing FIFO	www.xilinx.com/bvdocs/appnotes/xapp291.pdf
XAPP482 – MicroBlaze Platform Flash/PROM Boot Loader and User Data Storage	www.xilinx.com/bvdocs/appnotes/xapp482.pdf
CoolRunner-II CPLDs	
Handheld Consumer Electronics	
XAPP394 – Interfacing to Mobile SDRAM with CoolRunner-II CPLDs	www.xilinx.com/bvdocs/appnotes/xapp394.pdf
XAPP905 – Using CoolRunner-II with OMAP, Xscale, i.MX & Other Chipsets	www.xilinx.com/bvdocs/appnotes/xapp905.pdf
XAPP914 – Connecting Intel PXA27x Processors to Hard-Disk Drives with a CoolRunner-II CPLD	www.xilinx.com/bvdocs/appnotes/xapp914.pdf
Industrial, Scientific, and Medical	
XAPP384 – Interfacing to DDR SDRAM with CoolRunner-II CPLDs	www.xilinx.com/bvdocs/appnotes/xapp384.pdf
XAPP385 – CoolRunner-II CPLD I ² C Bus Controller Implementation	www.xilinx.com/bvdocs/appnotes/xapp385.pdf
XAPP386 – CoolRunner-II Serial Peripheral Interface Master	www.xilinx.com/bvdocs/appnotes/xapp386.pdf
XAPP387 – PicoBlaze 8-bit Microcontroller for CPLD Devices	www.xilinx.com/bvdocs/appnotes/xapp387.pdf
XAPP940 – Using Xilinx CPLDs as Motor Controllers	www.xilinx.com/bvdocs/appnotes/xapp940.pdf