

ASIC PROTOTYPING

HAPS™ – High-performance ASIC Prototyping System™

a high performance, high capacity FPGA platform for ASIC prototyping and emulation composed of multi-FPGA boards and standard or custom-made daughter boards

HapsTrak™

a set of rules for pinout and mechanical characteristics, which guarantees compatibility with previous and future generation HAPS motherboards and daughter boards



HAPS-50



HAPS-30



HAPS-20



HAPS-10



Synplicity®

Simply Better Results

www.synplicity.com, haps@synplicity.com

Synplicity, Inc. 600 West California Avenue, Sunnyvale, CA 94086

Phone: (U.S.) +1 408 215-6000

Copyright © 2007 Synplicity, Inc. All rights reserved. Specifications subject to change without notice. Synplicity, the Synplicity logo, and "Simply Better Results" are registered trademarks of Synplicity, Inc. HAPS, "High-performance ASIC Prototyping System", and HapsTrak are trademarks of Synplicity, Inc. Virtex-II, Virtex-II Pro, Virtex-4, and Virtex-5 are registered trademarks of Xilinx Inc.