

PERFORMANCE



Tired of spinning in circles to meet timing on your FPGA designs?

Synplicity's **Synplify® Premier** solution solves FPGA timing closure through a unique graph-based physical synthesis technology providing highly accurate correlation to final timing and 5 to 20% better performance in a single-pass flow.



FPGA Implementation

For more information on the Synplify Premier product and how it can help you quickly reach aggressive timing goals for your FPGAs, visit:

www.synplicity.com/products



Synplicity®

Simply Better Results

The **Synplify Premier** tool was awarded the prestigious LSI of the Year Grand Prix Award in June 2006. This award is sponsored by Semiconductor News (Japan). The software was also a finalist for the 2005 EDN Innovation Award, and 2005 DesignVision Award.