

Spartan™-3 Generation Display Solutions

inrevium
by Tokyo Electron Device Ltd.



Complete development solution with pre-verified reference designs and IPs for digital image processing applications.

Reference Designs (for Free):

- 7:1 LVDS Rx/Tx display link
- RSDS/Mini LVDS Tx
- 266Mbps DDR SDRAM Controller
- Color Temperature Correction
- Precise Gamma Correction(PGC)
- Image Dithering Engine(IDE) etc.

IPs (from Xilinx Partners):

- Apical Limited
- Dynamic Range Compression(iridix)
- Noise Reduction(NR)

Spartan-3A Display Solutions Board TB-3S-1400A-IMG

RoHS
Compliant

Features

Xilinx Device: XC3S1400A-FGG484, XCF08P VO48
Memory: DDR SDRAM 512Mb x 16bit x 4(32bit x 2ch)
Interfaces: RS-232C x1
High Speed I/O Connector x4
Option Board: DVI Tx and Rx

Spartan-3E Display Development Kit HW-SPAR3E-DISP-DK-UNI-G-PROMO

RoHS
Compliant

Features

Xilinx Device: XC3S1600E-FG484C, XCF08P-FS48
Memory: DDR SDRAM 512Mb x 16bit x 4 (32bit x 2ch)
Flash Memory: 16Mbit SPI
Interfaces: RS-232C x1
High Speed I/O Connector x4
Option Board: DVI Tx and Rx, Pin header adapter cards x2

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