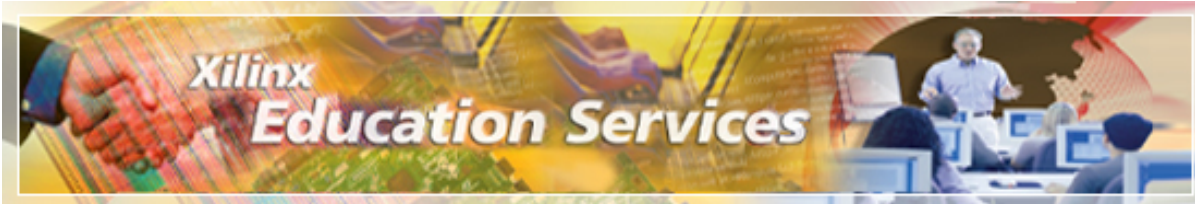




Xilinx Education Services Newsletter – December 2007



Xilinx Education Services: The Source for Xilinx Education

Brought to you by Xilinx, the leader in Programmable Logic technology — Xilinx Education Services is *the* source for Xilinx education.

Our programs:

- Provide targeted, high-quality education products and services that are designed by experts in Programmable Logic design and delivered by qualified trainers
- Offer targeted courses at all levels of Programmable Logic design
- Create an engaging learning environment by blending lecture, hands-on labs, interactive discussions, tips, and best practices
- Deliver training when and where you need it by leveraging our global network of 22 Authorized Training Providers (ATP) and online learning systems

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Authorized Training Provider (ATP)

The Xilinx Authorized Training Provider Network delivers consistent, high-quality Xilinx training worldwide. To qualify as an ATP, training providers must be:

- Training experts and providers of Xilinx training products, with the ability to offer local classes in the native language
- Qualified by Xilinx to teach our courses to a high level of quality and customer satisfaction
- Skilled in guiding customers in the use of curriculum paths and training plans to ensure they get the most from their learning experience
- Flexible, using an open approach to address customer training requirements
- Paid in local currency or with training credits available through the [Xilinx Productivity Advantage](#) program

Instructor-Led Training News

Instructor-led training offers the advantage of face-to-face learning from our team of expert trainers with first-hand knowledge of Programmable Logic products and technology. Their goal is to accelerate your success by enabling you to get the most out of the powerful capabilities of Xilinx products and technology. By attending an instructor-led course, you will become more competent and confident in the use of Programmable Logic devices. These classes can be brought to you on-site, or you can attend them at over 110 public locations around the world.

To register, or learn more about all of the courses, new courses and curriculum paths Xilinx offers, visit www.xilinx.com/education.

The following classes will use the ISE™ v9.1 software that is now available. All classes will use the new course workbook and design tools.

[Designing for Performance for CPLDs v9.1i](#) — Register for a class near you

This intermediate-level course provides you with a comprehensive overview of the CPLD software flow.

After completing this training, you will have the necessary skills to:

- Apply techniques to fit more logic into a device
- Describe the CoolRunner™-II CPLD timing model and how it can be used to analyze design performance
- Describe the advanced capabilities of the CoolRunner-II CPLD architecture
- Estimate the power consumption of a CPLD design
- ...and much more

Course Design

This is a 1-day course that balances lecture modules and practical hands-on labs.

New in this course:

- Updated software to ISE v9.1 SP3

Software

This course uses ISE 9.1i SP3

[Introduction to AccelDSP™](#) — Register for a class near you

In this course, you will learn how to synthesize an algorithm written in the language of MATLAB® software into a design that is optimized for a Xilinx FPGA. You will also find out how to make coding changes in MATLAB software and so much more. After completing this training, you will have the necessary skills to:

- Transform a non-synthesizable MATLAB software algorithm into a design that can be synthesized by the AccelDSP™ synthesis tool
- Identify the concepts of quantization as well as specify, monitor, and control bit growth in a MATLAB software design
- Modify the MATLAB software design for a direct form FIR filter into a synthesizable polyphase decimation filter
- Recognize that hardware may be required for high-level abstraction
- Apply coding style changes and AccelDSP synthesis tool directives to optimize a design for performance and efficiency
- Write MATLAB software coding changes to add hardware control features to a design
- And much more

Course Design

Through hands-on exercises in this 2-day course, you will implement a design from algorithm concept to hardware verification by using Xilinx capabilities.

New in this course:

- Updated to ISE v9.2 software

Software

This course uses ISE 9.2i, Xilinx System Generator 9.2, Xilinx AccelDSP Synthesis Tool 9.2, MATLAB R2007a, Mentor Graphics ModelSim PE 6.2f

Live e-Learning News

Live e-Learning (LEL) combines the excellence of our traditional instructor-led training with the benefits of learning from the comfort of your own desk. Live e-Learning saves time and money by delivering a series of lectures and labs online over the course of a one- to two-week period, without sacrificing the skills development that only comes from directly working with an instructor, development tools, and demo boards. Live e-Learning is available in North America only.

Sound interesting? Redeem your Training Credits for an upcoming Live e-Learning class today by accessing them at <https://xilinx.onsaba.net/Saba/Web/Main>. Select "Live e-Learning" to register for any offering, including new courses .

The following Live e-Learning courses have been updated using v9.1 course materials.

Fundamentals of FPGA Design LEL:

This course covers the ISE v9.1i software features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. The next available course will be conducted

on January 7, 2008 through January 11, 2008 on Monday, Tuesday, and Friday.

Designing for Performance LEL:

This Live e-Learning course will teach you how to fit your design into a smaller FPGA or a lower speed grade to reduce system costs. By mastering the tools and the design methodologies presented in this course, you can shorten your development time and lower development costs. The next available course will be conducted on January 14, 2008 through January 25, 2008 on Monday, Tuesday, and Friday.

For a complete Live e-Learning course listing and to find out class dates, contact a Xilinx registrar at registrar@xilinx.com or 1-(877) XLX-CLASS.

Recorded e-Learning News

Recorded e-Learning will help you get up to speed quickly on the powerful capabilities of Xilinx technology. As a prerequisite to many of the Xilinx Curriculum paths, these courses ensure that you can take full advantage of the powerful Instructor-Led training or Live e-Learning courses. These recordings are available at no charge, over the Internet, anytime, day or night. Recorded e-Learning is offered in English only.

For a list of Recorded e-Learning go to <http://www.xilinx.com/support/training/free-courses.htm>.

Interactive Recorded e-Learning News

Xilinx Education Services offers the following self-paced interactive Recorded e-Learning courses. These courses offer the most flexibility with optional audio and textual narration and interactive exercises to engage your participation and enhance your understanding of the presented material.

[View a list of available interactive recorded e-Learning and start learning today.](#)

Additional Xilinx Product Information

If you would like to have the latest product information on Virtex™ FPGAs, Spartan™ devices, CPLDs, Design tips, Development tools, and more, go to http://www.xilinx.com/push_email/newsletter/index.htm

Demos-on-Demand

[Virtex-5 Power Estimation and Measurement Demo](#)

This demonstration looks at the steps required to create an accurate power estimate for Virtex-5 devices using the XPower Estimator (XPE) tool. It demonstrates the low-power consumption characteristics of Virtex-5 devices by taking actual measurements on an ML550 board, the preferred platform for making detailed power measurements. Viewers will see a comparison of the Xilinx XPE results to the silicon measurements to determine the accuracy of our initial estimate. XPE is a free, downloadable tool that enables engineers to quickly calculate power consumption projections based on their design's parameters. It is available from the Power Central page at www.xilinx.com/power.

On-Demand Webcasts

[Dr. Howard Johnson Talks about Virtex-5 FPGAs](#)

In this new video series the acclaimed design consultant and signal integrity expert delivers an introduction to the Virtex-5 family and explores how its system-level capabilities address design challenges.

New Webcast - [Software Defined Radio: Enabling Defense Communication](#)

Learn about system-level solutions for rapid development and deployment of SDR products that meet demanding SWAP-C goals. Enabled by high-performance Virtex platform FPGAs, with built-in DSP and PowerPC® processor blocks, and Spartan-3 FPGAs to address small-form factor applications, these solutions also include powerful development tools, DSP libraries, and SDR development kits to help shrink design cycles.

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