



Xilinx Education Services Newsletter – March 2007

Instructor-Led Training News

Xilinx has made the following updates to our instructor-led course offerings.

Designing with Virtex-5-LX and LXT Platform FPGA

A new version of Designing with Virtex™-5-LX and LXT Platform FPGA is now being delivered taking advantage of the most recent release of ISE and the FPGAs it supports.

Course Design

This is a 1-day course covering the following topics: Virtex-5 FPGA overview, new CLB, DCM and PLL, global and regional clocking techniques, memory, DSP and arithmetic logic, and source-synchronous resources. The new resources available in the LXT platform (EMAC, PCI Express®, and GTP) are also discussed.

New in This Course:

- ISE™ 9.1i and its support for Virtex-5-LXT family of devices.
- An introduction to the new TriMode Ethernet MAC, PCI Express, Rocket IO™ GTP and System Monitor functionality.
- The opportunity to use the DSP slice by creating several cores and verifying their ALU and OP modes.

Software — This course uses ISE 9.1i

[Learn more about this Virtex-5-LXT course](#)

Designing with Ethernet MAC Controllers v8.2

A new course titled Designing with Ethernet MAC Controllers (EMAC) v8.2 is now available from Xilinx. This course is a comprehensive introduction to the Xilinx EMAC solution.

Course Design

This is a 2-day course covering the following topics: Learn the basics of the Ethernet standard, protocol, and OSI model while applying Xilinx solutions via hands-on laboratory exercises. Perform simulation to understand fundamental principles and obtain the knowledge to assess hardware design considerations and software development requirements.

Software

This course uses ISE 8.2 SP1, IP Update #1, EDK 8.2 software, Mentor Graphics ModelSim PE 6.0

Hardware

Virtex-4 is the target device and we use the ML403 board in two out of the six labs.

[Learn more about this eMAC Controller course](#)

Live e-Learning News

Live e-Learning (LEL) courses are a series of lectures and labs that are delivered by a live Xilinx instructor online. As a student, you can attend from the comfort of your desk and obtain the same knowledge taught during the traditional course. Live e-Learning works by scheduling course modules sequentially over a one to two-week period, depending upon the subject. Shortly after registering for a Live e-learning class, you will receive an FTP address for your course workbook allowing you to follow along with the material delivered by the live instructor through a central interface and a phone. How do we conduct labs? Toolwire technology makes it possible for you to perform Xilinx labs in a virtual environment and receive hands-on experience with the Xilinx software tools while remaining online.

Sound interesting? Redeem your Training Credits for upcoming Live e-Learning classes in 2007!

[View the complete schedule and sign up today.](#) Select "Live e-Learning" to register for any offering.

In This Issue

- [Instructor-Led Training News](#)
- [Live e-Learning News](#)
- [Recorded e-Learning News](#)
- [Demos-on-Demand](#)

Fundamentals of FPGA Design v8.2 LEL

This course covers the ISE 8 software features, such as the Architecture Wizard and the Pin and Area Constraint Editor (PACE). Other topics include design planning, implementation options, and global timing constraints.

This course is scheduled to begin on April 2, 2007 with sessions taking place on April 2, 3rd, and 6th. The Monday, Tuesday and Friday sessions will run 9-11AM PDT.

This course will also be offered May 7th through May 11th. With sessions taking place on Monday, Tuesday and Friday and will run 9-11AM PDT.

Designing for Performance v8.2 LEL

This Live e-Learning teaches you how to fit your design into a smaller FPGA or a lower speed grade to reduce system costs. By mastering the tools and the design methodologies presented in this course, you can shorten your development time and lower development costs.

This course is scheduled to begin on April 9, 2007 with sessions taking place on April 9th, 10th, 13th, 16th, 17th, and 20th. The Monday, Tuesday and Friday sessions will run 9-11AM PDT.

This course will also be offered May 14th through May 25th. With sessions taking place on Monday, Tuesday and Friday and will run 9-11AM PDT.

Advanced FPGA Implementation v8 LEL

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE tool suite and includes labs that provide hands-on experience. Scheduled over a two-week period, this course comprises a series of six two-hour sessions (with sessions running on Monday, Tuesday, and Friday of each week).

This course is scheduled to begin on April 23, 2007 with sessions taking place on April 23rd, 24th, 27th, 30th, May 1st and 4th. The Monday, Tuesday and Friday sessions will run 9-11AM PDT.

Are you looking for a particular Xilinx course in a Live e-Learning format? If a course you desire to take is not listed above, contact a Xilinx registrar at: registrar@xilinx.com or 1-(877) XLX-CLASS.

Recorded e-Learning News

Xilinx Education Services offers a series of Recorded e-Learnings that can help you get up to speed quickly on the powerful capabilities of Xilinx technology. These recordings are available at no charge, over the internet anytime day or night, and can be taken anywhere there is an internet connection. [View the complete set of modules available](#). Select "Recorded e-Learning" to register for any offering.

Basic FPGA Configuration

A new Recorded e-Learning is now available. Basic FPGA Configuration is comprised of two separate modules, this recorded e-learning introduces you to FPGA configuration so that you can choose the appropriate configuration scheme that best supports your application. After completing this training, you will be able to:

- Describe the FPGA configuration pins
- Choose an appropriate FPGA configuration scheme
- Identify how to connect multiple FPGAs into a configuration daisy chain
- Describe the prototyping hardware currently available

[Visit here to get more information](#)

Demos-on-Demand

Xilinx offers a wide variety of product demos and product specific presentations. The most recent offerings are:

- CoolRunner-II Starter Kit
- Spartan-3A Starter Kit
- Memory Interfaces Solutions (Virtex-5 & Spartan-3)
- ISE 9.1i What's New
- ISE 9.1i Performance, Design Tools and Flow
- ISE 9.1i SmartCompile Technology

[View all product demos and presentations here.](#)

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