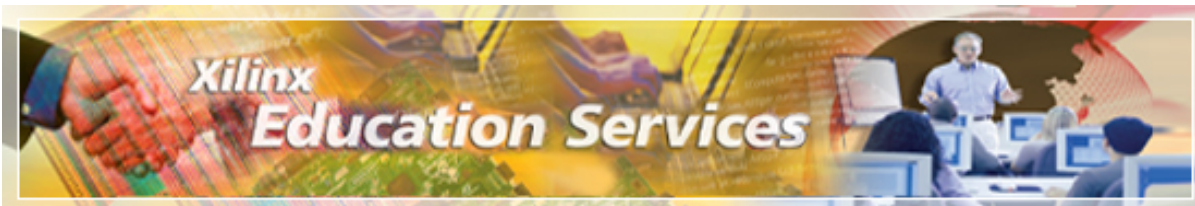




Xilinx Education Services Newsletter – March 2008, Last Issue



Xilinx Education Services: The Source for Xilinx Education

Brought to you by Xilinx, the leader in Programmable Logic technology — Xilinx Education Services is *the* source for Xilinx education.

Our programs:

- Provide targeted, high-quality education products and services that are designed by experts in Programmable Logic design and delivered by qualified trainers
- Offer targeted courses at all levels of Programmable Logic design
- Create an engaging learning environment by blending lecture, hands-on labs, interactive discussions, tips, and best practices
- Deliver training when and where you need it by leveraging our global network of 23 Authorized Training Providers (ATP) and online learning systems
- For more information visit: <http://www.xilinx.com/education-home.htm>

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[New Authorized Training Provider in the Northeast](#)

Authorized Training Provides (ATP) are a network of trainers that compliment Xilinx Education Services by offering training courses locally. Xilinx Education Services welcomes the latest ATP, Bottom Line Technologies, Inc. to the team. They are certified to deliver high quality training on programmable logic and system level design. Bottom Line Technologies, Inc. will deliver training to customers in CT, DC, DE, MD, NJ, NY, VA and eastern PA.

For more information visit: <http://www.xilinx.com/education-home.htm>

[New Xilinx Newsletter merges Multiple Xilinx Newsletters](#)

This will be your last issue of the Xilinx Education Services Newsletter. It will merge with the Xilinx Platform, DSP and Embedded newsletters to form one monthly Xilinx Newsletter!

You can now look forward to a single monthly newsletter packed with key relevant technical information. This new design will allow you to select the news you want to receive.

[> Register Free](#)

Instructor-Led Training News

Instructor-led training offers the advantage of face-to-face learning from our team of expert trainers with first-hand knowledge of Programmable Logic products and technology. Their goal is to accelerate your success by enabling you to get the most

out of the powerful capabilities of Xilinx products and technology. By attending an instructor-led course, you will become more competent and confident in the use of Programmable Logic devices. These classes can be brought to you on-site, or you can attend them at over 110 public locations around the world.

To register, or learn more about all of the courses, new courses and curriculum paths Xilinx offers, visit www.xilinx.com/support/education-home.htm.

The following classes will use the ISE™ v9.2i software that is now available. All classes will use the new course workbook and design tools.

[Designing a LogiCORE™ PCI Express System v9.2](#) — Register for a class near you

By learning PCI Express® core protocol fundamentals, you will gain a knowledge of how the PCI Express technology can be used in your systems.

After completing this training, you will have the necessary skills to:

- Effectively use the Xilinx PCI Express cores in your design environments
- Select the appropriate PCI solution for a specific application
- Identify how PCI Express specification requirements apply to using Xilinx PCI Express cores
- ...and much more

Course Design

This is a 2-day course that balances lecture modules and practical hands-on labs.

New in this Course:

- Updated software to ISE v9.2i, ISIM v9.2i, ChipScope™ Pro v9.2i, and ChipScope Pro Analyzer v9.2i

Software

- This course uses ISE v9.2i

[Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools](#) — Register for a class near you

— **NOTE: This course is not available in North America until the end of April 2008**

As FPGA designs become increasingly more complex, designers are searching to reduce design and debug time. The powerful, yet easy-to-use ChipScope Pro tool solution helps minimize the amount of time required for debug and verification, which can often consume a significant amount of total FPGA design time.

After completing this course, you will have the necessary skills to:

- Optimize debugging speed
- Overcome storage issues
- Perform a system-level debug
- And much more

Course Design

This 1-day training will provide hands-on labs that demonstrate how the ChipScope Pro tools can address advanced debugging and verification challenges.

New in this Course:

- Updated software to ISE v9.2i, ChipScope Pro v9.2i, ChipScope Pro Serial I/O Toolkit v9.2i and Memory Interface Generator 2.0

Software

- This course uses ISE v9.2i

Live e-Learning News

Live e-Learning (LEL) combines the excellence of our traditional instructor-led training with the benefits of learning from the comfort of your own desk. Live e-Learning saves time and money by delivering a series of lectures and labs online over the course of a one- to two-week period, without sacrificing the skills development that only comes from directly working with an instructor, development tools, and demo boards. Live e-Learning is available in North America only.

Sound interesting? Redeem your Training Credits for an upcoming Live e-Learning class today by accessing them at <https://xilinx.onsaba.net/Saba/Web/Main>. Select "Live e-Learning" to register for any offering, including new courses .

The following Live e-Learning courses have been updated using v9.1 course materials.

Fundamentals of FPGA Design LEL

This course covers the ISE v9.1i software features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. The next available course will be conducted on April 7, 2008 through April 11, 2008 on Monday, Tuesday, and Friday.

Designing for Performance LEL

This Live e-Learning course will teach you how to fit your design into a smaller FPGA or a lower speed grade to reduce system costs. By mastering the tools and the design methodologies presented in this course, you can shorten your development time and lower development costs. The next available course will be conducted on April 14, 2008 through April 25, 2008 on Monday, Tuesday, and Friday.

Introduction to Verilog LEL

This comprehensive 9-session course is a thorough introduction to the Verilog language. The emphasis is on writing Register Transfer Level (RTL) and behavioral source code. This class addresses targeting Xilinx devices specifically and FPGA devices in general. Incoming students with little or no Verilog knowledge will finish this course empowered with the ability to write efficient hardware designs and perform high-level HDL simulations. The next course will be conducted on March 18, 2008 through March 28, 2008 on Monday, Tuesday and Friday.

Designing with PlanAhead LEL

In this course, you will learn how to increase design performance and achieve repeatable results by using the PlanAhead™ software tool. Topics include: a product overview, synthesis and project tips, design analysis, creating a floorplan, improving performance and much more. The next course will take place on April 14, 2008 through April 25, 2008 on Monday, Tuesday and Friday.

For complete Live e-Learning course listings and to find out additional class dates, contact a Xilinx registrar at registrar@xilinx.com or 1-877-XLX-CLAS.

Recorded e-Learning News

Recorded e-Learning will help you get up to speed quickly on the powerful capabilities of Xilinx technology. As a prerequisite to many of the Xilinx curriculum paths, these courses ensure that you can take full advantage of the powerful Instructor-led training or Live e-Learning courses. These recordings are available at no charge, over the Internet, anytime, day or night. Recorded e-Learning is offered in English only.

For a list of Recorded e-Learning go to <http://www.xilinx.com/support/training/free-courses.htm>.

Interactive Recorded e-Learning News

Xilinx Education Services offers the following self-paced interactive Recorded e-Learning courses. These courses offer the most flexibility with optional audio and textual narration and interactive exercises to engage your participation and enhance your understanding of the presented material.

[View a list of available interactive recorded e-Learning and start learning today.](#)

Additional Xilinx Product Information

[Visit Xilinx Newsletter archives](#) to get the latest product information on Virtex™ FPGAs, Spartan™ devices, CPLDs, Design tips, Development tools, and more.

[New Memory Interface Design Demo With MIG 2.0](#)

This 31 minute video demonstration, by Adrian Cosoroaba, is an overview of Xilinx solutions for memory interfaces and the features and benefits of the Memory Interface Generator (MIG) 2.0 design tool. This demonstration includes an explanation of the options available during a walk-through of an example design from start to final generation of the ucf and rtl files.

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