



AppsRst

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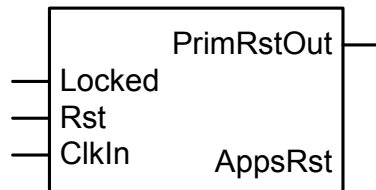
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Toplevel



When Rst is low and Locked is high the PrimRstOut output will go low after a delay set by a generic value. The delay is the address input of a SRL32, the delay can thus be selected between 0 and 31. When Locked goes low or Rst goes high the output of the circuit goes high, forcing the components connected to the PrimRstOut output in reset.

