

FPGA LPC IO-Banks 12 and 13 connected via cable

As shown on page 7 the transmitter, Virtual ADC, will be connected to the receiver, ADC interface in the FPGA. This holds that IO-Bank 12 is connected to IO-Bank 13 by means of a XM101 board plugged into the LPC FMC connector of the KC705 board.

This slide shows the interconnections via the XM101 board between the IO-Banks.

