AdcMem

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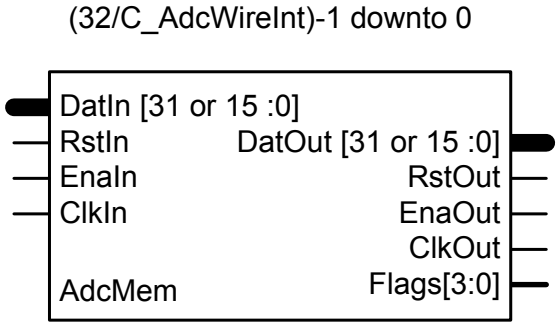
AdcMem



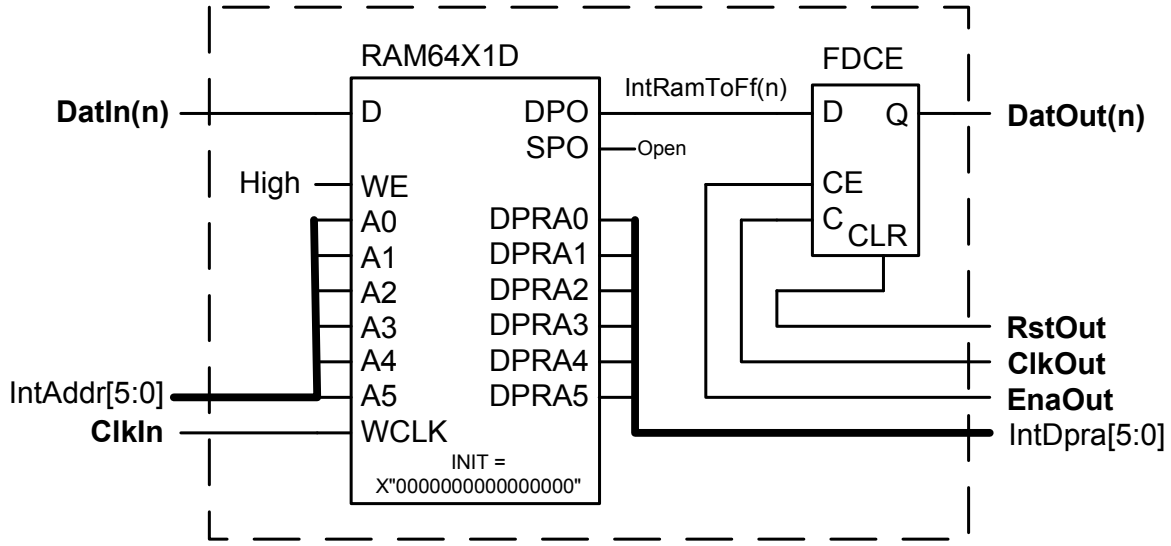
ADC interface side.
ClkIn is the CLKDIV

Application side.
ClkOut is the application clock who can be the same as CLKDIV but phase shifted or who can be different than CLKDIV.

The size of the memory is 16-bit when the interface is running in 2-wire mode or 32-bit for an interface running at 1-wire mode.

The AdcData interface contains all logic for two channel inputs.
When a ADC is used in 2-wire mode its data is spread over two LVDS lanes or twoi channels.
Then the AdcData interface figures as interface for one channel an dthe output is 16-bit. In fact the output is 32-bit bit MSB word and LSB word contain the same data.

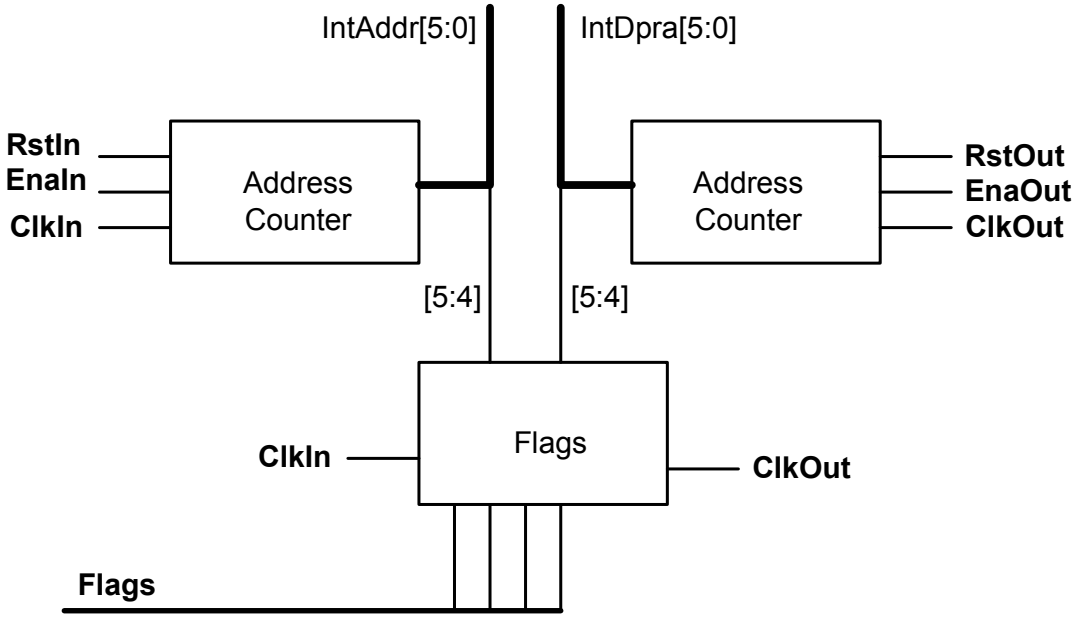
When an ADC is used in 1-wire mode, two channels can be hooked to one AdcData interface. The 32-bit output will conain data of both channels. MSB word = Channel 1 and LSB word = channel 0.



N = (32/C_AdcWireInt) -1 downto 0

In 1-wire mode the memory is 32-bit wide.

In 2-wire mode the memory is 16-bit wide.



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