

UART I/O Signals

The I/O signals for the UART are listed in **Table 2**. The interfaces referenced in this table are shown in **Figure 1** in the UART block diagram.

Table 2: UART I/O Signals

| Grouping | | Signal Name | Interface | I/O | Description |
|-------------------|-----|-------------------------------------|-----------|-----|--|
| PLB Slave Signals | P1 | PLB_abort | PLB | I | PLB abort bus request indicator |
| | P2 | PLB_ABus(0:C_PLB_AWIDTH-1) | PLB | I | PLB address bus |
| | P3 | PLB_BE(0:(C_PLD_DWIDTH / 8) -1) | PLB | I | PLB byte enables |
| | P4 | PLB_busLock | PLB | I | PLB bus lock |
| | P5 | PLB_compress | PLB | I | PLB compressed data transfer indicator |
| | P6 | PLB_guarded | PLB | I | PLB guarded transfer indicator |
| | P7 | PLB_lockErr | PLB | I | PLB lock error indicator |
| | P8 | PLB_masterInd(0:C_PLB_MID_WIDTH -1) | PLB | I | PLB current master indicator |
| | P9 | PLB_ordered | PLB | I | PLB synchronize transfer indicator |
| | P10 | PLB_PAVali | PLB | I | PLB primary address valid indicator |
| | P11 | PLB_rdBurst | PLB | I | PLB burst read transfer indicator |
| | P12 | PLB_rdPrim | PLB | I | PLB secondary to primary read request indicator |
| | P13 | PLB_RNW | PLB | I | PLB read not write |
| | P14 | PLB_SAVali | PLB | I | PLB secondary address valid indicator |
| | P15 | PLB_size(0:3) | PLB | I | PLB transfer size |
| | P16 | PLB_type(0:2) | PLB | I | PLB transfer type |
| | P17 | PLB_wrBurst | PLB | I | PLB burst write transfer indicator |
| | P18 | PLB_wrDBus(0:C_PLB_DWIDTH -1) | PLB | I | PLB write data bus |
| | P19 | PLB_wrPrim | PLB | I | PLB secondary to primary write request indicator |
| | P20 | PLB_MSize(0:1) | PLB | I | PLB master data bus size |
| | P21 | PLB_pendReq | PLB | I | PLB pending bus request indicator |
| | P22 | PLB_pendPri(0:1) | PLB | I | PLB pending request priority |
| | P23 | PLB_reqPri(0:1) | PLB | I | PLB current request priority |
| | P24 | SI_addrAck | PLB | O | Slave address acknowledge |
| | P25 | SI_MBusy(0:C_NUM_MASTERS-1) | PLB | O | Slave busy indicator |
| | P26 | SI_MErr(0:C_NUM_MASTERS-1) | PLB | O | Slave error indicator |
| | P27 | SI_rdBTerm | PLB | O | Slave terminate read burst transfer |
| | P28 | SI_rdComp | PLB | O | Slave read transfer complete indicator |
| | P29 | SI_rdDAck | PLB | O | Slave read data acknowledge |
| | P30 | SI_rDBus(0:C_PLB_DWIDTH -1) | PLB | O | Slave read bus |
| | P31 | SI_rdWdAddr(0:3) | PLB | O | Slave read word address |
| | P32 | SI_rearbitrate | PLB | O | Slave rearbitrate bus indicator |

