



SYMBOL	MILLIMETERS			NOTE
	MIN.	NOM.	MAX.	
A	\approx	1.00	1.10	
A ₁	0.15	0.20	0.25	
D/E	7.90	8.00	8.10	
D ₁ /E ₁	6.50 BSC			
e	0.50 BSC			
ϕb	0.25	0.30	0.35	
ccc	\approx	\approx	0.10	
ddd	\approx	\approx	0.08	
eee	\approx	\approx	0.15	
Z _D /Z _E	0.60	0.75	0.90	
M	14			2

NOTES:

1. ALL DIMENSIONS AND TOLERANCES CONFORM TO ASME Y14.5M-1994
2. SYMBOL "M" IS THE PIN MATRIX SIZE.
3. CONFORMS TO JEDEC MO-275

PK400_01_111209

Revision History

The following table shows the revision history for this document.

Date	Version	Description of Revisions
12/07/09	1.0	Initial Xilinx release.

Notice of Disclaimer

Xilinx regards this materials data to be correct but makes no guarantee as to its accuracy or completeness, including, but not limited to, with respect to its compliance with applicable environmental laws and regulations. Xilinx subcontracts the production, test and assembly of hardware devices to independent third-party vendors and materials suppliers (“Contractors”). All data provided hereunder is based on information received from Contractors. Xilinx has not independently verified the accuracy or completeness of this information which is provided solely for your reference in connection with the use of Xilinx products.