

Schedule:

Day 1 (24th Feb. 2012):

Morning Session:

- An introduction to Partial Reconfiguration – myths and facts
- Need for Partial Reconfiguration & Concepts of Dynamic Partial Reconfiguration
- Applications that demand Partial Reconfiguration - research opportunities Partial Reconfiguration and Xilinx Design flow

Afternoon Session:

Lab 1: Introduction to Partial Reconfiguration

Lab 2: Reconfigurable peripherals

Day 2 (25th Feb. 2012):

Morning Session:

- Challenges in application domains (i.e. Telecom, networking, broadcast, automotive and embedded) and solutions from Partial Reconfiguration

Afternoon Session:

Lab 3: Partial Reconfiguration of audio filters and implementation on Virtex-5 FPGA Development Kit

Lab 4: Partial Reconfiguration and Embedded Computing

Resource Team:

CoreEL University Program Team &
VLSI Division Faculty, VIT, Vellore.

Course Fee:

Students (VIT)	- Rs.500
Students (External)	- Rs.1000
Faculty/Research Scholars	- Rs.1500

Note :

- DD should be taken in favor of “**VIT University**” payable at **Vellore**
- The participants are requested to make their own arrangement for accommodation.

Last date for Registration:

On or before 20th Feb.2012.

(Registration limited to 50 participants only)

Course Coordinators:

Prof. Arunachalam V

Assistant Professor Selection Grade,

Mobile : 9443038445

E-Mail: varunachalam@vit.ac.in

Prof. Sivanantham S

Assistant Professor Selection Grade,

Mobile : 9894432359

E-Mail: ssivanantham@vit.ac.in



VIT
UNIVERSITY
(Estd. u/e 3 of UGC Act 1956)

Vellore- 632014, Tamilnadu, India

**National Level workshop on
Xilinx Partial Reconfiguration
(24th and 25th February, 2012)**

Course Coordinators

Prof. Arunachalam V

Prof. Sivanantham S.

Organized by

VLSI Division

School of Electronics Engineering

Sponsored by



About VIT University

Founded in 1984 as Vellore Engineering College, the Institute was declared as University in recognition of its academic excellence by the Ministry of Human Resources Development, Government of India in 2001. The University has since grown by leaps and bounds—establishing excellent infrastructure spread over an impeccably clean and green 350 acre campus - well on way to fulfill the vision of the founder and Chancellor Dr. G. Viswanathan to make it truly world class.

VIT today comprises of nine constituent Schools and interdisciplinary Centers offering undergraduate, graduate and research programs up to PhD level. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and researches spur us on in our relentless pursuit of excellence.

About School of Electronics Engineering

The School was established for imparting a state-of-the-art education, training and research in the field of Electronics and Communication Engineering and allied areas. The School offers a B.Tech. Degree in ECE and four M.Tech. Degree in VLSI Design, Communication Engineering, Sensor System Technology and Nanotechnology. The School offers a double degree program in M.Tech Sensor System Technology with the University of Applied Sciences, Karlsruhe, Germany, and

M.Tech. Communication Engineering with the UAS, Darmstadt, Germany. The School also offers M.S. and Ph.D programmes. A major emphasis in both the undergraduate and post graduate program is the teaching and learning process that makes the student industry ready.

The School is actively involved in R&D activities and has sponsored projects from various agencies like DRDO, DST, ISRO (RESPOND), and BRNS. It has MOU's with industries like Texas Instruments, Altera, Cadence etc. and other reputed institutions and R&D organizations of our country and other parts of the world.

About Corel

CoreEL Technologies is a technology company with businesses spread across Design Services & Product Development, Distribution and Training. Head Quartered in Bangalore, India, CoreEL is a leading provider of VLSI & Embedded System design services and Intellectual Property. Since its inception in 1999, CoreEL Technologies a privately held corporation has always strived to deliver quality solutions & support in all the business areas that it serves.

CoreEL's Services offerings include Distribution of Silicon solutions, EDA tools, COTS products, Engineering Services (Turn Key Systems Design, Turn Key FPGA Design and High Speed PCB Design), Education and Manufacturing. These services are offered to our broad customer base comprising Defense and Aerospace, Telecommunication and Networking, Homeland Security, Broadcast

Video and Education segments. CoreEL undertake's manufacturing of all the hardware that we design for our customers there by providing end to end solutions in the System Design space.

The CoreEL University Program provides Eco-System support to Indian Academia in Engineering Higher Education, In the field of Embedded systems thereby enabling the delivery of quality education.

CoreEL university achieves this by providing state of the art products from XILINX, MENTOR GRAPHICS, WIND RIVER, MATLAB & OPAL RT to universities Multiyear application engineering support on these products Faculty and student training, providing industry specific inputs to update the curriculum and helping universities set up Centers of Excellence in Embedded Systems arena.

Agenda for the workshop

The Two day workshop is aimed at providing in depth coverage of Xilinx partial reconfiguration and its powerful features.

Prerequisite: (Preferable & Not essential)

The participants are expected to be familiar with:

- Xilinx basic FPGA architecture and ISE Design flow.
- Basic overview of Xilinx PlanAhead and its features.



VIT
UNIVERSITY
(Estd. u/s 3 of UGC Act 1956)



Vellore-632014, Tamilnadu, India

National Level workshop on Xilinx Partial Reconfiguration

(24th and 25th February, 2012)

Registration Form

Name:.....

Designation:

Department:.....

Organization:.....

Address for Correspondence:

.....

.....

E-mail:.....

Mobile No:.....

Demand Draft Details :

Category: Faculty / Student / Research Scholar *(Please tick mark one)*

Amount: DDNo.:

Bank: Dated:

Signature: Date:

Send your completed form along with DD to:

Prof. Arunachalam V, Assistant Professor Selection Grade,
School of Electronics Engineering, VIT University, Vellore -632014
Mobile : 9443038445 E-Mail: varunachalam@vit.ac.in

(Use Photocopy of the above form, if required)