

Design Services Partner Profile



SANEI HYTECHS Co., Ltd. Company Overview

Date : 09/22/2017

www.sanei-hy.co.jp

Partner Details	Partner Tier:	Select	
	Headquarters Location:	311-3 Koyasu-Cho, Higashi-Ku, Hamamatsu-shi, Shizuoka-ken,, Japan	
	Xilinx Alliance Partner Since:	1/31/2012	
Key Contact Info	Norihito Suzuki suzuki.norihito@sanei-hy.co.jp		
Regions Served	Japan		
Supported Product Types	Yes	Design Services	No Embedded Products
	No	IP	No EDA Tools
	No	Boards	
Engineering Service Types	Yes	General Purpose Services	Preferred
	No	Services in Support of Products Sold	
	No	Services Leading to Contract Manufacturing	
	Maximum Service Scope: Chip level FPGA / ASIC design		
Engagement Models Supported	Yes	SOW-Based Fixed Bid Projects	Preferred
	Yes	Time and Materials	
	None	Captive Resources at Partner or Customer Premises	
Xilinx Design Tool Experience	Yes	Vivado	Yes SDSoC
	Yes	HLx	No SDAccel
Industry Certifications Achieved	ISO 9001		
Industry Certification Services Offered			
Government Clearances	none		

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SANEI HYTECHS Co., Ltd. Segment and Application Focus

No Focus	Defense/Aerospace	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Homeland Security Electronic Warfare ISR Space Missiles & Munitions Military Avionics Commercial Avionics SatComm Wireless Milcom Other A&D
Secondary Focus	Audio, Video, Broadcast	<ul style="list-style-type: none"> No Focus No Focus No Focus Secondary Focus Secondary Focus No Focus No Focus Secondary Focus No Focus 	<ul style="list-style-type: none"> Encoders & Decoders Monitors & Projection Networking & Converters Audio Systems Video Processing Cards Video Conferencing Routers and Switches Pro Cameras & Camcorders Other Broadcast
Primary Focus	Automotive & Transport	<ul style="list-style-type: none"> Primary Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Driver Assist - Camera Driver Assist Radar/LIDAR Driver Info/Infotainment Automated Driving Other Automotive
No Focus	Consumer	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Digital Display Drones Multifunction Printers Other Consumer
No Focus	Datacenter	<ul style="list-style-type: none"> No Focus No Focus No Focus 	<ul style="list-style-type: none"> Storage Compute Networking
Primary Focus	Industrial, Medical	<ul style="list-style-type: none"> Secondary Focus No Focus No Focus Secondary Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Motor Control Industrial Networking Surveillance Machine Vision Medical Imaging Medical / Clinical Other ISM

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SANEI HYTECHS Co., Ltd. Segment and Application Focus (continued)

Primary Focus	Test & Measurement	<ul style="list-style-type: none"> Primary Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Simulation / Emulation T & M Instrumentation Semiconductor Test Wired Communication Test Wireless Communications Test Other Test & Measurement
No Focus	Wired Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Access Equipment Long Haul/Transport Wired Mobile Backhaul OTN Enterprise Equipment Ethernet Interlaken Connectivity Packet Processing Other Wired Communications
No Focus	Wireless Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Baseband Wireless Backhaul Connectivity & Switching Radio Other Wireless

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Design Competencies

System Architecture	No Focus	
RF and Analog Design	No Focus	
FPGA Design and Integration	Primary Focus	We designed FPGA Prototype board of Human detection for Automotive, and FPGA Evaluation board for CIS. In recent experience, we are offering CNN implementation by SDSoC used algorithm from customer.
FPGA Timing Closure	Secondary Focus	We also do it consistently with FPGA Design Optimization.
FPGA Design Optimization	Secondary Focus	If you do not satisfy performance such as throughput, optimize by rebuild the architecture if necessary. Sometimes we will also reverse engineer the FPGA source.
Signal Integrity Design and Analysis	No Focus	
	Inhouse or Subcontracted:	
	Typical Serial Speed:	0
PCB Design & Layout	No Focus	
	Inhouse or Subcontracted:	Both
Industrial and Mechanical Design	No Focus	
	Inhouse or Subcontracted:	Both
Industry Certification and Compliance Testing	No Focus	
	Inhouse or Subcontracted:	
Quality and Reliability Testing	No Focus	
	Inhouse or Subcontracted:	
Contract Manufacturing	No Focus	
	Inhouse or Subcontracted:	
	Max Quantities Supported:	
Digital Signal Processing	No Focus	
	Experience:	
Embedded Processors	Primary Focus	we are offering CNN implementation by SDSoC used algorithm from customer.
	Experience:	Zynq with Linux OS and Bare Metal.
Embedded Porting / BSP Extension	No Focus	
	OS and RTOS Design Experience:	
Embedded Application	No Focus	

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Development

Image Sensor Fusion

No Focus

Homo or Heterogeneous:

Sensor Types Design Experience:

Computer Vision

Machine Learning

Primary Focus

We have implement the CNN to Zynq with SDSoc.

Framework Experience:

Network Model Experience:
