

XQR Versal® ACAP Product Table

XQRVC1902-1MSBVSRA2197 (AI Core Series)		
Intelligent Engines	AI Engine Tiles	400
	AI Engine Data Memory (Mb)	100
	DSP Engines	1,968
Adaptable Engines	System Logic Cells (K)	1,968
	LUTs	899,840
	NoC Master/NoC Slave Ports	28
Memory	Distributed RAM (Mb)	27
	Total Block RAM (Mb)	34
	UltraRAM (Mb)	130
	Total PL Memory (Mb)	191
	DDR Memory Controllers	4
	DDR Bus Width	256
Scalar Engines	Application Processing Unit	Dual-core Arm® Cortex®-A72, 48KB/32KB L1 Cache w/ECC 1 MB L2 Cache w/ECC
	Real-time Processing Unit	Dual-core Arm Cortex-R5F, 32KB/32KB L1 Cache, and 256KB TCM w/ECC
	Memory	256KB On-Chip Memory w/ECC
	Connectivity	Ethernet (x2); UART (x2); CAN-FD (x2) USB 2.0 (x1); SPI (x2); I2C (x2)
Serial Transceivers	GT Transceivers	44 GTY (26.5625 Gb/s)
Integrated Protocol IP	CCIX & PCIe® w/DMA (CPM)	1 x Gen4x8, CCIX
	PCI Express	4 x Gen4x8
	Multirate Ethernet MAC	4
Platform Management Controller	Platform Management Controller	Boot, Security, Safety, Monitoring, High-Speed Debug, SEU Mitigation (XilSEM)
Package	Ruggedized Organic BGA	VSRA2197, 45mm x 45mm, 0.92mm pitch
I/O	I/O	648 XPIO, 44 HDIO, 78 MIO, 44 GTY