The Xilinx® ZYNQ™-7000 EPP ZC702 Evaluation Kit gives developers a complete development platform including hardware, development tools, IP, and pre-verified reference designs. The included targeted reference design jump-starts development, and enables in-depth exercising of the ARM processing system and Xilinx 28nm scalable optimized programmable logic architecture. Additional reference designs and industry-standard FPGA Mezzanine Connectors (FMCs) let developers scale and customize designs using Xilinx and third-party daughter cards to best fit their application and system with this development board. This evaluation kit offers a wide feature set and abundant I/O expandability, to shorten time to market for applications that call for industry-leading performance, feature differentiation, and power efficiency.

**What’s Inside the ZC702 Evaluation Kit**

- ZC702 Evaluation Board featuring the XC7Z020-1CLG484CES device
- Agile Mixed Signal (AMS)
- Full seat ISE® Design Suite Embedded Edition
- Cables and Power Supply:
  - Universal 12V power supply
  - USB cables, Ethernet cable
  - 8 GB Class 4 SD card
- USB Flash Drive (contains documentation, designs, and demos)

**Documentation, Designs, and Demos**

- Step-by-Step Getting Started Guide
- Hardware User Guide
- Reference Design and Design Example User Guide
- Schematics and PCB files
- Demonstrations
- Board design files

**A New Class of Devices: Zynq-7000 Extensible Processing Platform**

- ARM® dual-core Cortex™-A9 processor meets Xilinx 28nm programmable logic
- ASIC-like performance and power with the flexibility of an FPGA and ease of programming of a microprocessor
- Flexibility to create a custom ARM-based ASSP by adding peripherals and accelerators to the programmable logic
ZYNQ-7000 EPP ZC702 EVALUATION KIT

Take the NEXT STEP

For more information about the ZC702 Evaluation Kit, please visit: www.xilinx.com/zc702

For more information about the Zynq-7000 EPP product family, please visit: www.xilinx.com/zynq

Corporate Headquarters
Xilinx, Inc.
2100 Logic Drive
San Jose, CA 95124
USA
Tel: 408-559-7778
www.xilinx.com

Europe
Xilinx Europe
One Logic Drive
Citywest Business Campus
Saggart, County Dublin
Ireland
Tel: +353-1-464-0311
www.xilinx.com

Japan
Xilinx K.K.
Art Village Osaka Central Tower 4F
1-2-2 Osaka, Shimagawa-ku
Tokyo 141-0032 Japan
Tel: +81-3-6744-7777
japan.xilinx.com

Asia Pacific Pte. Ltd.
Xilinx, Asia Pacific
5 Changi Business Park
Singapore 486040
Tel: +65-6407-3000
www.xilinx.com

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