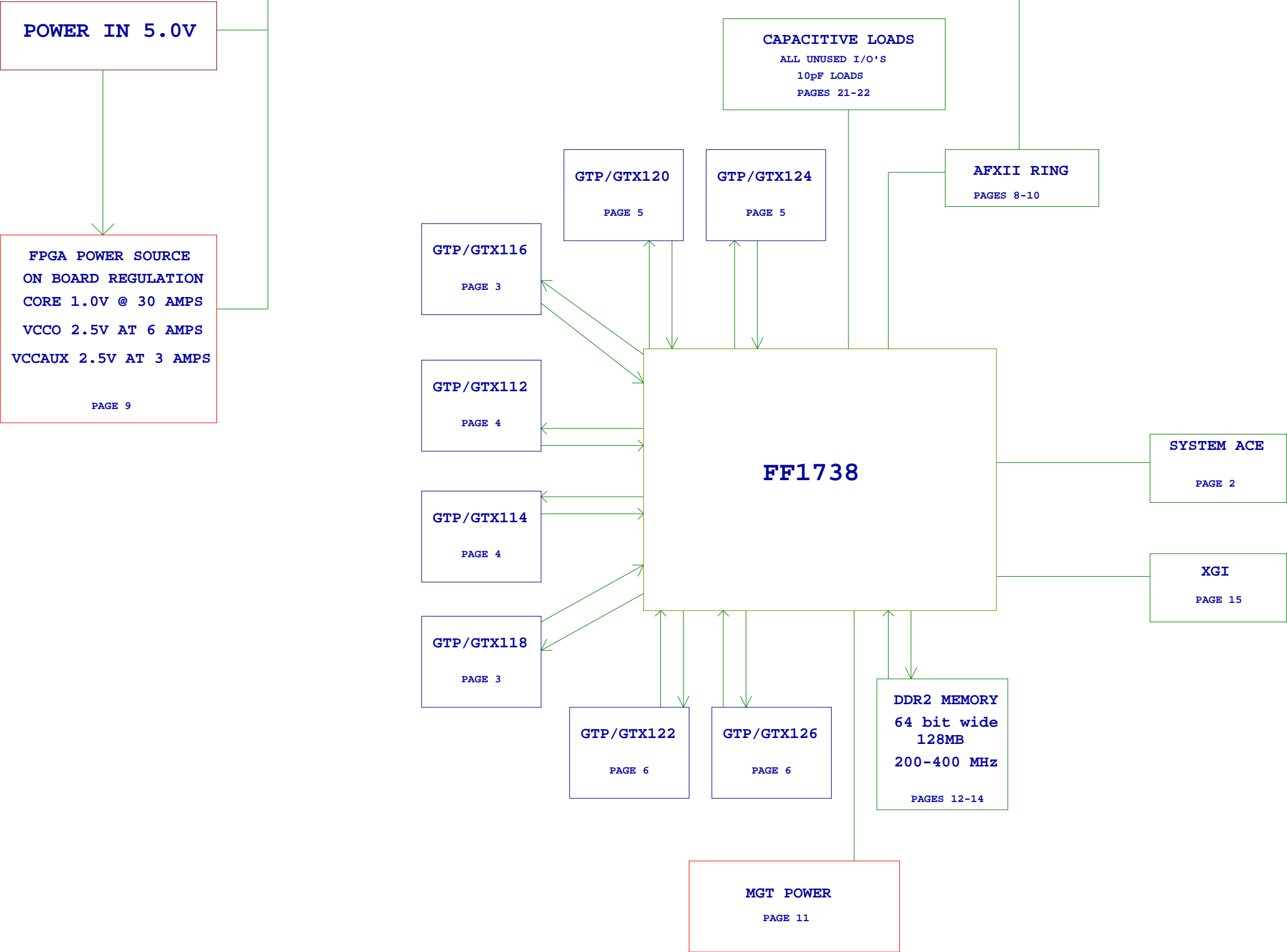


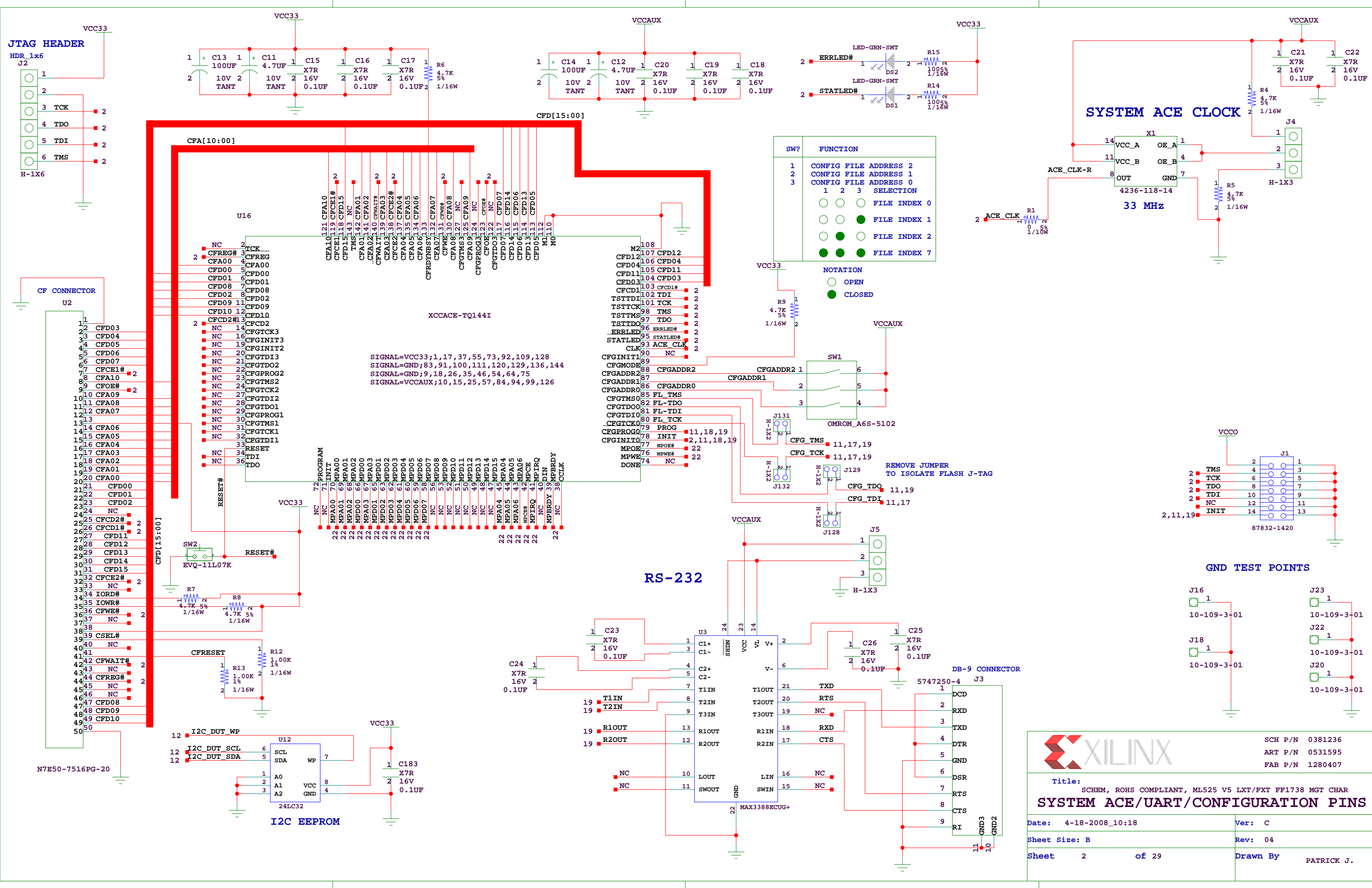
MGT CHARACTERIZATION BOARD FOR V5
AFXII COMPATIBLE



SCH P/N 0381236
ART P/N 0531595
FAB P/N 1280407

Title: SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR
BLOCK DIAGRAM

Date: 4-18-2008_10:27	Ver: C
Sheet Size: B	Rev: 04
Sheet 1 of 29	Drawn By PATRICK J.



SW?	FUNCTION			
1	CONFIG FILE ADDRESS 2			
2	CONFIG FILE ADDRESS 1			
3	CONFIG FILE ADDRESS 0			
	1	2	3	SELECTION
	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	FILE INDEX 0
	<input type="radio"/>	<input type="radio"/>	<input checked="" type="radio"/>	FILE INDEX 1
	<input type="radio"/>	<input checked="" type="radio"/>	<input type="radio"/>	FILE INDEX 2
	<input checked="" type="radio"/>	<input checked="" type="radio"/>	<input checked="" type="radio"/>	FILE INDEX 7

NOTATION
☐ OPEN
☒ CLOSED

1	6
2	5
3	4

OMROM A6S-5102

CFG TMS 11,17,19

CFG TCK 11,17,19

CFG TDO 11,19

CFG TDI 11,17

REMOVE JUMPER
TO ISOLATE FLASH J-TAG

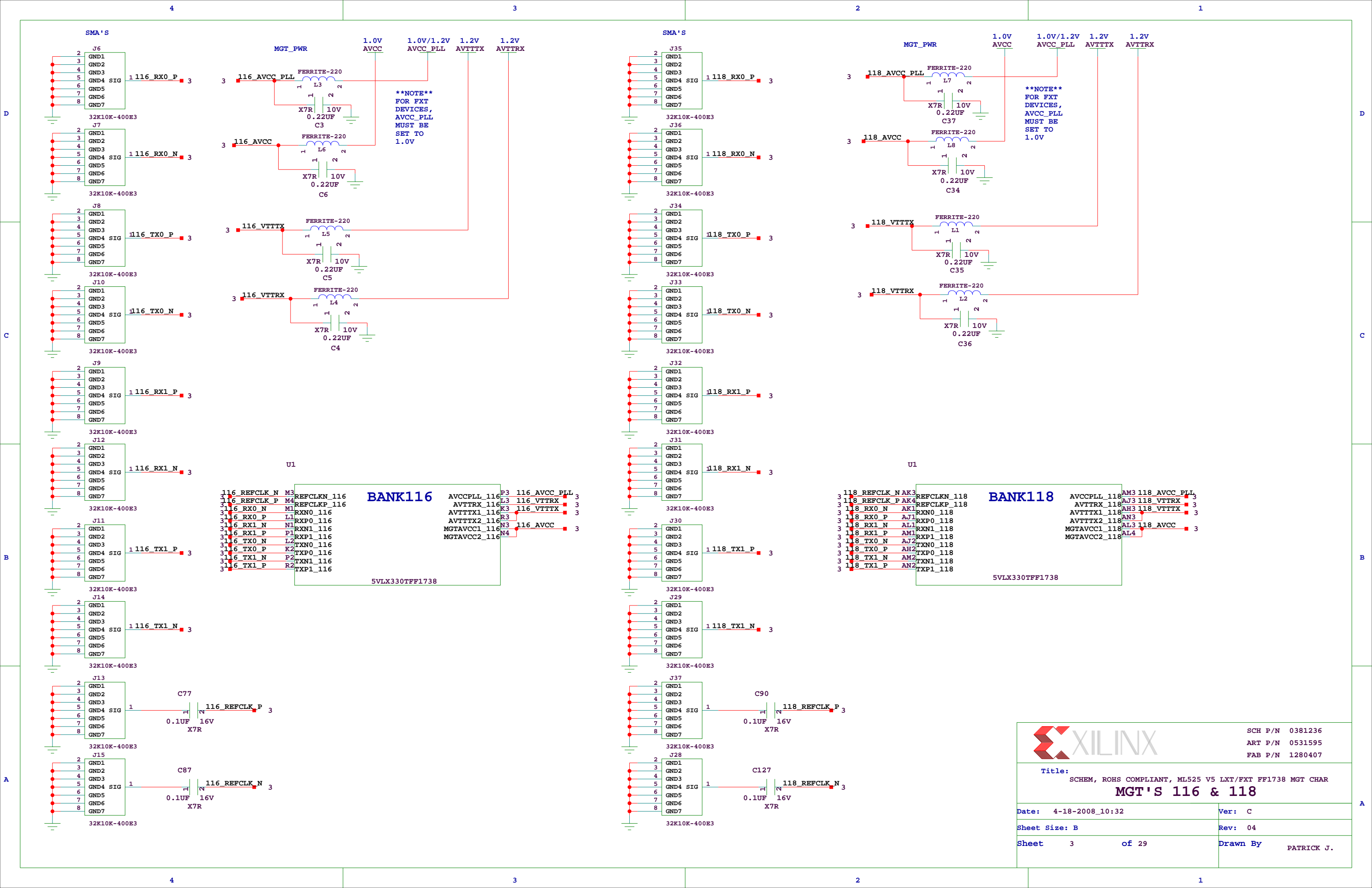
GND TEST POINTS

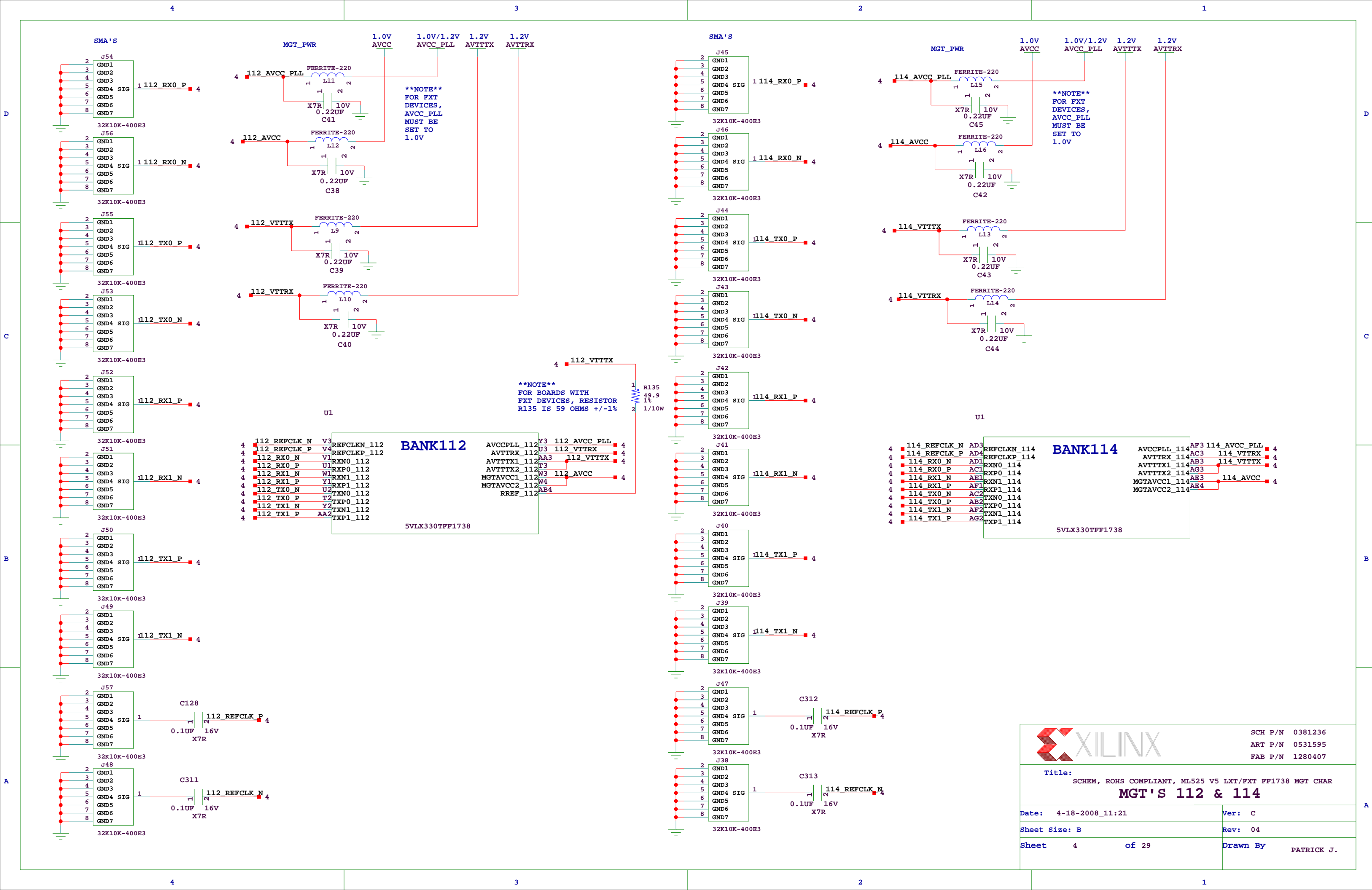
J16	1	J23	1
10-109-3-01		10-109-3-01	
J18	1	J22	1
10-109-3-01		10-109-3-01	
		J20	1
		10-109-3-01	

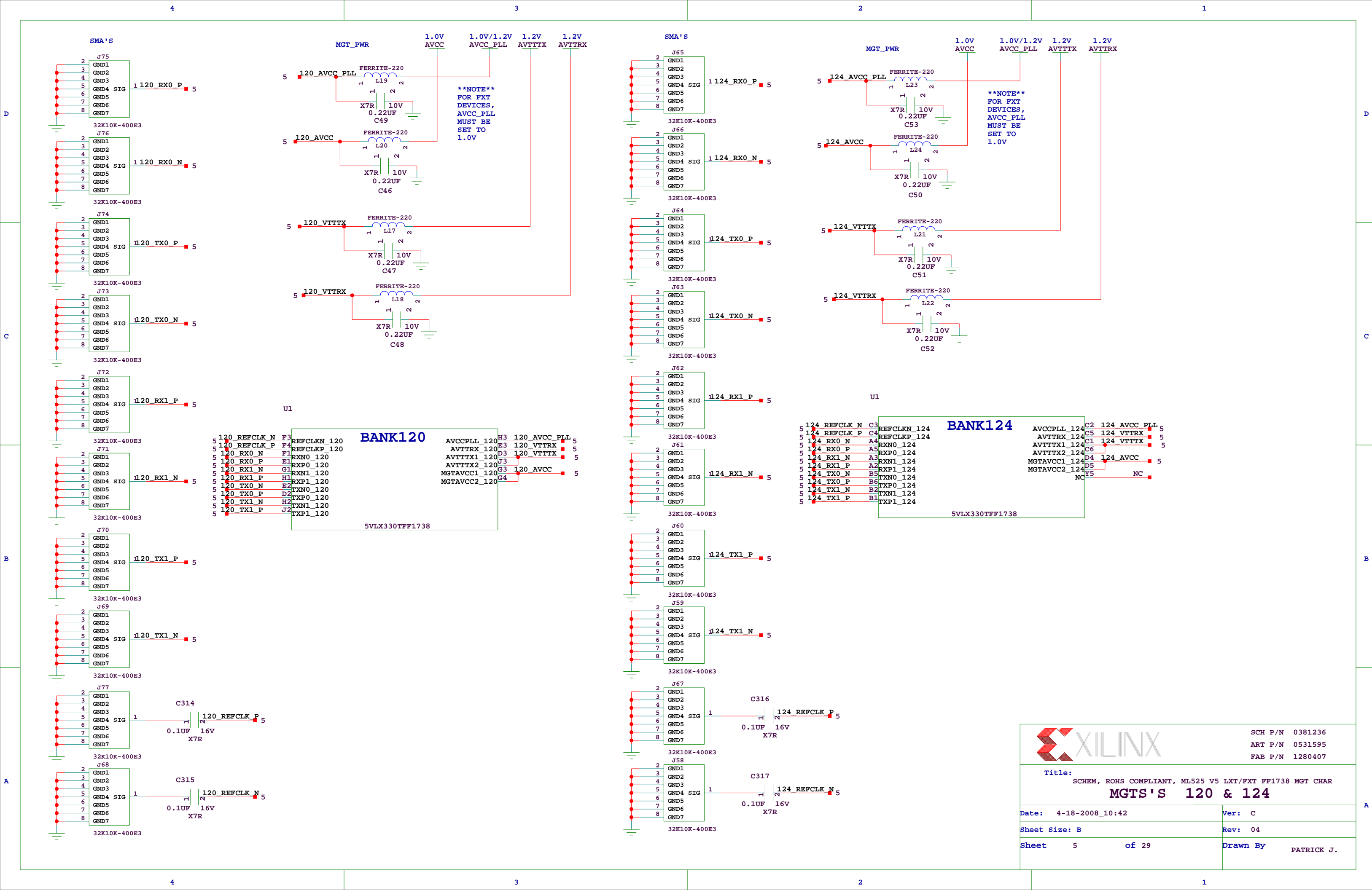


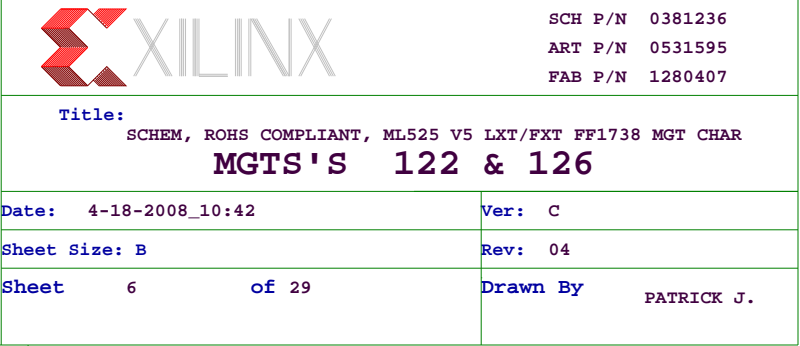
Title: SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR
SYSTEM ACE/UART/CONFIGURATION PINS

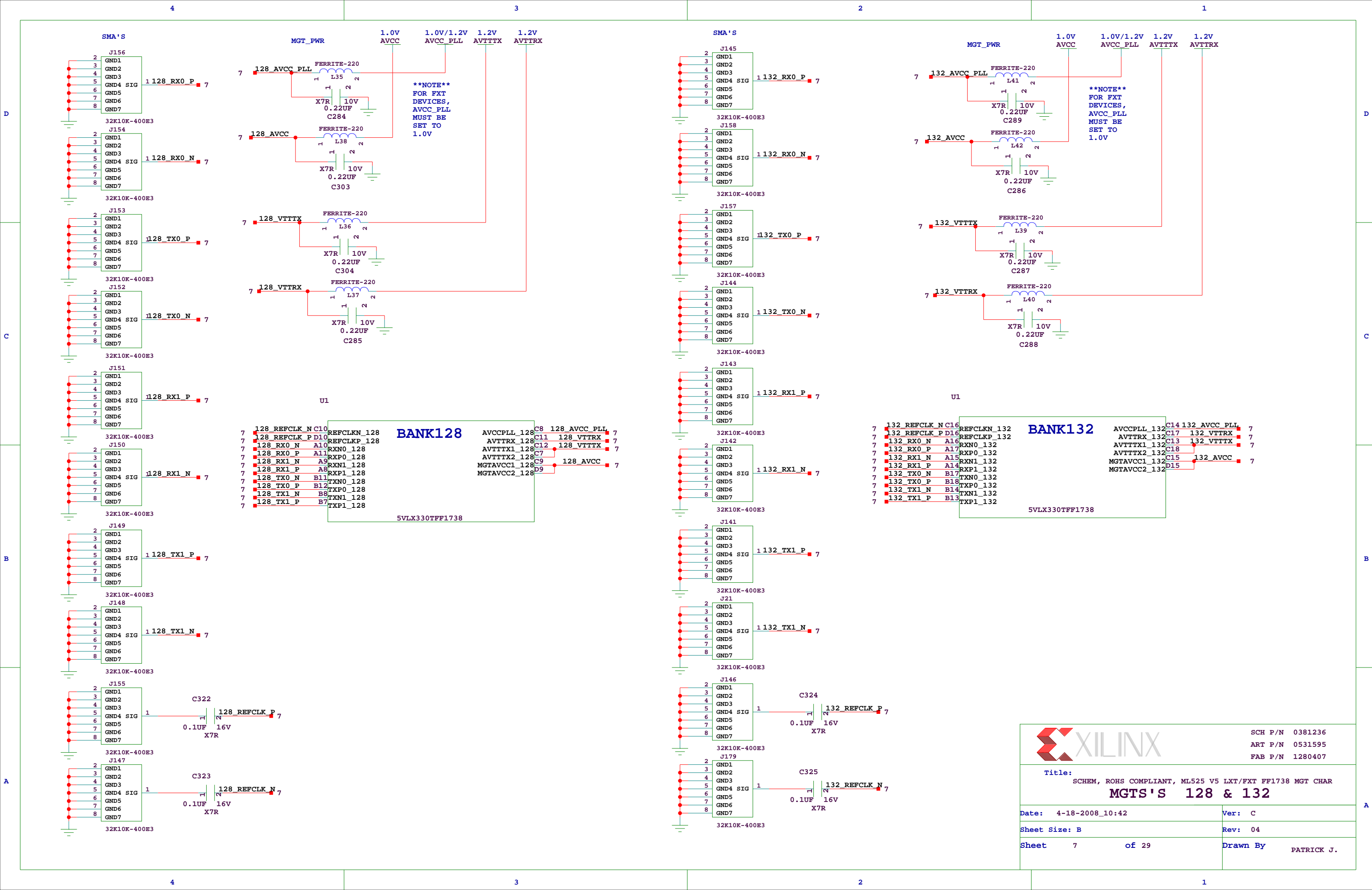
Date: 4-18-2008_10:18	Ver: C
Sheet Size: B	Rev: 04
Sheet 2 of 29	Drawn By PATRICK J.











D

C

B

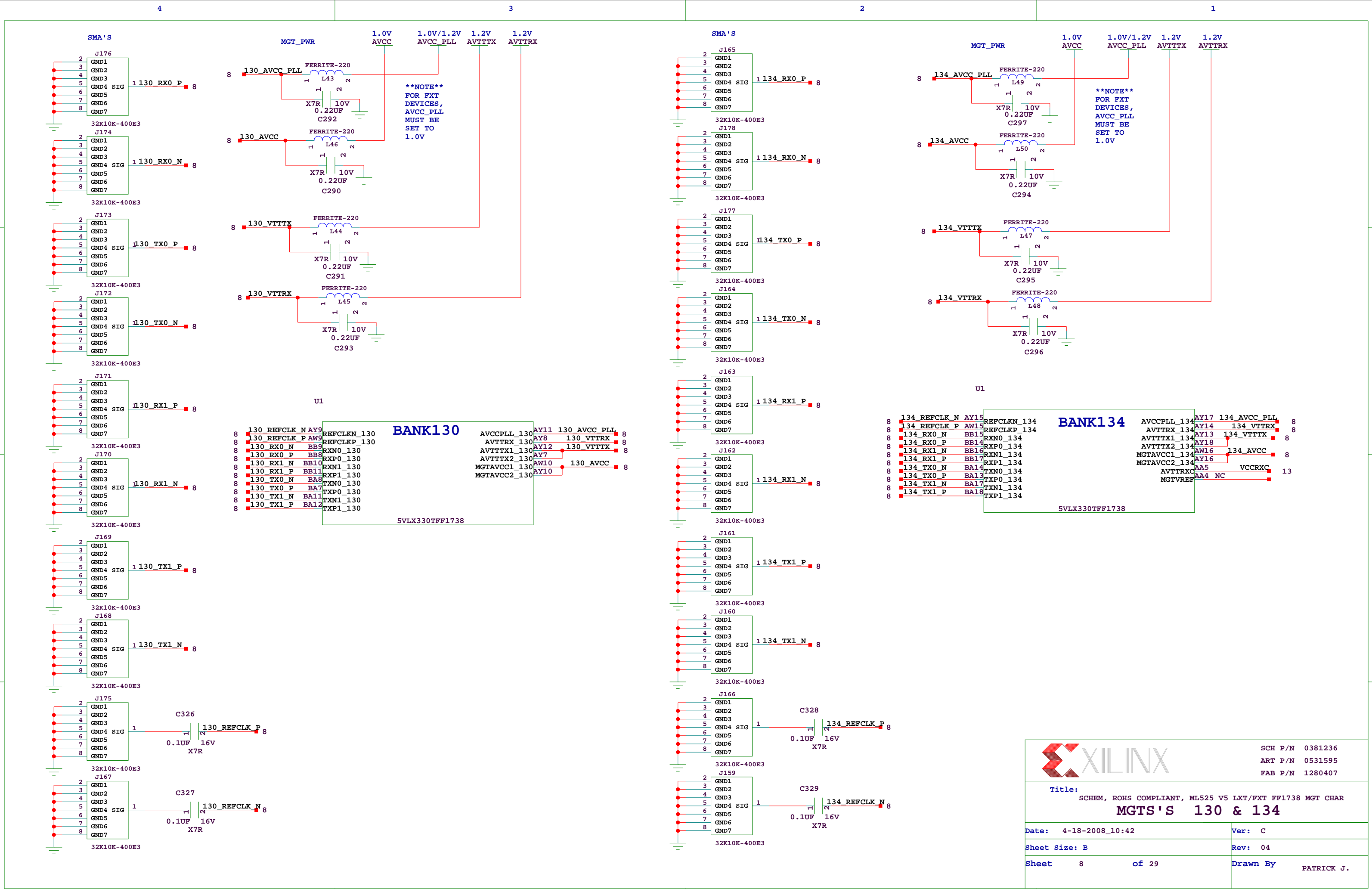
A

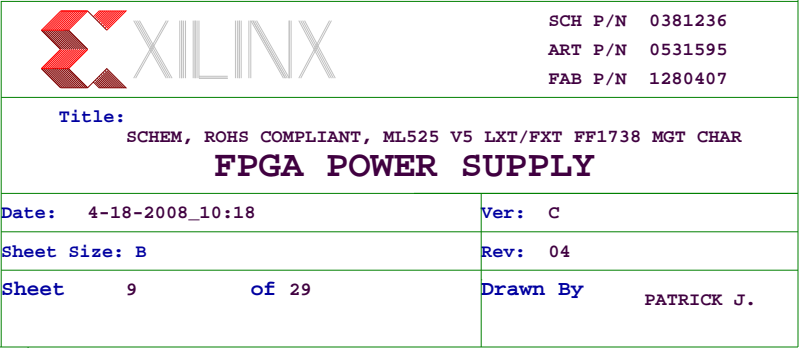
D

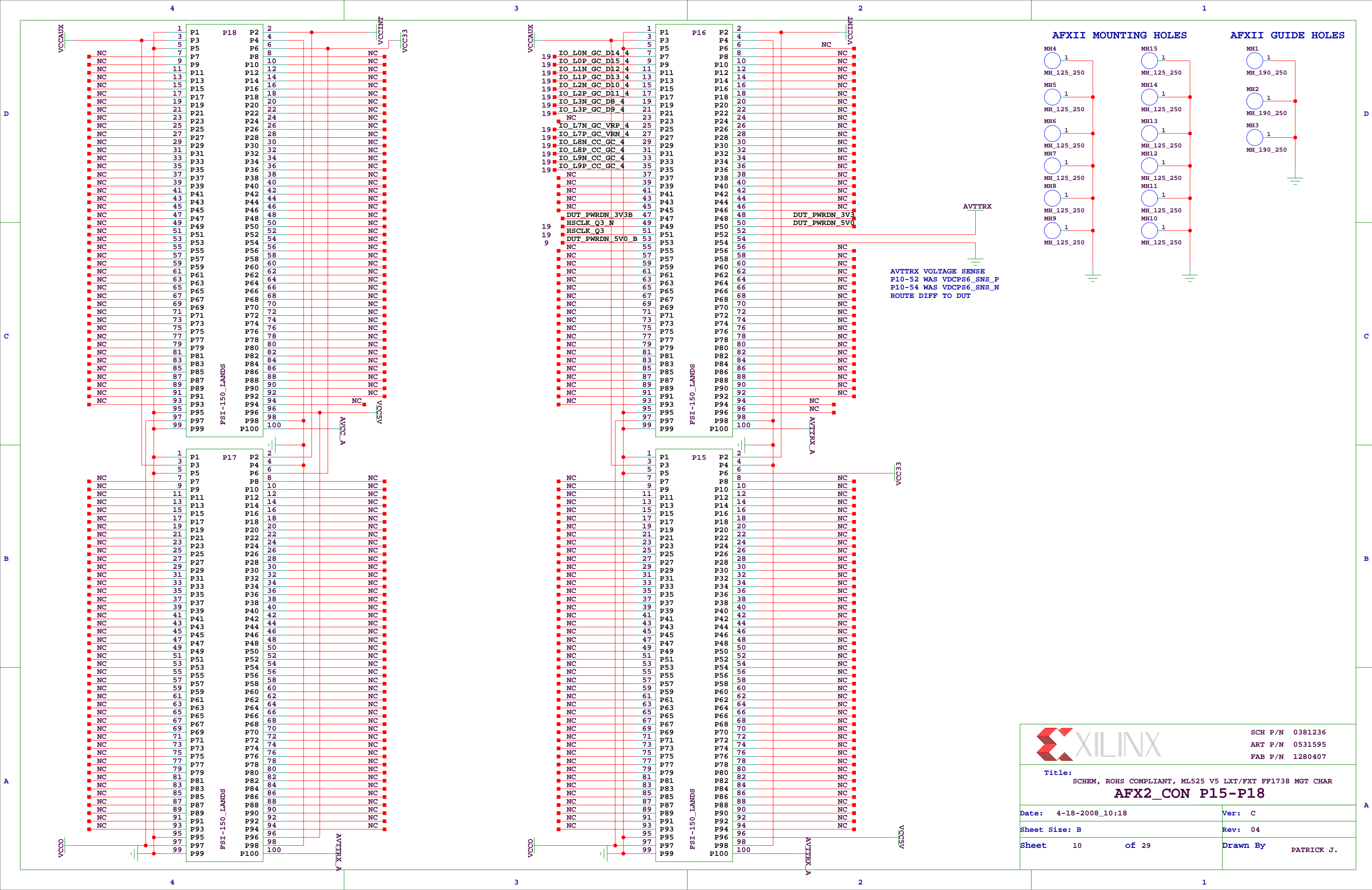
C

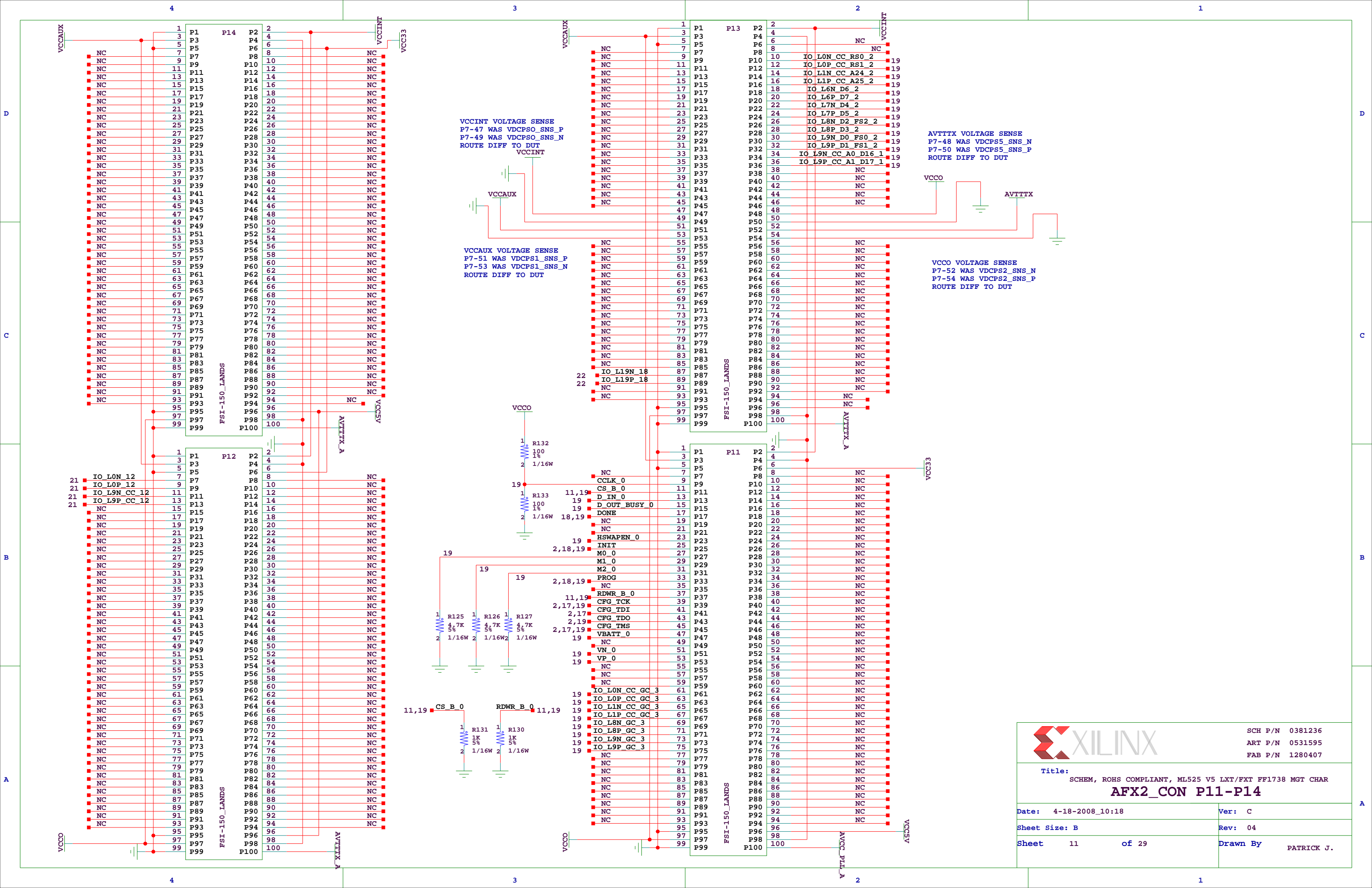
B

A







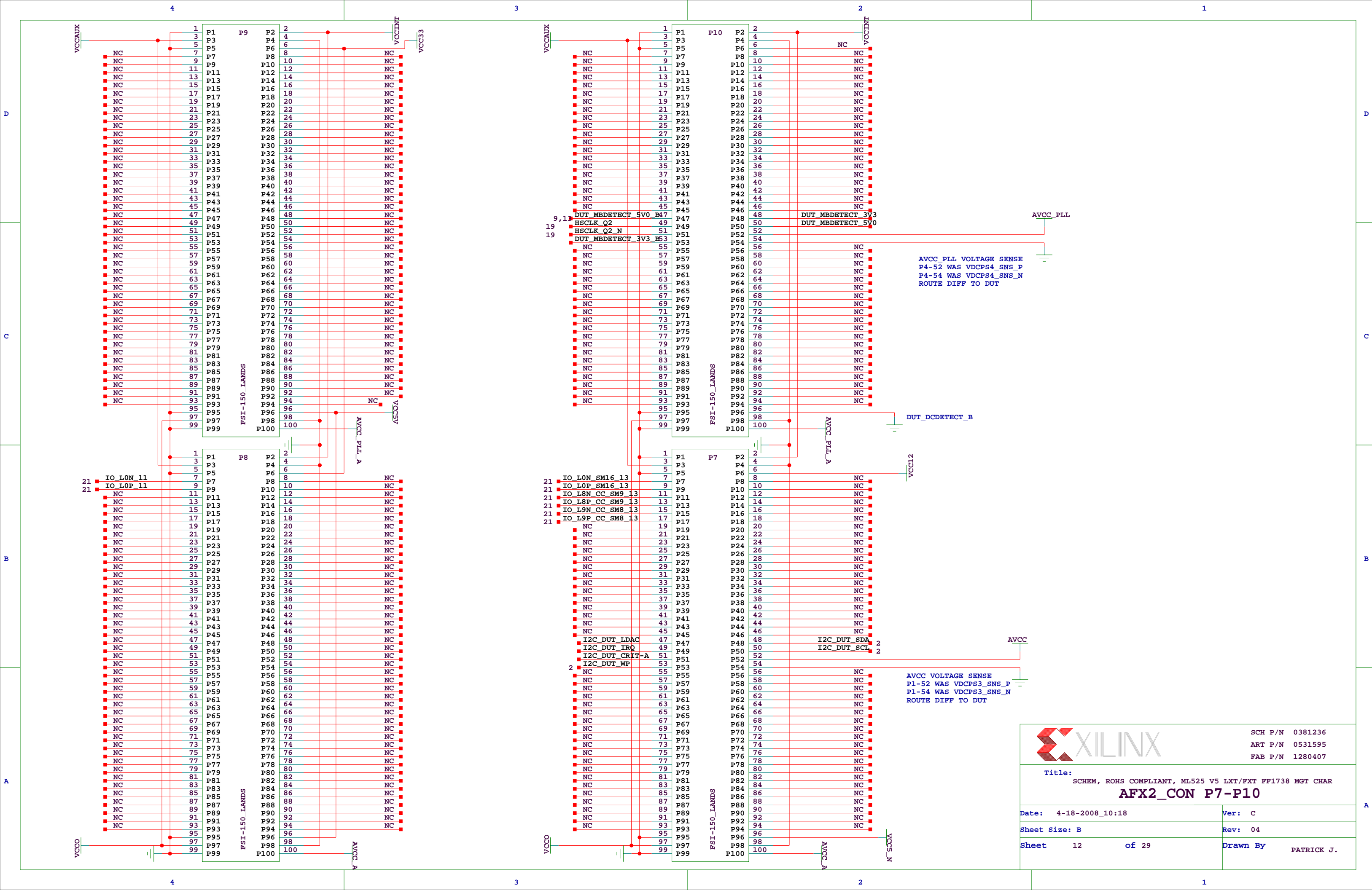


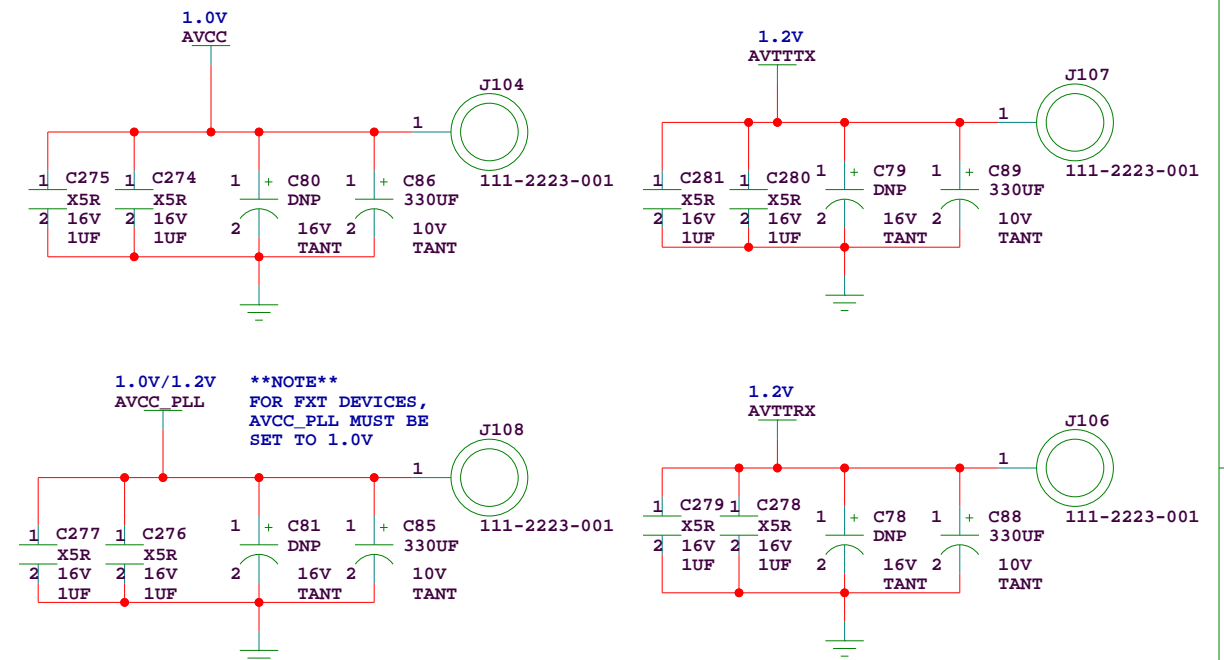
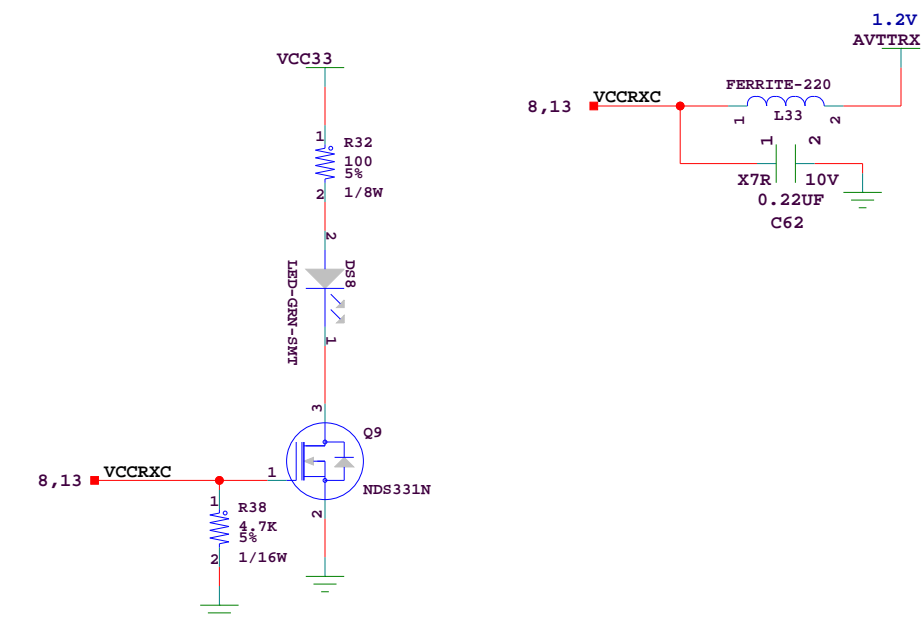
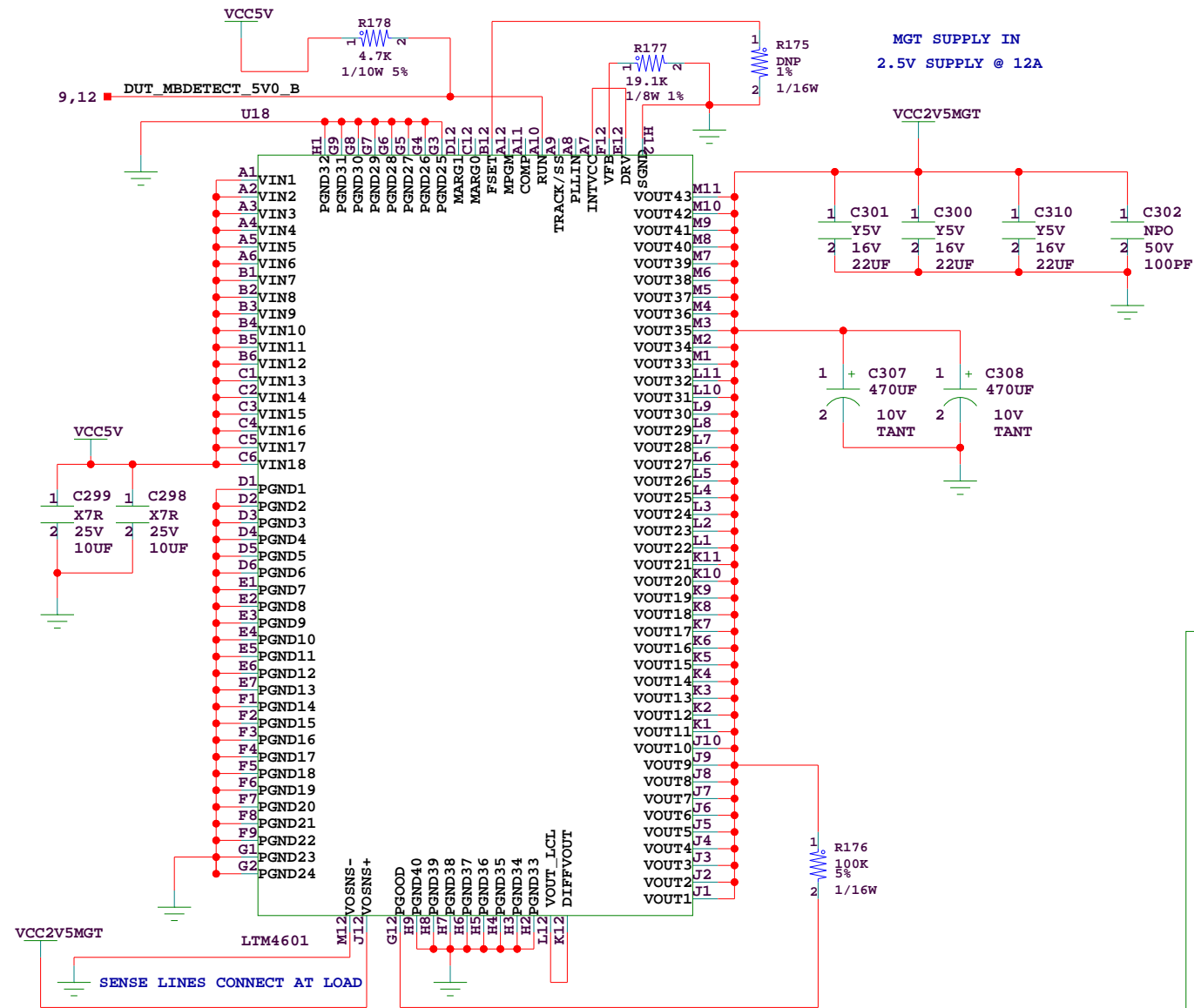
SCH P/N 0381236
ART P/N 0531595
FAB P/N 1280407

Title: SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR

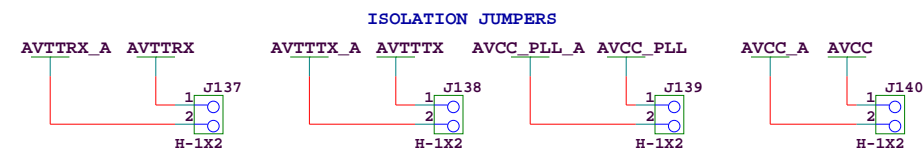
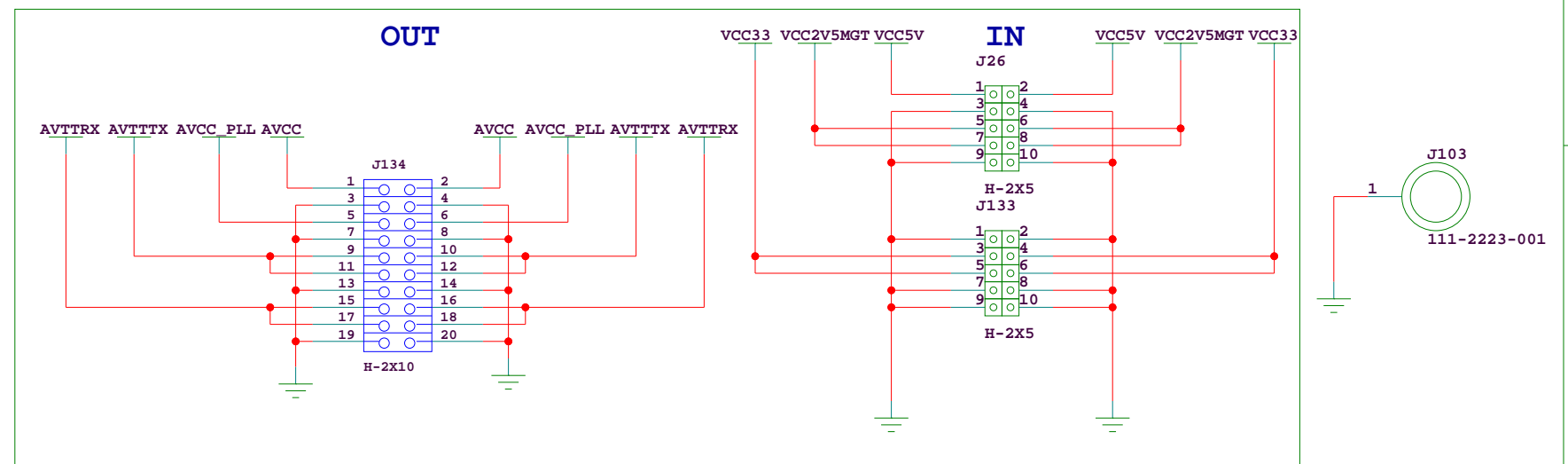
AFX2_CON P11-P14

Date: 4-18-2008_10:18	Ver: C
Sheet Size: B	Rev: 04
Sheet 11 of 29	Drawn By PATRICK J.




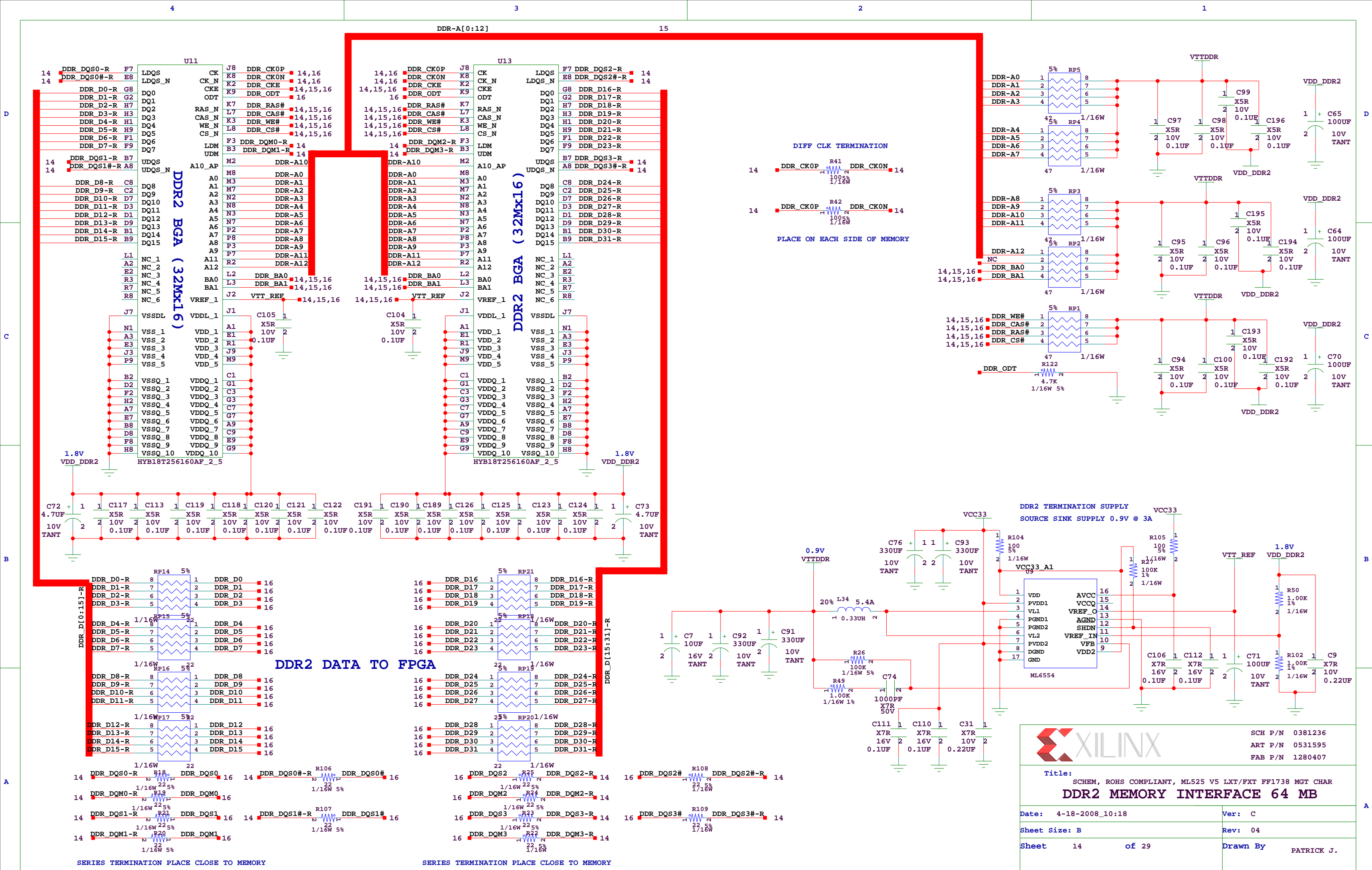


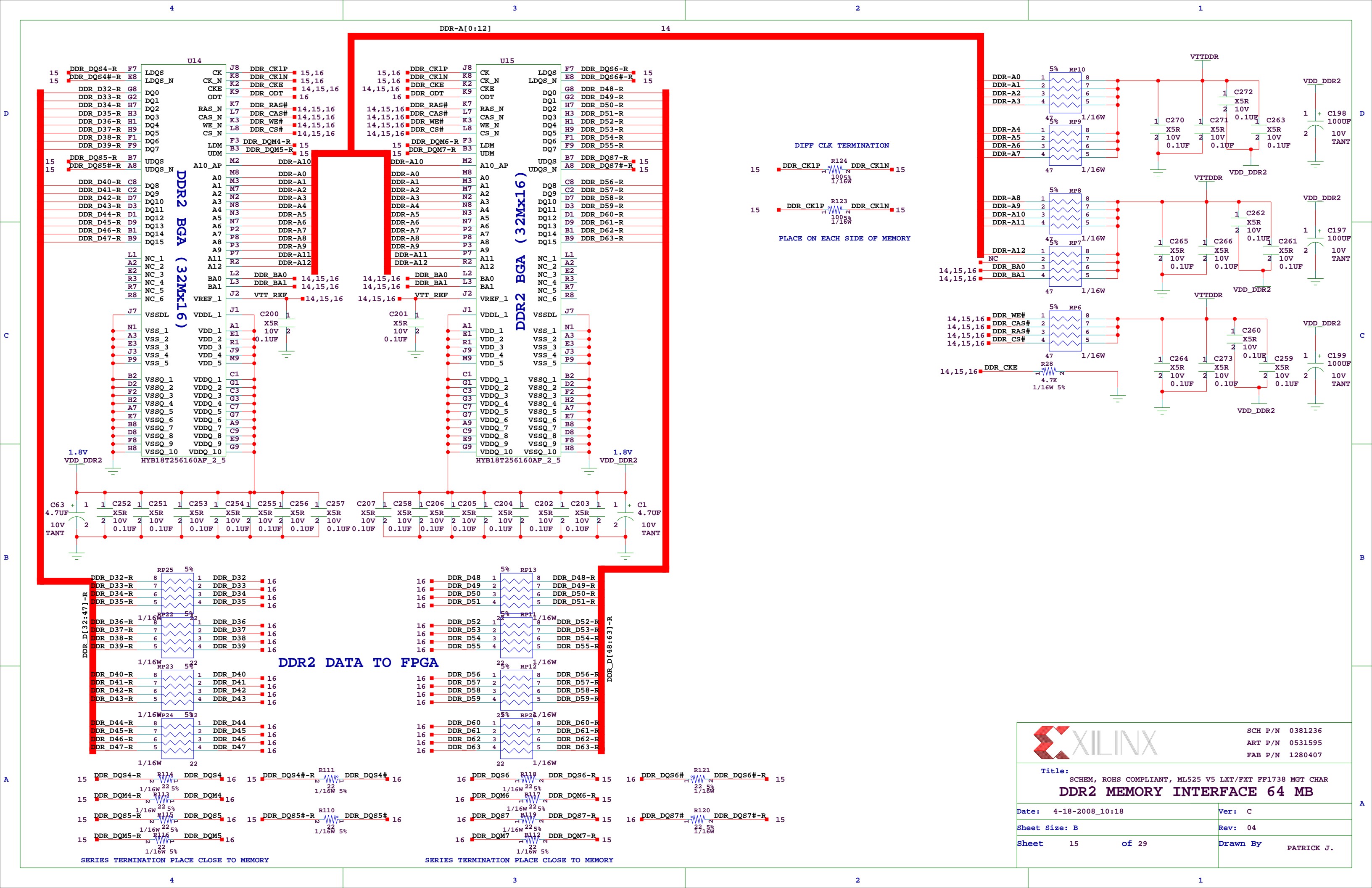
POWER SUPPLY MODULE



JUMPER ON FOR AFXII DCPS POWER FOR MGT'S
JUMPER OFF FOR ON BOARD REGULATION

		SCH P/N 0381236 ART P/N 0531595 FAB P/N 1280407	
Title: SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR MGT'S POWER SUPPLY			
Date: 4-18-2008_13:24		Ver: C	
Sheet Size: B		Rev: 04	
Sheet 13		Drawn By PATRICK J.	

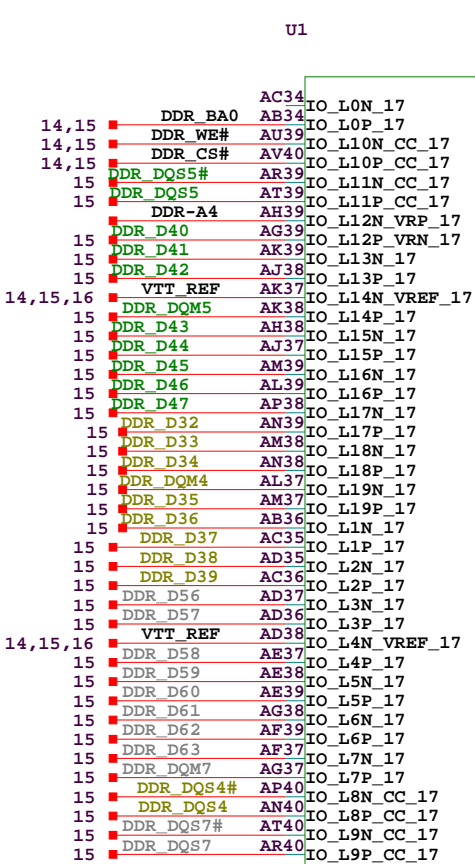
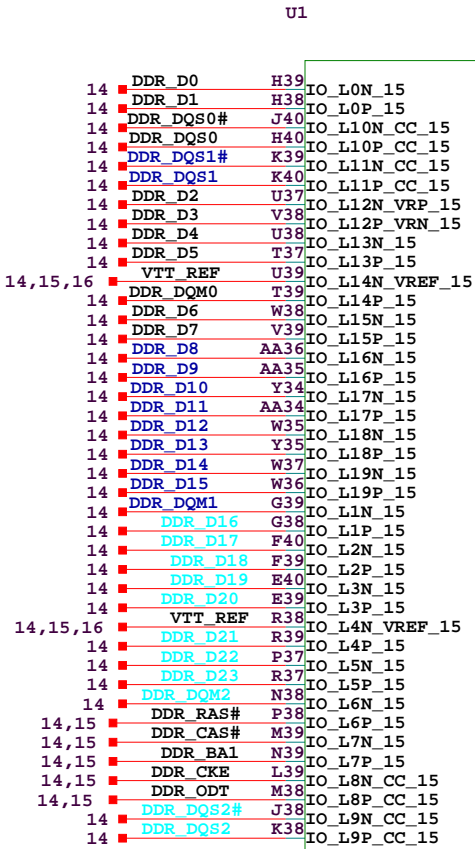
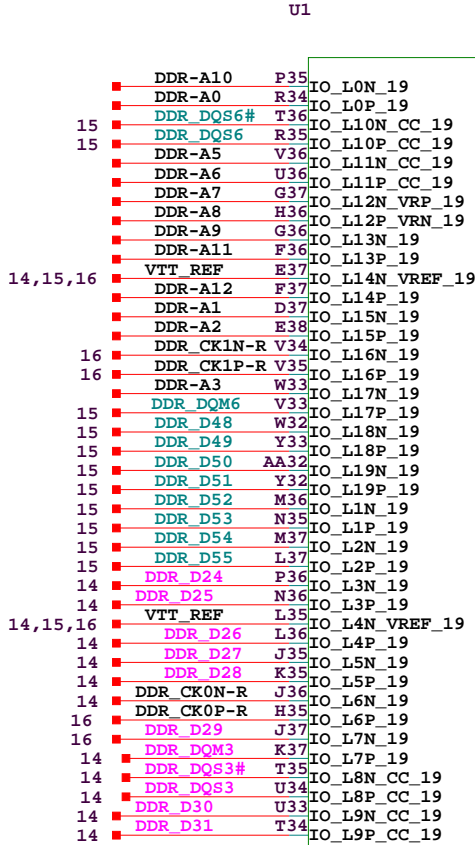
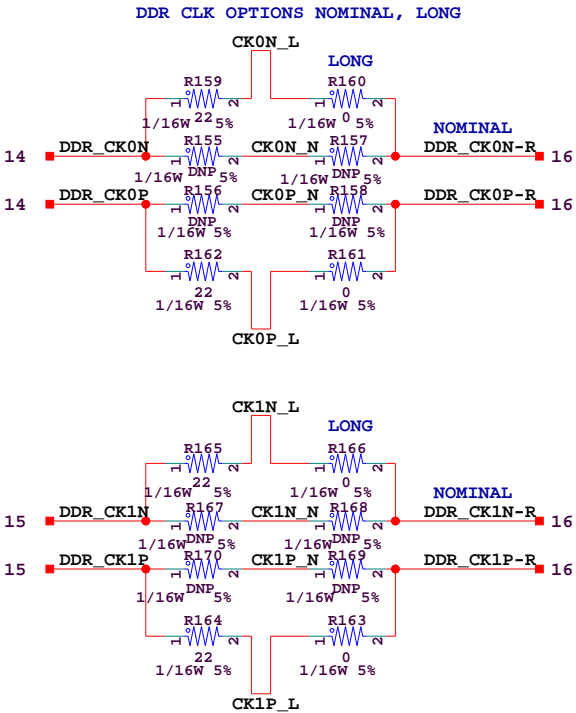




NOTE:

DCI IS NOT REQUIRED
WILL USE EXTERNAL SERIES TERMINATION
VREF PINS ARE TIED TO VEREF TERMINATION

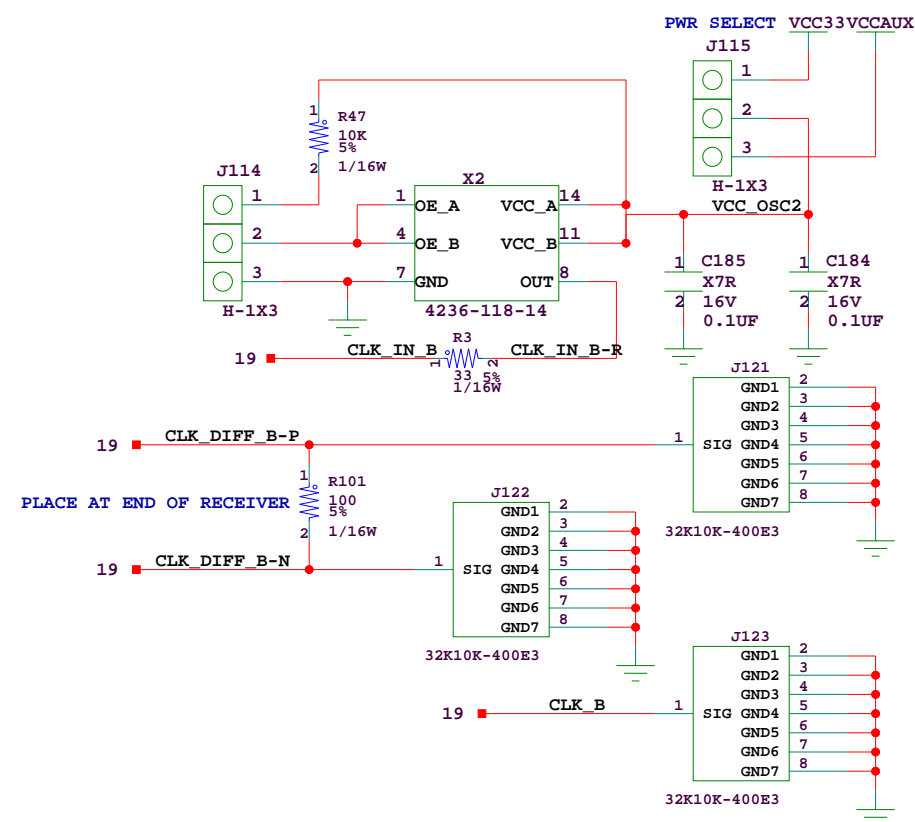
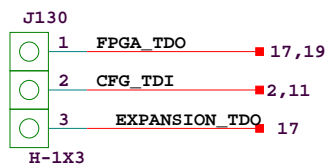
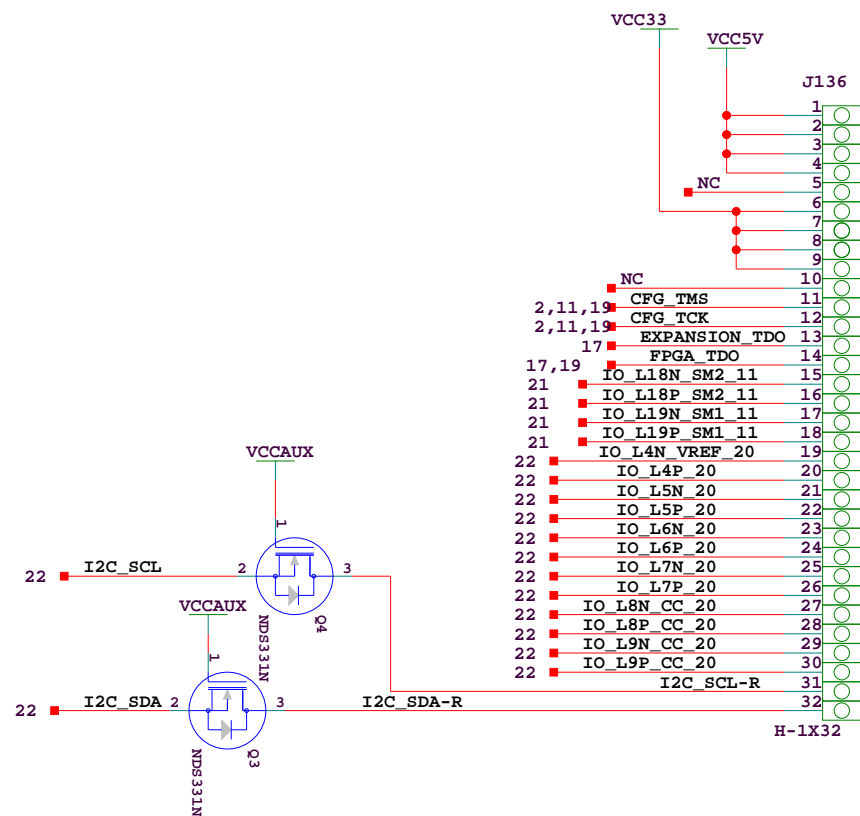
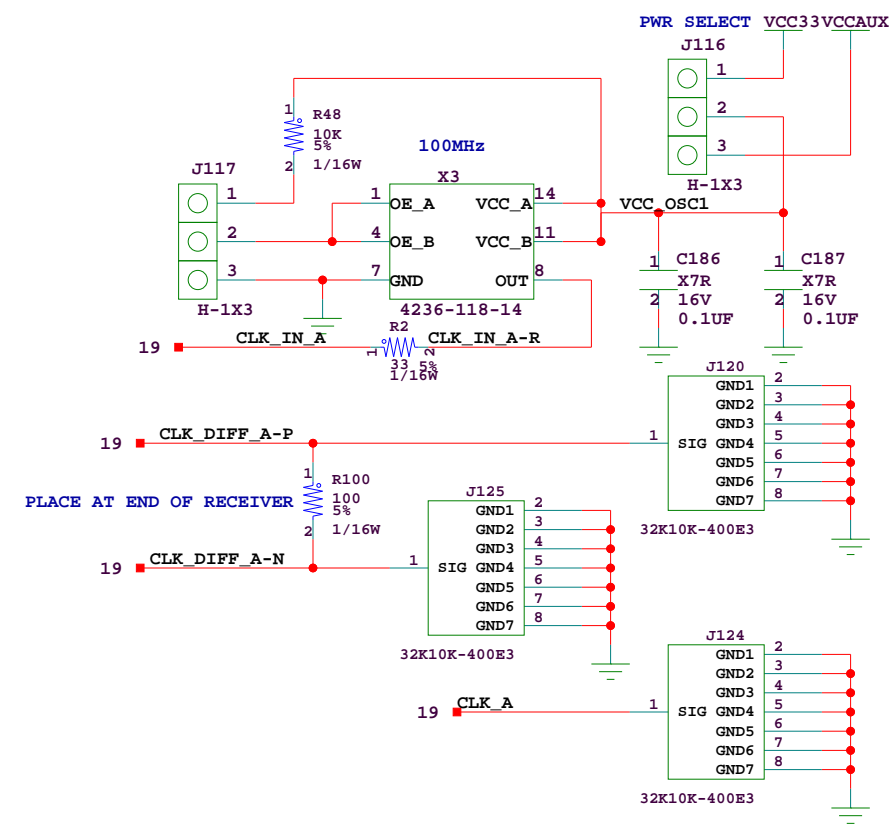
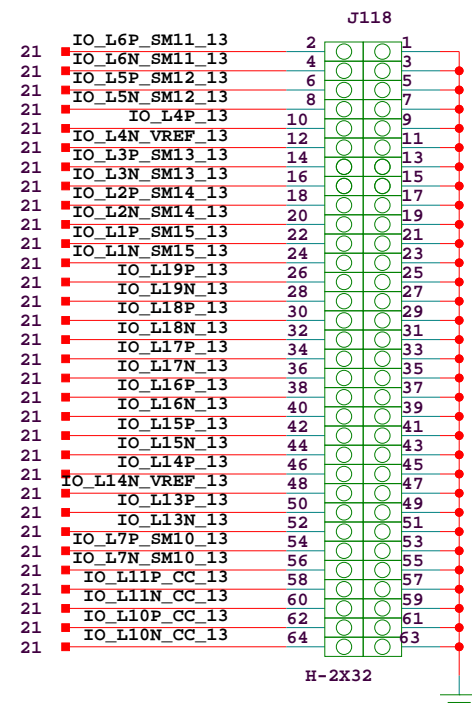
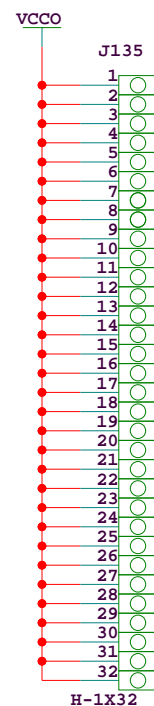
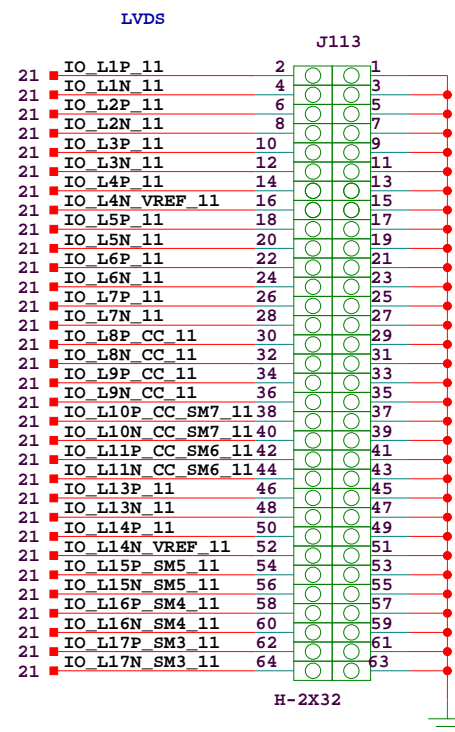
CHECK TO SEE IF CC PINS ARE CAPABLE
OF DRIVING DIFFERENTIAL CLOCKS

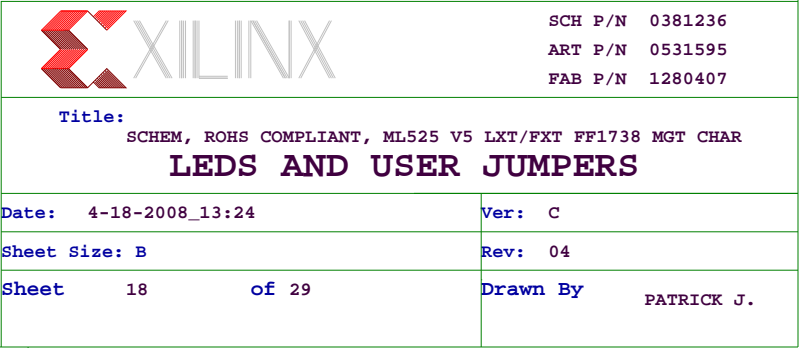


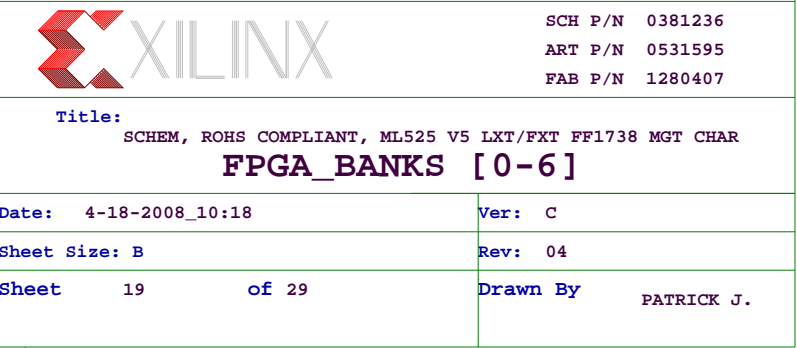
SCH P/N 0381236
ART P/N 0531595
FAB P/N 1280407

Title:
SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR
DDR2 MEMORY INTERFACE FPGA SIDE

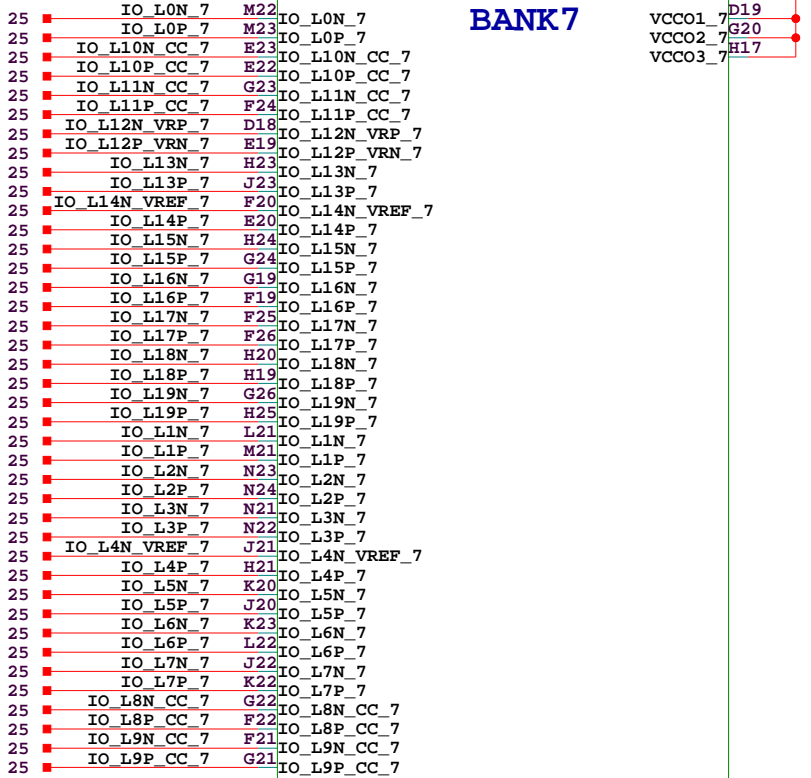
Date: 4-18-2008_10:18 Ver: C
Sheet Size: B Rev: 04
Sheet 16 of 29 Drawn By PATRICK J.





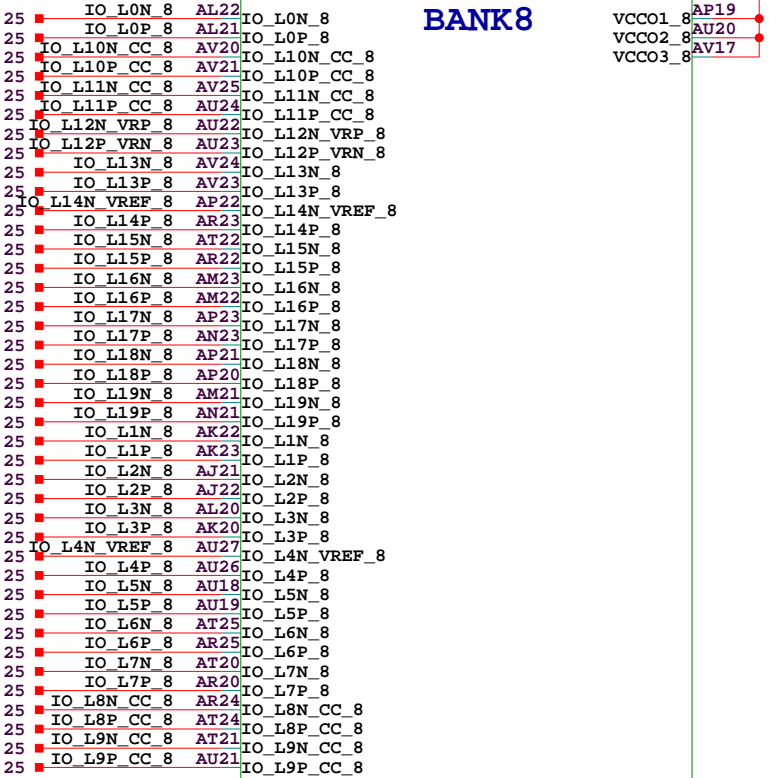


U1



5VLX330TFF1738

U1



5VLX330TFF1738

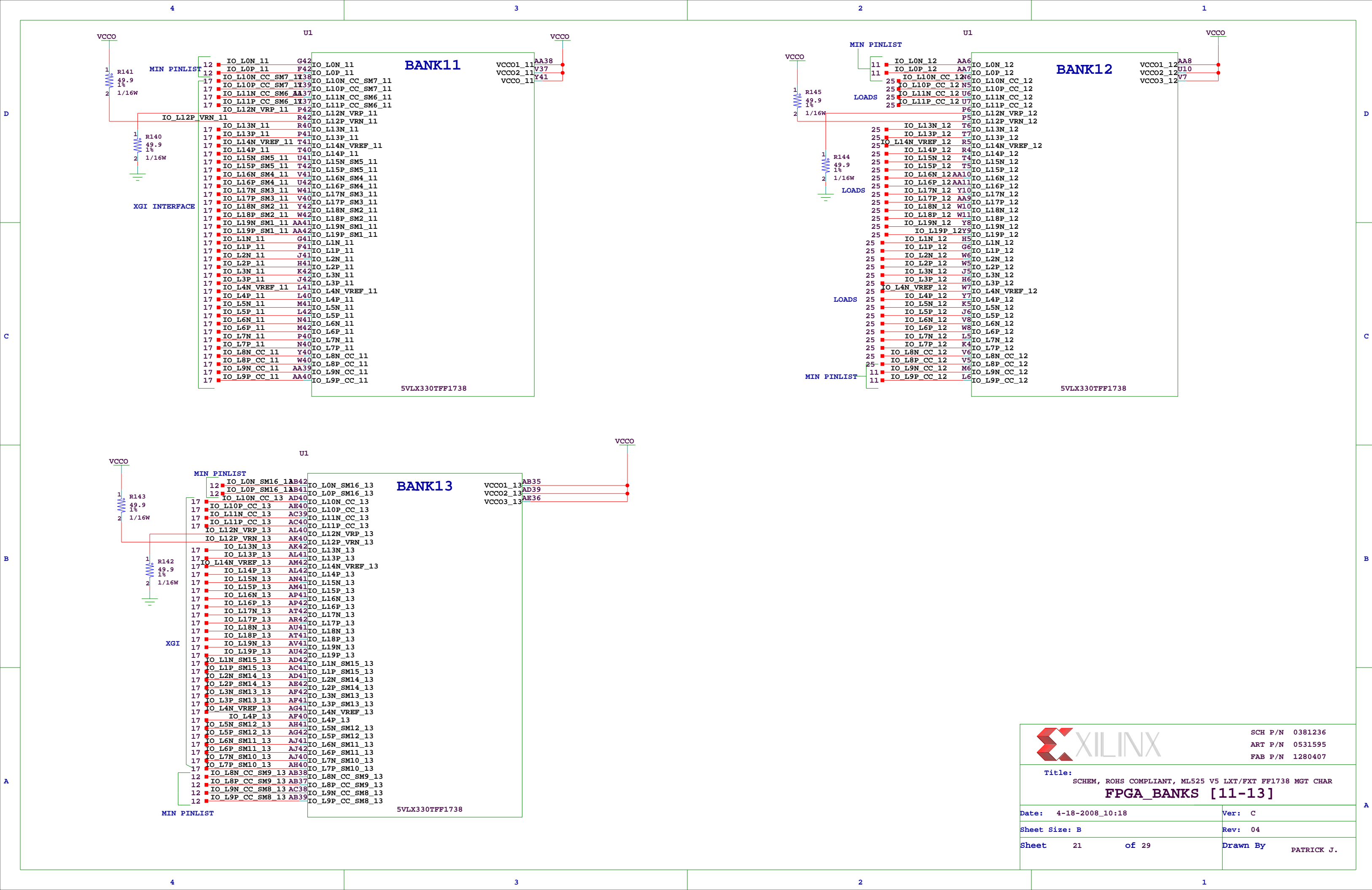


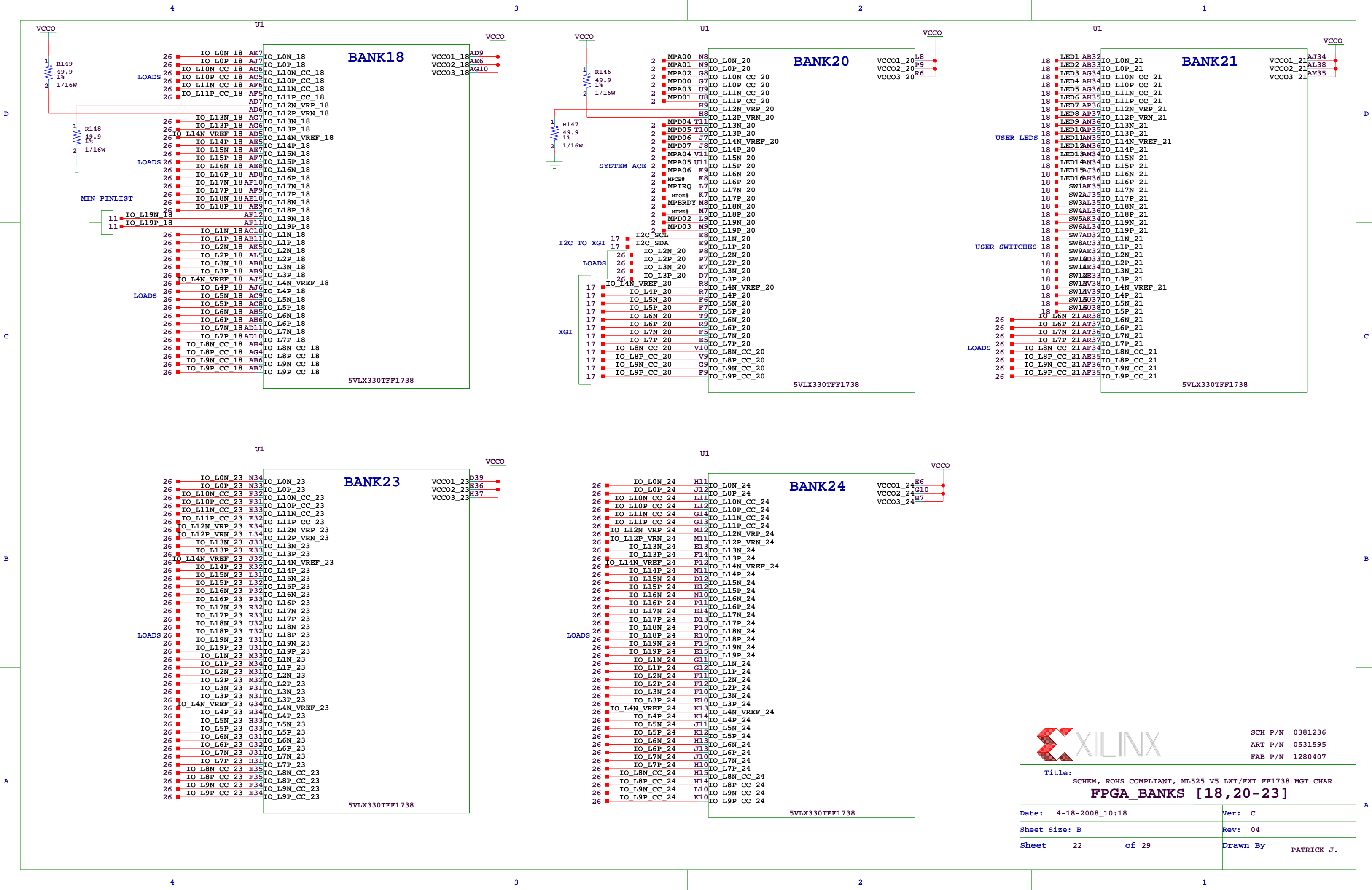
SCH P/N 0381236
ART P/N 0531595
FAB P/N 1280407

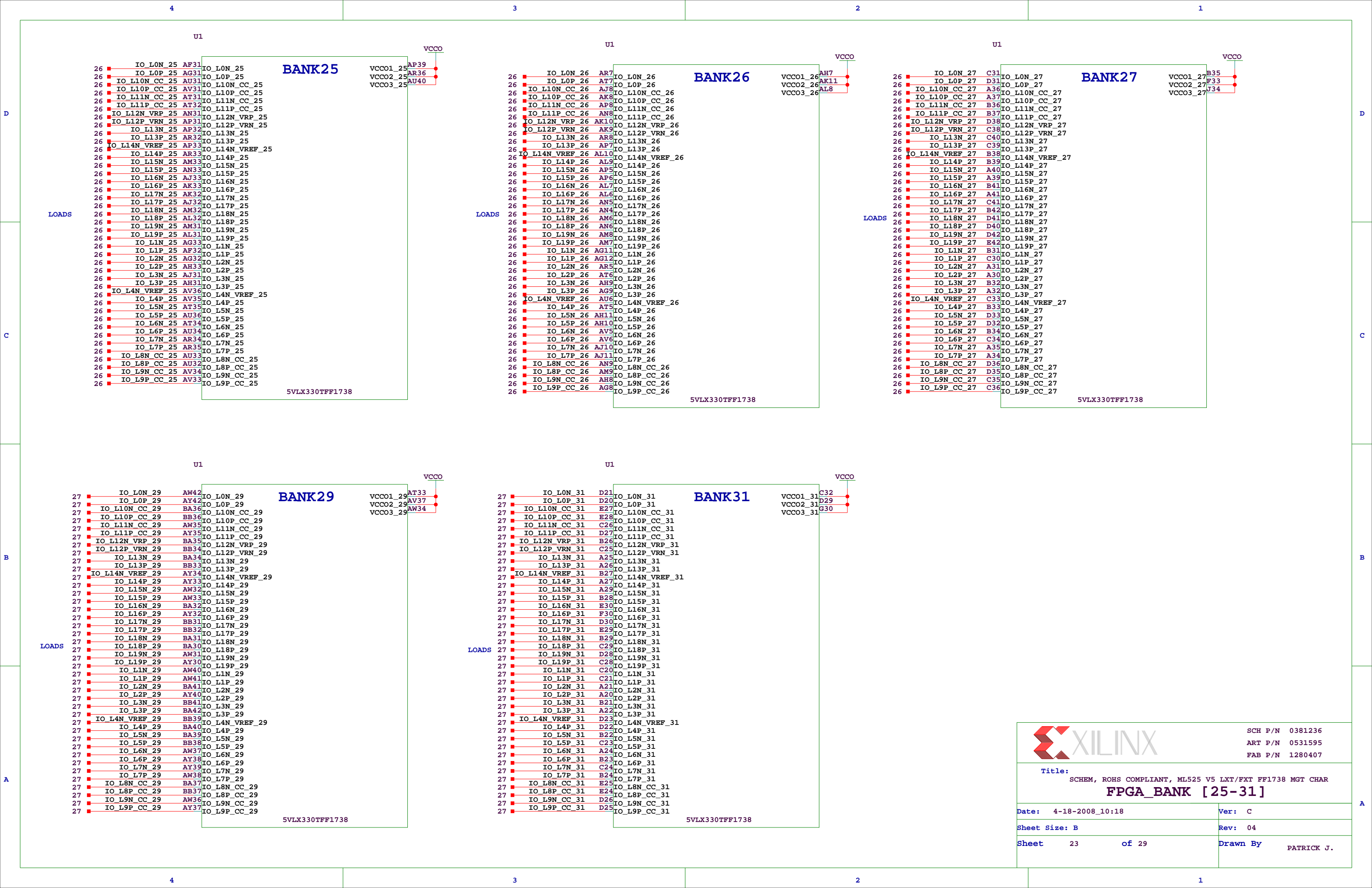
Title:
SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR


FPGA_BANKS [7-8]

Date: 4-18-2008_10:18	Ver: C
Sheet Size: B	Rev: 04
Sheet 20 of 29	Drawn By PATRICK J.









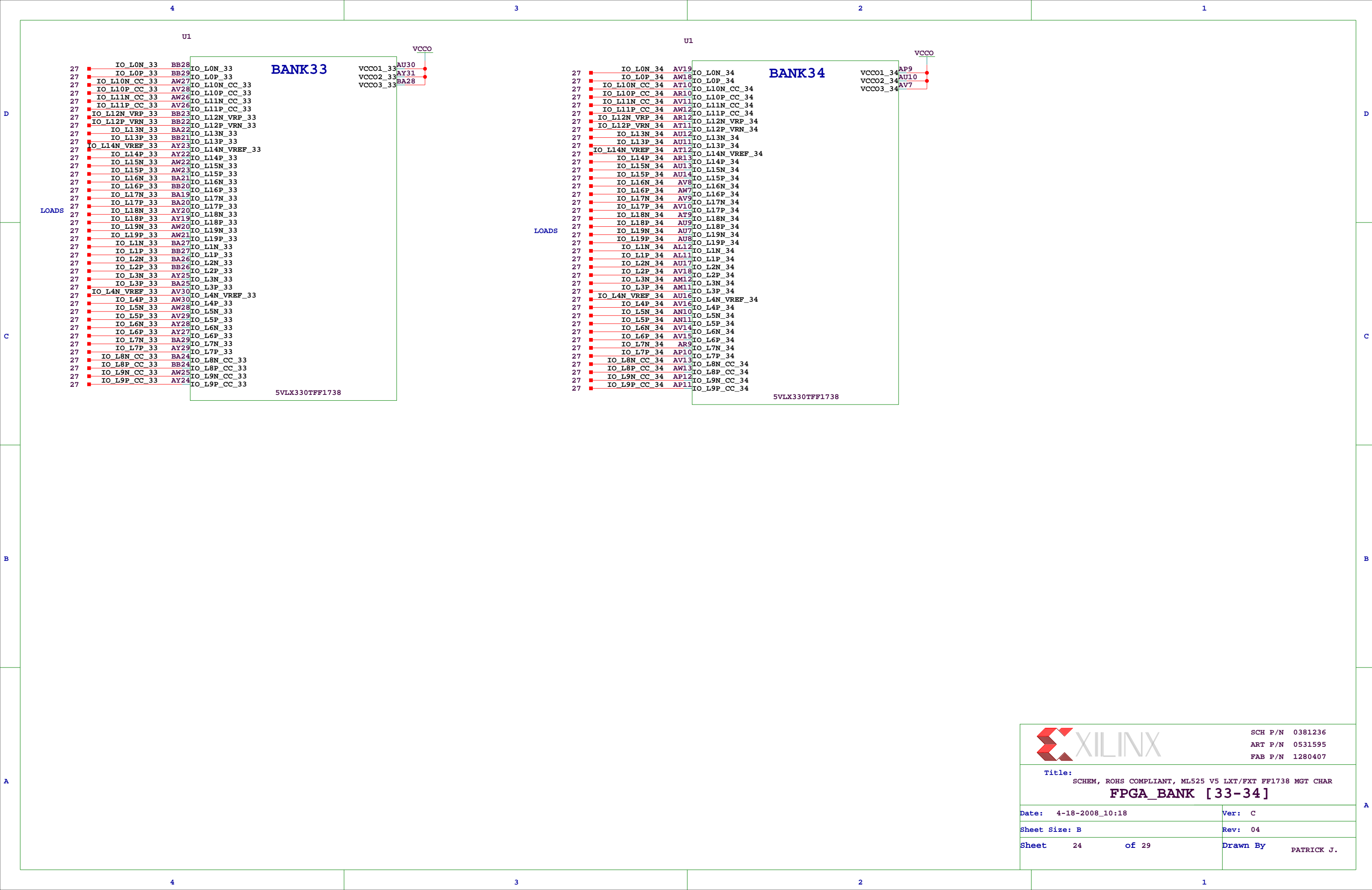
SCH P/N 0381236
ART P/N 0531595
FAB P/N 1280407

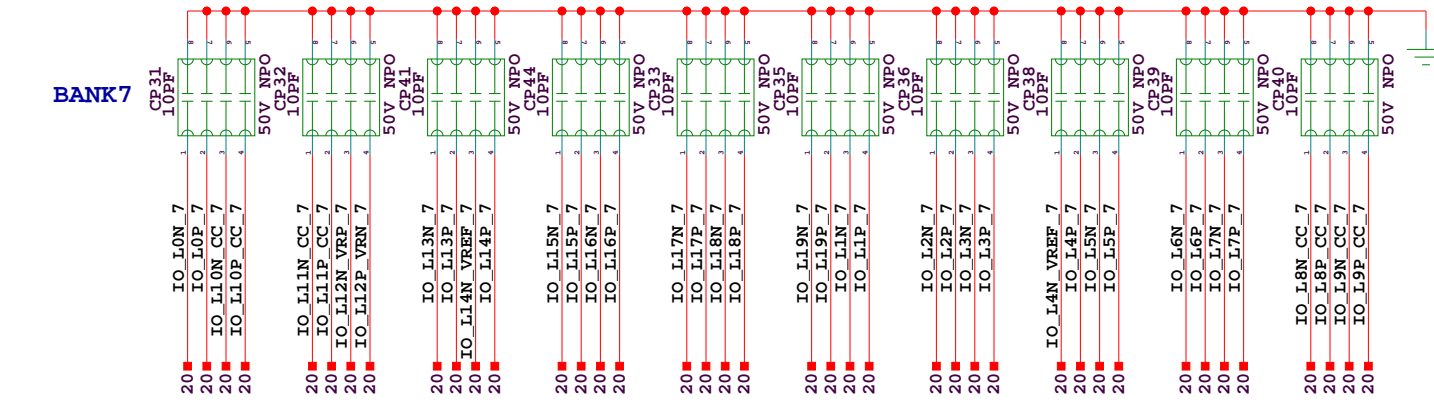
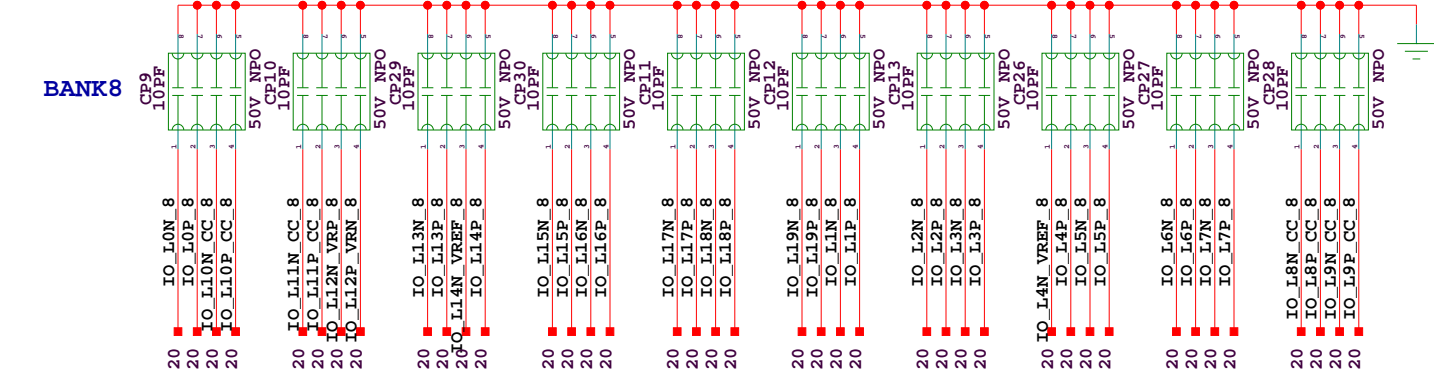
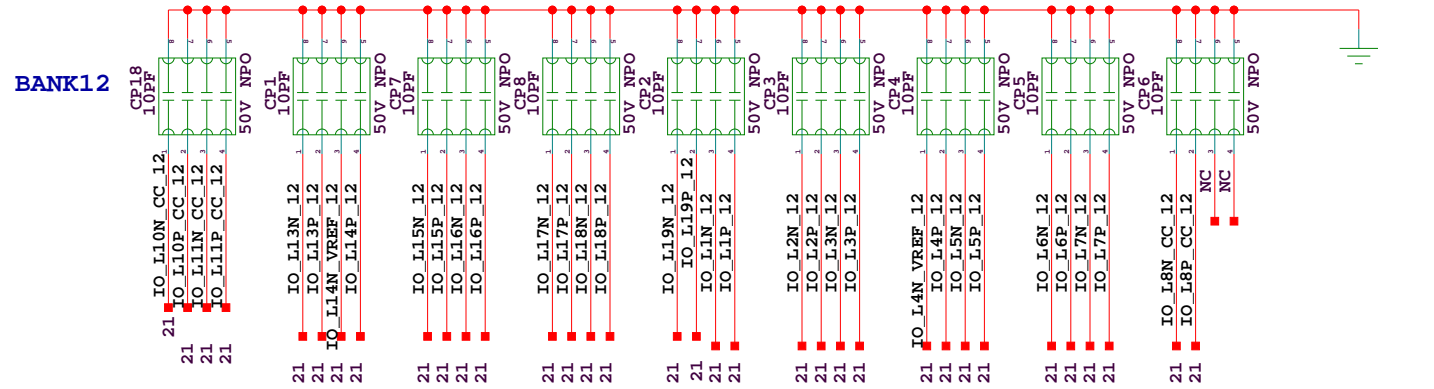
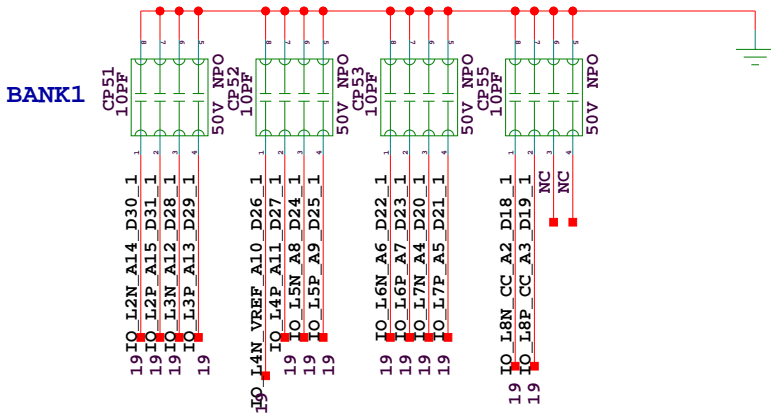
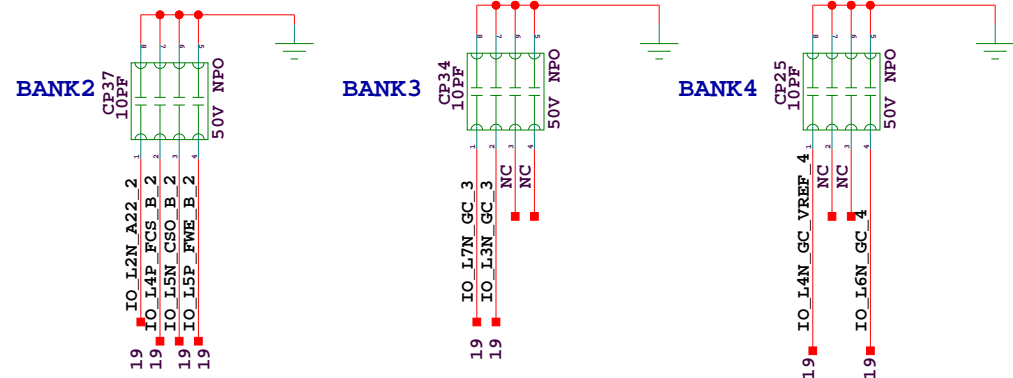
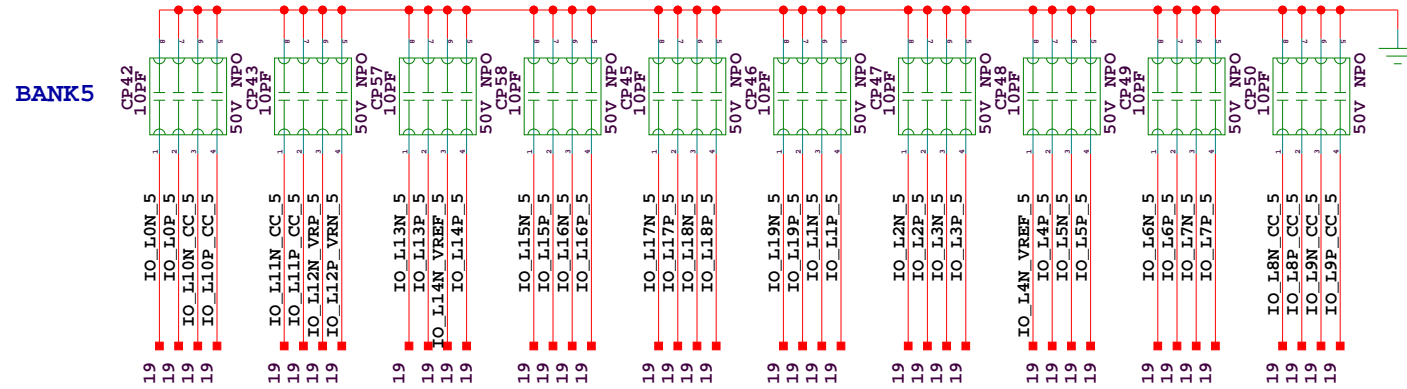
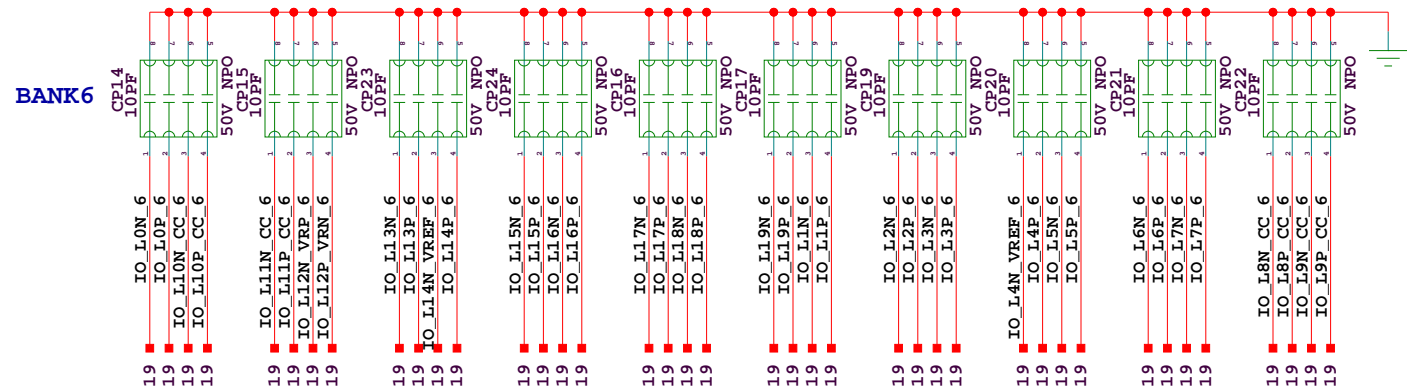
Title:
SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR


FPGA_BANK [25-31]

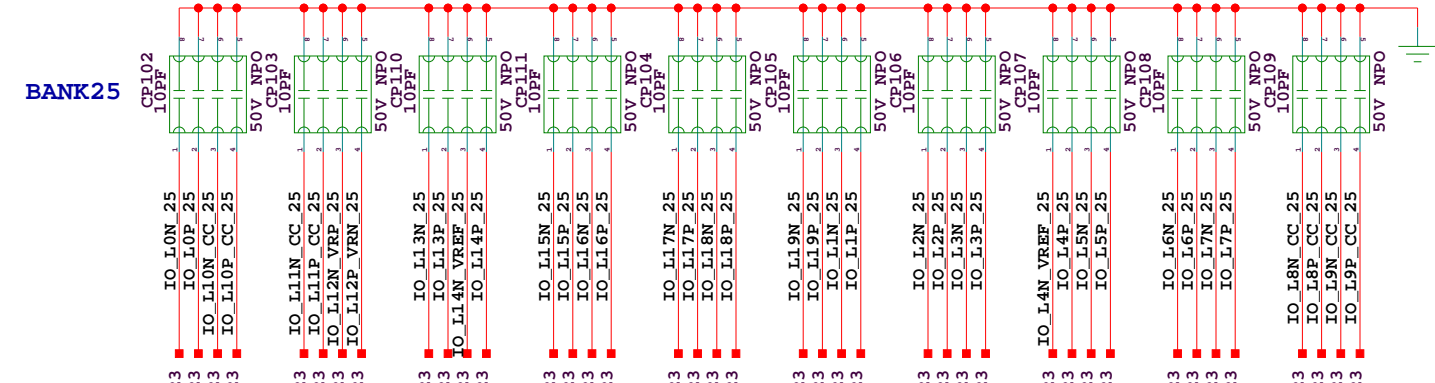
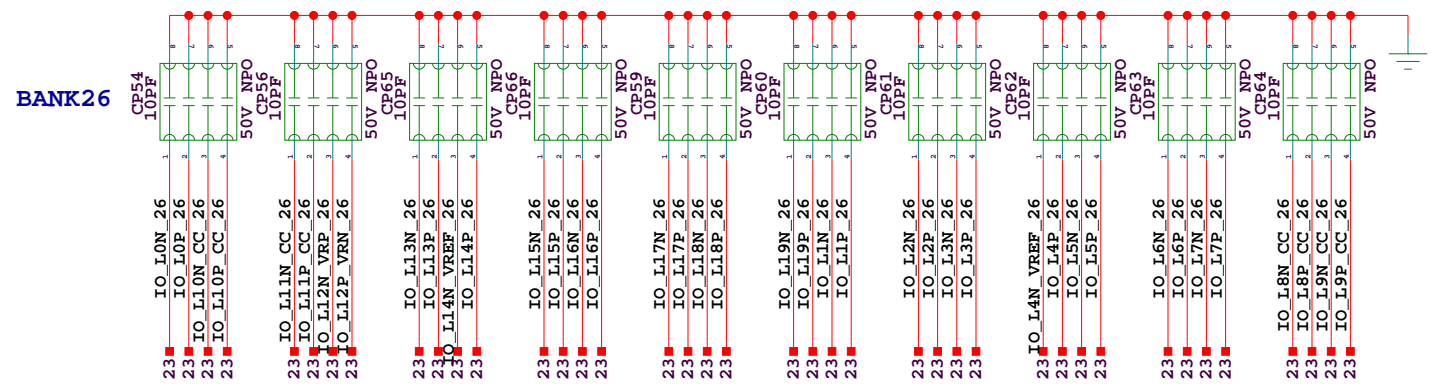
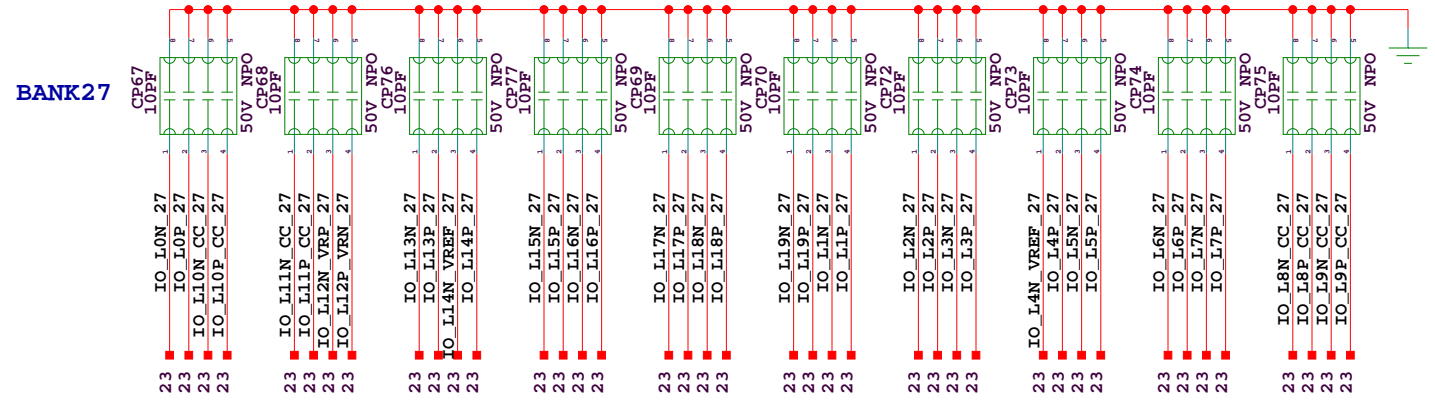
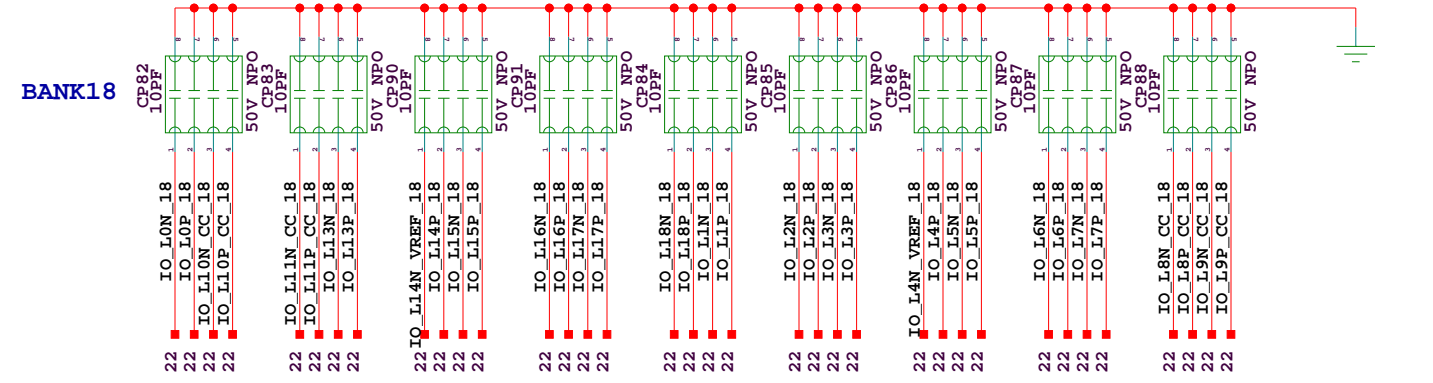
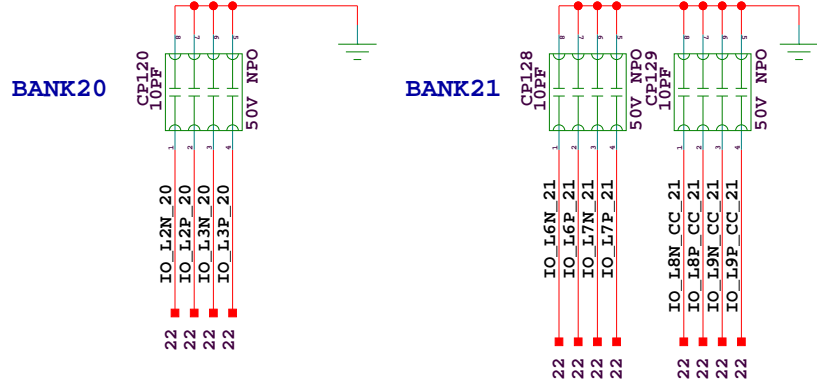
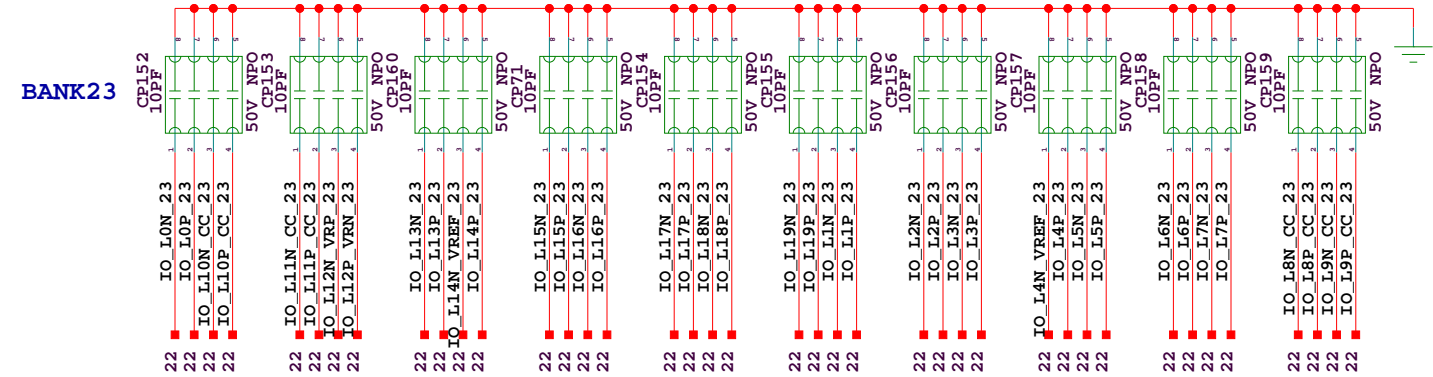
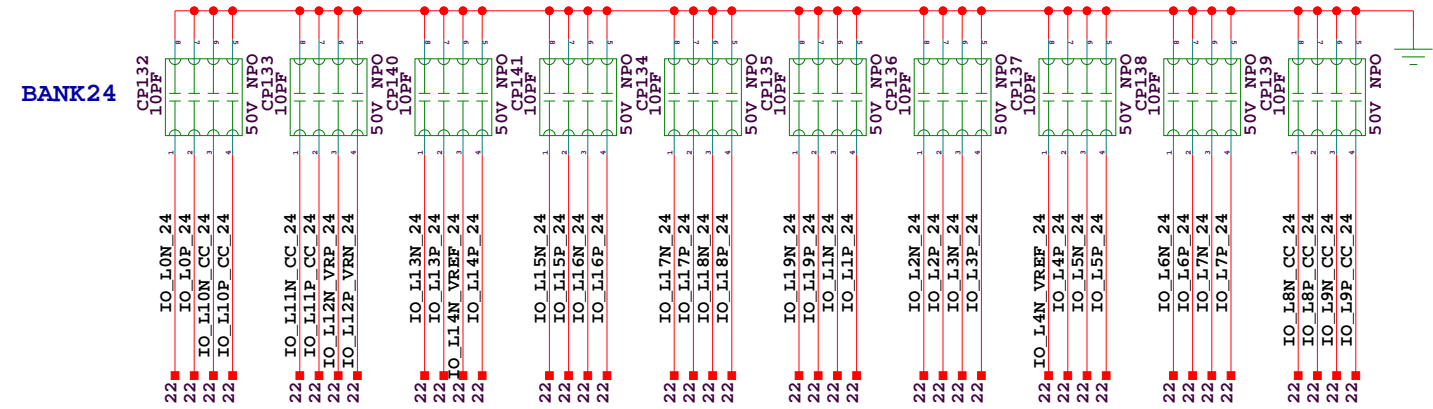
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Sheet Size: B
Sheet 23 of 29


Ver: C
Rev: 04
Drawn By PATRICK J.



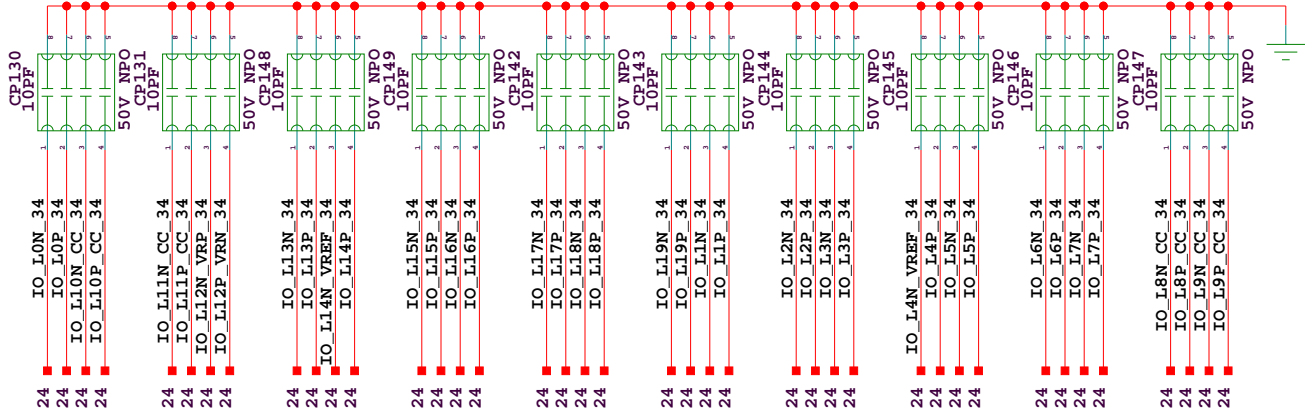


		SCH P/N 0381236 ART P/N 0531595 FAB P/N 1280407	
Title: SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR <h2 style="text-align: center;">CAPACITIVE LOADS</h2>			
Date: 4-18-2008_10:18		Ver: C	
Sheet Size: B		Rev: 04	
Sheet 25		Drawn By PATRICK J.	

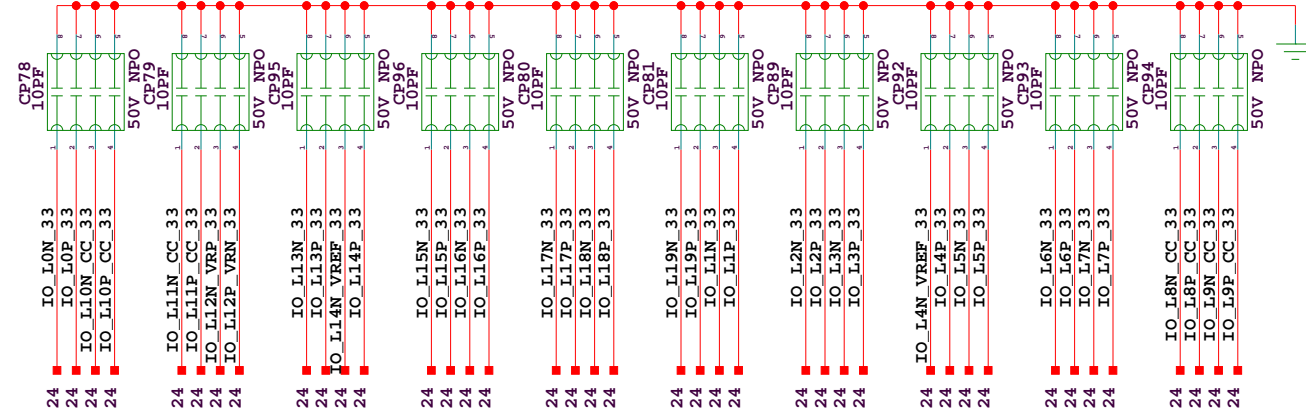


		SCH P/N 0381236 ART P/N 0531595 FAB P/N 1280407
Title: SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR <h2 style="text-align: center;">CAPACITIVE LOADS</h2>		
Date: 4-18-2008_10:18		Ver: C
Sheet Size: B		Rev: 04
Sheet 26 of 29		Drawn By PATRICK J.

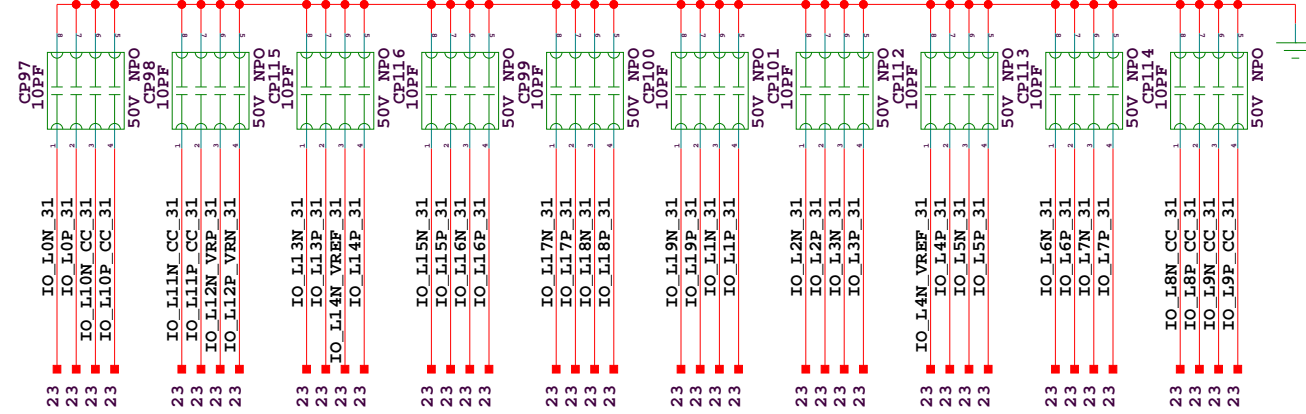
BANK34



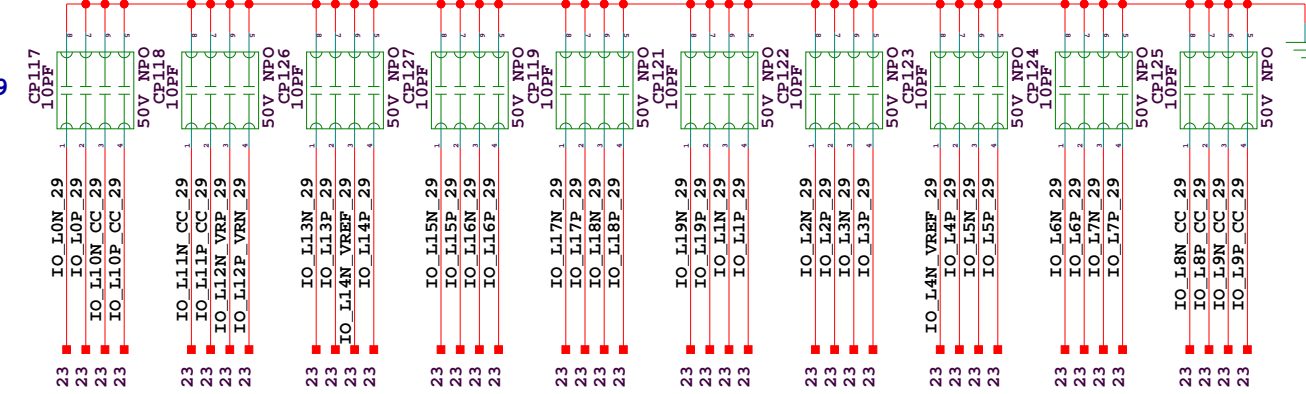
BANK33



BANK31



BANK29



SCH P/N 0381236
ART P/N 0531595
FAB P/N 1280407

Title:
SCHEM, ROHS COMPLIANT, ML525 V5 LXT/FXT FF1738 MGT CHAR

CAPACITIVE LOADS

Date: 4-18-2008_10:18	Ver: C
Sheet Size: B	Rev: 04
Sheet 27 of 29	Drawn By PATRICK J.

