This document describes known issues for the SP605 evaluation board.

General Issues

A summary of known issues related to Xilinx tools, IP, and other issues related to the SP605 Evaluation Kit is provided by AR #33839, SP605 Known Issues and Release Notes Master Answer Record.

Boards having assembly number 0431534 are affected by the silicon errata associated with Spartan-6® FPGA LXT CES devices. See EN118, Spartan-6 FPGA LX45T CES Errata.

All revision D SP605 evaluation board assemblies are modified to comply with the VCCINT requirement defined in EN118 under “Operating Conditions Required when Using I/O Delay Variable Mode.” The VCCINT regulator has been adjusted to provide an output of 1.25V instead of the nominal 1.2V by changing the firmware for the TI programmable power device.

A subset of SP605 evaluation board assemblies exceed the Spartan-6 FPGA MGTAVCC voltage rail maximum by 0.18 V. See AR #34093, SP605 Change Two Resistor Network Values on the MGT_AVCC Rail for details.

PCB Issues

There are no known issues associated with the SP605 printed circuit board (PCB).
Revision History

The following table shows the revision history for this document.

<table>
<thead>
<tr>
<th>Date</th>
<th>Version</th>
<th>Description of Revisions</th>
</tr>
</thead>
<tbody>
<tr>
<td>10/20/09</td>
<td>1.0</td>
<td>Initial Xilinx release.</td>
</tr>
<tr>
<td>10/03/11</td>
<td>2.0</td>
<td>Converted document to the current template. Revised the use of “SP605 printed circuit board” to be more specific when referring to the “SP605 evaluation kit” and the “SP605 evaluation board assembly.” Added reference to answer record 34903.</td>
</tr>
</tbody>
</table>

Notice of Disclaimer

The information disclosed to you hereunder (the “Materials”) is provided solely for the selection and use of Xilinx products. To the maximum extent permitted by applicable law: (1) Materials are made available “AS IS” and with all faults, Xilinx hereby DISCLAIMS ALL WARRANTIES AND CONDITIONS, EXPRESS, IMPLIED, OR STATUTORY, INCLUDING BUT NOT LIMITED TO WARRANTIES OF MERCHANTABILITY, NON-INFRINGEMENT, OR FITNESS FOR ANY PARTICULAR PURPOSE; and (2) Xilinx shall not be liable (whether in contract or tort, including negligence, or under any other theory of liability) for any loss or damage of any kind or nature related to, arising under, or in connection with, the Materials (including your use of the Materials), including for any direct, indirect, special, incidental, or consequential loss or damage (including loss of data, profits, goodwill, or any type of loss or damage suffered as a result of any action brought by a third party) even if such damage or loss was reasonably foreseeable or Xilinx had been advised of the possibility of the same. Xilinx assumes no obligation to correct any errors contained in the Materials, or to advise you of any corrections or update. You may not reproduce, modify, distribute, or publicly display the Materials without prior written consent. Certain products are subject to the terms and conditions of the Limited Warranties which can be viewed at [http://www.xilinx.com/warranty.htm](http://www.xilinx.com/warranty.htm); IP cores may be subject to warranty and support terms contained in a license issued to you by Xilinx. Xilinx products are not designed or intended to be fail-safe or for use in any application requiring fail-safe performance; you assume sole risk and liability for use of Xilinx products in Critical Applications: [http://www.xilinx.com/warranty.htm#critapps](http://www.xilinx.com/warranty.htm#critapps).