

## Overview

The purpose of this notification is to communicate Xilinx will add an additional tray supplier for the Xilinx CS324/CSG324, CS325/CSG325, CSGA324, FTGB196 packages.

## Description

Xilinx is adding an additional tray produced by Daewon for the CS324/CSG324, CS325/CSG325, CSGA324, FTGB196 packages.

The Daewon tray matrices are designed to be compatible with the current Kostat shipping tray, and will have the same X/Y tray dimension. However, the Daewon tray are non-stackable with the Kostat tray due to a slip lock feature. Customers shall not stack or mix the Daewon trays with Kostat trays.

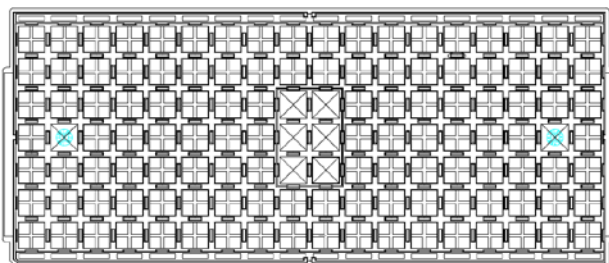
Xilinx will begin using the new shipping tray upon release of this customer notification.

Please reference [Table 1](#) and [Figure 1](#) for further information:

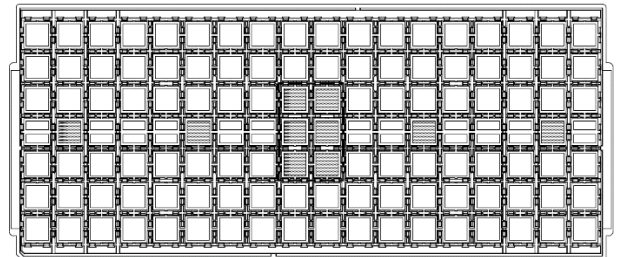
**Table 1: Tray Information**

| Tray Description  | Current Tray            |                | New Tray                |                |
|---|-------------------------|----------------|-------------------------|----------------|
|   | Kostat Tray Part Number | Xilinx Drawing | Daewon Tray Part Number | Xilinx Drawing |
| 15x15 PBGAJEDED LOW PROFILE TRAY (126 pockets) for CS324/CSG324, CS325/CSG325, CSGA324, FTGB196 pkg | KS-8808-02              | SIT0127        | 1WG-1515-E19 A          | SIT0176        |

**Figure 1: Tray Details**



Current Tray: KS-8808-02  
Vendor: Kostat



New Tray: 1GW-1515-E19 A  
Vendor: Daewon

## Products Affected

This change affects all CS324/CSG324, CS325/CSG325, CSGA324, FTGB196 packages, in any temperature or speed grade offering. Affected devices are included in the following [Table 2](#):

Table 2: Affected Devices

| Device    | Device    | Device   |
|-----------|-----------|----------|
| XA6SLX16  | XA7S6     | XC7A25T  |
| XA6SLX25  | XA7S15    | XC7A35T  |
| XA6SLX25T | XA7S25    | XC7A50T  |
| XA6SLX45  | XA7S50    | XC7A75T  |
| XA6SLX45T | XC6SLX16  | XC7S6    |
| XA6SLX9   | XC6SLX25  | XC7S15   |
| XA7A100T  | XC6SLX25T | XC7S25   |
| XA7A12T   | XC6SLX45  | XC7S50   |
| XA7A15T   | XC6SLX45T | XQ7A100T |
| XA7A25T   | XC6SLX9   | XQ7A50T  |
| XA7A35T   | XC7A100T  |          |
| XA7A50T   | XC7A12T   |          |
| XA7A75T   | XC7A15T   |          |

## Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

**Important Notice:** Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<http://www.xilinx.com/support>). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

## Revision History

The following table shows the revision history for this document.

| Date       | Version | Revision         |
|------------|---------|------------------|
| 04/16/2018 | 1.0     | Initial release. |

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