



# Mask Set Modification for the XCF08P and XCF16P

XCN06004 (v1.0) March 20, 2006

Product/Process Change Notice

## Overview

This notification describes a mask change to the 8-Mbit and 16-Mbit devices of the Platform Flash™ family of Xilinx In-System Programmable Configuration PROMs manufactured by STMicroelectronics in Catania, Italy.

## Description

A mask set modification for yield optimization will be implemented for the XCF08P and XCF16P devices of the Platform Flash family. This is not a process or technology change. The new die will be form, fit, and functionally compatible with the current devices in production.

## Products Affected

This notification applies only to the following Xilinx part numbers manufactured by STMicroelectronics. Reference the Traceability section for further information on identifying these products.

Table 1: Products Affected

Devices	
XCF08PFS48C	XCF08PVO48C
XCF16PFS48C	XCF16PVO48C

## Key Dates

Table 2: Key Dates and Ordering Information

Time Period	Current Mask Set	New Mask Set
March 20, 2006 - June 20, 2006	Standard ordering code	SCD0973 ordering code
After June 20, 2006	Standard ordering code (cross ship period until current mask set inventory is depleted)	

Products manufactured at STMicroelectronics with the new mask set are now available. Customers who would like to receive only die with the new mask set manufactured by STMicroelectronics prior to June 20, 2006 should use special ordering code SCD0973. To use SCD0973, append "0973" to the end of the standard ordering part number (e.g., XCF16PVO48C0973). The ordering code "0973" will not be marked on the package top mark.

Xilinx will begin shipping production devices manufactured by STMicroelectronics with the new mask set starting June 20, 2006. After this date, customers ordering the standard part number may receive product manufactured with the current or new mask set, until the current mask set inventory is depleted.

Customers who need material from the current mask set prior to June 20, 2006 should continue to order the standard part number. Only product manufactured with the current mask set will be used to fulfill standard orders prior to June 20, 2006.

## Traceability

The devices manufactured at STMicroelectronics with mask set modification can be distinguished both visually and electrically.

The devices can be distinguished visually with the top mark “ART” for the current mask set devices and “BRT” for the new mask set devices.



Figure 1: Sample Top Mark for the 48-pin TSOP Package

## Actual Package Photos



Figure 2: Current mask set devices



Figure 3: New mask set devices

The devices can be distinguished electrically by the IDCODE:

Table 3: IDCODEs

Device	Current Mask Set STMicroelectronics IDCODE	New Mask Set STMicroelectronics IDCODE
XCF08P	F5057093h	E5057093h
XCF16P	F5058093h	E5058093h

The current version of the programming algorithms ignore this field (revision field), so customers do not need to update their programming algorithms.

## Qualification and Characterization Data

Qualification and technical data associated with this change are available in *Qualification of XCF08P/XCF16P Design Modification (RPT045)*, <http://www.xilinx.com/bvdocs/reports/rpt045.pdf>.

## Response

No response is required. For additional information or questions, please contact Xilinx Technical Support.

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## Revision History

The following table shows the revision history for this document.

Date	Version	Revision
3/20/06	1.0	Initial Xilinx release.