



## Package Substrate Change for Spartan-3A and Spartan-3AN Devices

XCN08014 (v1.0) October 20, 2008

Product/Process Change Notice

### Overview

The purpose of this notification is to announce the standardization of remaining Spartan<sup>®</sup>-3A devices to the current 2-layer substrate for the FT256 and FTG256 packages. All other Spartan FT/FTG256 devices currently use 2-layer substrates including Spartan-3A XC3S700A and XC3S1400A devices.

### Description

This change is to standardize FT256 and FTG256 in the XC3S50A, XC3S200A/AN and XC3S400A to the current 2-layer substrate package construction. All other Spartan devices, including the XC3S700A and XC3S1400A, use a 2-layer substrate for the FT256 and FTG256 packages. The 2-layer substrate has equivalent performance and specifications to the 4-layer substrate and there is no difference in the fit or form between these substrates.

### Products Affected

This change affects all speed and temperature variations of the commercial (C) and industrial (I) grade devices for the three affected devices using the FT256 or FTG256 package. Automotive XA I and Q grade devices are already offered in 2-layer and are not affected.

Table 1: Affected Part Numbers

XC3S50A-4FT256C	XC3S50A-4FT256I	XC3S50A-5FT256C
XC3S50A-4FTG256C	XC3S50A-4FTG256I	XC3S50A-5FTG256C
XC3S200A-4FT256C	XC3S200A-4FT256I	XC3S200A-5FT256C
XC3S200A-4FTG256C	XC3S200A-4FTG256I	XC3S200A-5FTG256C
XC3S200AN-4FT256C	XC3S200AN-4FT256I	XC3S200AN-5FT256C
XC3S200AN-4FTG256C	XC3S200AN-4FTG256I	XC3S200AN-5FTG256C
XC3S400A-4FT256C	XC3S400A-4FT256I	XC3S400A-5FT256C
XC3S400A-4FTG256C	XC3S400A-4FTG256I	XC3S400A-5FTG256C

#### NOTES:

1) SCD part numbers based on these base part numbers will also be affected.

## Key Dates and Ordering Information

Xilinx will begin to cross-ship the 2-layer package on or after January 10, 2009. At that time, shipment of either or both 2-layer and 4-layer substrate material may occur.

## Response

**Note:** In accordance with JESD46-C, this change is deemed accepted by the customer if no acknowledgement is received within 30 days from this notice.

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

**Important Notice:** Xilinx Customer Notices (PCNs, PDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your “MyAlerts” area to include Customer Notices. This change provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

## Additional Documentation

Qualification Report (RPT112):

<https://secure.xilinx.com/webreg/clickthrough.do?cid=113468>

Additional Information for XCN08014: Q&A (XTP036):

<https://secure.xilinx.com/webreg/clickthrough.do?cid=113469>

---

## Revision History

The following table shows the revision history for this document.

Date	Version	Revision
10/20/08	1.0	Initial release.