



Product Discontinuation Notice AccelDSP Synthesis Tool

XCN09018 (v1.0.1) July 31, 2009

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx will not be enhancing the AccelDSP™ synthesis tool and starting with ISE® Design Suite 12.1 will be discontinuing the AccelDSP synthesis tool as part of the DSP Edition and System Edition of ISE Design Suite. System Generator for DSP will continue to be included in the DSP and System Edition which is recommended for DSP design of Xilinx devices.

Description

The contents of the DSP and System Edition, beginning with ISE Design Suite 12.1, will change to not include the AccelDSP synthesis tool. System Generator for DSP will continue to be included in both Editions of ISE Design Suite for DSP design.

AccelDSP 11.2 will include support for the Spartan-6 and Virtex-6 families of devices and will be the last release with new enhancements. AccelDSP 11.3 and 11.4 will be tested to ensure compatibility with the rest of ISE Design Suite 11.3 and 11.4. Only critical bugs will be fixed in 11.3 and 11.4.

For customers of record, AccelDSP 11.4 will be supported for 1 year after it is released.

Part Numbers Affected

The following part numbers in Table 1 have been affected. These part numbers are still active but no longer contain AccelDSP. The price of these part numbers have been adjusted to reflect the removal of AccelDSP

Table 1:

Part Number	Description
EF-ISE-DSP-NL	ISE Design Suite: DSP Edition, Node-Locked License
EF-ISE-DSP-FL	ISE Design Suite: DSP Edition, Floating License
EF-ISE-SYSTEM-NL	ISE Design Suite: System Edition, Node-Locked License
EF-ISE-SYSTEM-FL	ISE Design Suite: System Edition, Floating License
EFR-ISE-DSP-NL	ISE Design Suite: DSP Edition, Node-Locked License Renewal
EFR-ISE-DSP-FL	ISE Design Suite: DSP Edition, Floating License Renewal
EFR-ISE-SYSTEM-NL	ISE Design Suite: System Edition, Node-Locked License Renewal
EFR-ISE-SYSTEM-FL	ISE Design Suite: System Edition, Floating License Renewal
EF-DSP-PC-NL	DSP Bundle: System Generator for DSP and AccelDSP Synthesis Tool, Node-Locked License
EF-DSP-PC-FL	DSP Bundle: System Generator for DSP and AccelDSP Synthesis Tool, Floating License
EFR-DSP-PC-NL	DSP Bundle: System Generator for DSP and AccelDSP Synthesis Tool, Node-Locked License Renewal
EFR-DSP-PC-FL	DSP Bundle: System Generator for DSP and AccelDSP Synthesis Tool, Floating License Renewal

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Table 1 (Continued):

Part Number	Description
UEF-ISE-DSP-25	University ISE Design Suite: DSP Edition, 25 License Bundle
UEF-ISE-DSP-50	University ISE Design Suite: DSP Edition, 50 License Bundle
UEF-ISE-DSP-100	University ISE Design Suite: DSP Edition, 100 License Bundle
UEFR-ISE-DSP-25	University ISE Design Suite: DSP Edition, 25 License Bundle Renewal
UEFR-ISE-DSP-50	University ISE Design Suite: DSP Edition, 50 License Bundle Renewal
UEFR-ISE-DSP-100	University ISE Design Suite: DSP Edition, 100 License Bundle Renewal
UEF-ISE-SYSTEM-25	University ISE Design Suite: System Edition, 25 License Bundle
UEF-ISE-SYSTEM-50	University ISE Design Suite: System Edition, 50 License Bundle
UEF-ISE-SYSTEM-100	University ISE Design Suite: System Edition, 100 License Bundle
UEFR-ISE-SYSTEM-25	University ISE Design Suite: System Edition, 25 License Bundle Renewal
UEFR-ISE-SYSTEM-50	University ISE Design Suite: System Edition, 50 License Bundle Renewal
UEFR-ISE-SYSTEM-100	University ISE Design Suite: System Edition, 100 License Bundle Renewal
UEF-DSP-PC	University System Generator and AccelDSP
UC-DSP-PC	University Support Contract for System Generator and AccelDSP

Key Dates and Ordering Information

AccelDSP will continue to be included as part of the DSP and System Edition of ISE Design Suite until version 12.1.

For customers of record, AccelDSP 11.4 will be supported for 1 year after it is released.

Response

No response is required.

For technical questions, please go to Xilinx [Technical Support](#).

For any additional product questions, please contact your local [Sales Representative](#) or [Distributor](#).

Note: In accordance with JESD48, this change is deemed accepted by the customer if no acknowledgement is received within 30 days from this notice.

Important Notice: Xilinx Customer Notices (PCNs, PDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your “MyAlerts” area to include Customer Notices. This change provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
06/29/09	1.0	Initial release.
07/31/09	1.0.1	Revise to add explanation to “Parts Affected” section

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