



Product Discontinuation Notice For Virtex-7 HT FPGA HCG Packages

XCN14005 (v1.0) June 16, 2014

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx is discontinuing ceramic substrate packages (package nomenclature HC/HCG) for the Virtex®-7 HT FPGA family of devices and releasing organic substrate packages (package nomenclature FL/FLG) as replacements. This product discontinuance notice (PDN) applies to both **Commercial (C)** and **Extended (E)** temperature grade products.

Description

Xilinx is upgrading the packages provided for the Virtex-7 HT FPGA family of devices by moving to organic substrates for the family. The new FL(G) packages will be released to production in July 2014, are footprint compatible to the ceramic HC(G) packages, and provide better board level reliability thermal cycling characteristics for our customers without the need for special manufacturing flows required for ceramic packages. The package height will change slightly, and because of the different properties of the ceramic vs organic substrates, there will be some differences in electrical characteristics, package flight times, and PCB pad guidelines. These differences will be noted in the Virtex-7 HT Data Sheet (DS184), the Virtex-7 HT Pin and Packaging Specification (UG358) and the Vivado tool suite will contain timing files for both types of packages (Vivado 2013.4).

This notice should be used in conjunction with previous discontinuance notices which can be found at Xilinx.com.

Products Affected

The products affected include all standard part numbers and specification control document (SCD) versions of the standard part numbers listed in this notice.

Table 1: Virtex-7 HT Family FPGA Products Affected

Part Number	Replacement Part	Part Number	Replacement Part
XC7VH580T-1HCG1155C	XC7VH580T-1FLG1155C ¹	XC7VH580T-G2HCG1155E	XC7VH580T-G2FLG1155E ¹
XC7VH580T-2HCG1155C	XC7VH580T-2FLG1155C ¹	XC7VH580T-L2HCG1155E	XC7VH580T-L2FLG1155E ¹

¹ This part will be production released in July 2014.

Key Dates and Ordering Information

Final orders are accepted until December 31, 2014.

Regardless of the date an order was placed, an order that remains open on or after October 31, 2014 will be deemed Non-Cancellable, Non-Returnable (NCNR).

Final deliveries must occur on or before March 31, 2015.

Return Material Authorizations (RMAs) for replacement devices due to quality issues will be accepted until June 30, 2015.

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Technical Support

For Technical Support on these products refer to Solution Center [Xilinx Answer Record 40174](#). Support for these products will be limited to tools usage and existing documentation.

Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis. All orders placed within 60 days of the last time buy date will be non-cancellable and non-returnable (NCNR). For additional information or questions, contact your local Xilinx Sales representative.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<http://www.xilinx.com/support>). Register today and personalize your “Documentation and Design Advisory Alerts” area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
06/16/2014	1.0	Initial release.

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