Substrate Supplier Change for Kintex-7Q, Virtex-7Q and Zynq-7000Q All Programmable Hi-Rel "XQ" FPGAs Flip Chip RF Packages

XCN14013 (v1.0) November 3, 2014 **Product Change Notice**

Overview

The purpose of this notification is to communicate that Xilinx will change substrate supplier for Kintex®-7Q, Virtex®-7Q and Zynq®-7000Q All Programmable Hi-Rel "XQ" FPGAs flip chip RF packages.

Description

The change of this supplier is to ensure continuity of supply for Xilinx flip chip packages. 7 series FPGAs flip chip packages with the changed substrate supplier will have the identical material set, design and dimensions as the current 7 series FPGAs production substrates. There is no change to the form, fit, or function.
Xilinx has three qualified substrate suppliers for qualified use. The Xilinx Hi-Rel "XQ" product line will only use the designated primary substrate supplier. The backup supplier is qualified and can be used with customer notification from Xilinx.

Products Affected

This change affects all speed, package, and temperature variations of Hi-Rel "XQ" grade devices.

Affected devices are included in the following Table 1:

Table 1: **Kintex-7Q, Virtex-7Q and Zynq-7000Q All Programmable Hi-Rel "XQ" Devices Products Affected and Cross-Ship Schedule**

Device Family	Device	Package-Pin	Supplier 1	Supplier 2	Supplier 3	Target Cross Shipping Date
Kintex-7Q	XQ7K325T	RF676	P	--	B	Q1 CY15
		RF900	P	--	B	
	XQ7K410T	RF676	P	--	B	
		RF900	P	--	B	
Virtex-7Q	XQ7V585T	RF1157	P	--	B	
		RF1761	--	P	B	
	XQ7VX330T	RF1157	P	--	B	
		RF1761	--	P	B	
	XQ7VX485T	RF1761	--	P	B	
		RF1930	--	P	B	
	XQ7VX690T	RF1157	P	--	B	
		RF1761	--	P	B	
		RF1930	--	P	B	
	XQ7VX980T	RF1930	--	P	B	
Zynq-7000Q	XQ7Z045	RF676	P	--	B	
		RF900	P	--	B	
	XQ7Z030	RF676	P	--	B	

Note: P is Primary Source, B is Backup Source, -- is Not a Source.

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Key Dates and Ordering Information

Xilinx will begin to cross-ship product after 90 days from the release of this PCN.

Qualification Data

Xilinx has successfully passed and completed the qualification. Please refer to Xilinx RPT204 for more details.

Response

No response is required. For additional information or questions, please contact Xilinx Technical Support.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (http://www.xilinx.com/support). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Answer Record 18683: http://www.xilinx.com/support/answers/18683.htm.

Additional Documentation

Qualification Report (RPT204):
https://secure.xilinx.com/webreg/clickthrough.do?cid=370836.

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
11/03/2014	1.0	Initial release.

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