

Overview

To ensure business continuity and enable high volume supply chain capabilities for all Virtex[®]-6 and selected 7 series FPGAs product families, Xilinx is qualifying an additional substrate supplier, Unimicron Technology Corporation (UMTC) for flip chip ball grid area (FCBGA) packages.

For Virtex-6 FPGAs, this change affects all standard and specification control document (SCD) XC Commercial (C) and Industrial (I) grade devices. Hi-Rel “XQ” devices are not affected by this PCN.

For Artix[®]-7, Zynq[®]-7000 All Programmable, Virtex[®]-7 and Kintex[®]-7 in the SB, FB, FF, SBG, FBG and FFG packages, this change affects all standard and specification control document (SCD) XC Commercial (C) grade, Extended (E) grade and Industrial (I) grade devices. Virtex-7 in the FL, FLG, FH and FHG packages, and Automotive “XA” devices for 7 series FPGAs are not affected by this PCN. Kintex[®]-7Q, Virtex[®]-7Q and Zynq[®]-7000Q All Programmable Hi-Rel “XQ” FPGAs RF flip chip packages are affected (Refer to [XCN14013](#)).

This additional supplier will adhere to the same performance, quality and reliability specifications that apply to all product families proven through extensive qualification and testing. As a result, there is no change in form, fit, function, or reliability with this substrate supplier addition.

FAQs

Q: What is the change?

Xilinx is qualifying an additional substrate supplier, Unimicron Technology Corporation (UMTC) for flip chip ball grid area (FCBGA) packages for all Virtex-6 and selected 7 series FPGAs product families. UMTC is a reputable company supplying component substrate and system printed circuit board to many semiconductor customers and original equipment make (OEM) customers for over 10 years.

Q: Why is Xilinx making this change?

This change ensures business continuity and enables high volume supply chain capabilities for Xilinx product families.

Q: Why adding Phase 3?

As a result of the successful implementation for Xilinx FPGAs (Virtex-6 FPGAs and 7-series), we are expanding this program to include SoC (Zynq-7000 All Programmable) to this change.

Q: Which products are affected?

For Virtex-6 FPGAs, this change affects all standard and specification control document (SCD) XC Commercial (C) and Industrial (I) grade devices. Hi-Rel “XQ” devices in the FFG1156 package are not affected by this PCN.

For Artix-7, Zynq-7000 All Programmable, Virtex-7 and Kintex-7 in the SB, FB, FF, SBG, FBG and FFG packages, this change affects all standard and specification control document (SCD) XC Commercial (C) grade, Extended (E) grade and Industrial (I) grade devices. Virtex-7 in the FL, FLG, FH and FHG packages, and Automotive “XA” devices

for 7 series FPGAs are not affected by this PCN. Kintex-7Q, Virtex-7Q and Zynq-7000Q All Programmable Hi-Rel “XQ” FPGAs RF flip chip packages are affected (Refer to [XCN14013](#)).

Affected device package-pin are listed in the [Table 1](#), [Table 2](#), [Table 3](#), [Table 4](#) and [Table 5](#) below:

Table 1: Virtex-6 FPGAs Devices Package Product Affected

Device	Package-Pin	Device	Package-Pin	Device	Package-Pin
XC6VCX130T	FF(G)784 ⁽¹⁾	XC6VHX565T	FF(G)1923 ⁽¹⁾	XC6VLX760	FF(G)1760 ⁽¹⁾
	FF(G)1156 ⁽¹⁾		FF(G)1924 ⁽¹⁾		XC6VSX315T
XC6VCX195T	FF(G)784 ⁽¹⁾	XC6VLX130T	FF(G)784 ⁽¹⁾	XC6VSX475T	
	FF(G)1156 ⁽¹⁾		FF(G)1156 ⁽¹⁾		FF(G)1156 ⁽¹⁾
XC6VCX240T	FF(G)784 ⁽¹⁾	XC6VLX195T	FF(G)784 ⁽¹⁾		
	FF(G)1156 ⁽¹⁾		FF(G)1156 ⁽¹⁾		
XC6VCX75T	FF(G)784 ⁽¹⁾	XC6VLX240T	FF(G)784 ⁽¹⁾		
XC6VHX250T	FF(G)1154 ⁽¹⁾		FF(G)1156 ⁽¹⁾		
XC6VHX255T	FF(G)1155 ⁽¹⁾		FF(G)1759 ⁽¹⁾		
	FF(G)1923 ⁽¹⁾	XC6VLX365T	FF(G)1156 ⁽¹⁾		
XC6VHX380T	FF(G)1154 ⁽¹⁾		FF(G)1759 ⁽¹⁾		
	FF(G)1155 ⁽¹⁾	XC6VLX550T	FF(G)1759 ⁽¹⁾		
	FF(G)1923 ⁽¹⁾		FF(G)1760 ⁽¹⁾		
	FF(G)1924 ⁽¹⁾	XC6VLX75T	FF(G)784 ⁽¹⁾		

Table 2: Artix-7 FPGAs Devices Package Product Affected

Device	Package-Pin
XC7A200T	FF(G)1156 ⁽²⁾
	FB(G)484 ⁽³⁾
	FB(G)676 ⁽³⁾
	SB(G)484 ⁽³⁾

Table 3: Kintex-7 FPGAs Devices Package Product Affected

Device	Package-Pin	Device	Package-Pin	Device	Package-Pin
XC7K70T	FB(G)484 ⁽³⁾	XC7K325T	FF(G)676 ⁽¹⁾	XC7K410T	FF(G)676 ⁽²⁾
	FB(G)676 ⁽³⁾		FF(G)900 ⁽¹⁾		FF(G)900 ⁽²⁾
XC7K160T	FF(G)676 ⁽²⁾		FB(G)676 ⁽³⁾		FB(G)676 ⁽³⁾
	FB(G)484 ⁽³⁾		FB(G)900 ⁽³⁾		FB(G)900 ⁽³⁾
	FB(G)676 ⁽³⁾	XC7K355T	FF(G)901 ⁽²⁾	XC7K420T	FF(G)901 ⁽²⁾
					FF(G)1156 ⁽²⁾
				XC7K480T	FF(G)901 ⁽²⁾
					FF(G)1156 ⁽²⁾

Table 4: Virtex-7 FPGAs Devices Package Product Affected

Device	Package-Pin	Device	Package-Pin	Device	Package-Pin		
XC7V585T	FF(G)1157 ⁽²⁾	XC7VX485T	FF(G)1157 ⁽²⁾	XC7VX690T	FF(G)1157 ⁽²⁾		
	FF(G)1761 ⁽²⁾		FF(G)1158 ⁽²⁾		FF(G)1158 ⁽²⁾		
XC7VX330T	FF(G)1157 ⁽²⁾		FF(G)1761 ⁽²⁾		FF(G)1761 ⁽²⁾		
	FF(G)1761 ⁽²⁾		FF(G)1927 ⁽²⁾		FF(G)1926 ⁽²⁾		
XC7VX415T	FF(G)1157 ⁽²⁾		FF(G)1930 ⁽²⁾		FF(G)1927 ⁽²⁾		FF(G)1927 ⁽²⁾
	FF(G)1158 ⁽²⁾		XC7VX550T		FF(G)1158 ⁽²⁾		FF(G)1930 ⁽²⁾
	FF(G)1927 ⁽²⁾	FF(G)1927 ⁽²⁾			FF(G)1926 ⁽²⁾		
				XC7VX980T	FF(G)1928 ⁽²⁾		
					FF(G)1930 ⁽²⁾		

Table 5: Zynq-7000 FPGAs Devices Package Product Affected

Device	Package-Pin	Device	Package-Pin	Device	Package-Pin
XC7Z030	FB(G)484 ⁽³⁾	XC7Z045	FB(G)676 ⁽³⁾	XC7Z100	FFG1156 ⁽³⁾
	FB(G)676 ⁽³⁾		FF(G)676 ⁽³⁾		FFG900 ⁽³⁾
	FF(G)676 ⁽³⁾		FF(G)900 ⁽³⁾		
	SB(G)485 ⁽³⁾				
XC7Z035	FB(G)676 ⁽³⁾				
	FF(G)676 ⁽³⁾				
	FF(G)900 ⁽³⁾				

Notes:

- (1) Please refer to [Table 6](#) Phase 1 cross-ship schedule
- (2) Please refer to [Table 7](#) Phase 2 cross-ship schedule
- (3) Please refer to [Table 8](#) Phase 3 cross-ship schedule

* For inquiries about a specific part number, please contact your customer operations representative or CQE representative for any additional questions.

Q: When will this change take effect?

This change will take effect in Q1, CY2015. At that time, Xilinx will start cross shipping all Virtex-6 product families and selected 7 series FPGAs product families from UMTC. This will result in initial production device shipments to customers in the timelines indicated in [Table 6](#) and [Table 7](#) and [Table 8](#) below.

Table 6: Phase 1 - Virtex-6 and Kintex-7 Devices Qualification Completion and Cross-Ship Schedule

Device	Qualification Report Available	PCN Approval	Cross-Ship Date
XC6VCX130T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VCX195T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VCX240T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VCX75T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VHX250T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VHX255T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VHX380T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VHX565T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VLX130T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VLX195T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VLX240T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VLX365T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VLX550T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VLX75T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VLX760	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VSX315T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC6VSX475T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015
XC7K325T	November 3 rd , 2014	March 23 rd , 2015	March 24 th , 2015

Table 7: Phase 2 - Artix-7, Kintex-7 and Virtex-7 Devices Qualification Completion and Cross-Ship Schedule

Device	Qualification Report Available	PCN Approval	Cross-Ship Date
XC7A200T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7K160T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7K355T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7K410T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7K420T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7K480T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7V585T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7VX330T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7VX415T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7VX485T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7VX550T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7VX690T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015
XC7VX980T	November 3 rd , 2014	May 5 th , 2015	May 6 th , 2015

Table 8: Phase 3 - Artix-7, Zynq-7000 All Programmable, Kintex-7 and Virtex-7 Cross-Ship Schedule

Device	Qualification Report Available	PCN Approval	Cross-Ship Date
XC7A200T-FBG484	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7A200T-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7A200T-SBG484	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K160T-FBG484	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K160T-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K325T-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K325T-FBG900	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K410T-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K410T-FBG900	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K70T-FBG484	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7K70T-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z030-FBG484	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z030-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z030-FFG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z030-SBG485	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z045-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z045-FFG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z045-FFG900	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z035-FBG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z035-FFG676	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z035-FFG900	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z100-FFG1156	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016
XC7Z100-FFG900	November 3 rd , 2014	October 2 nd , 2015	January 1 st , 2016

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
11/03/2014	1.0	Initial release.
11/10/2014	1.1	Minor update on Table 1 to remove FF(G)1759 package from XC6VCX240T device.
03/30/2015	2.0	Added Zynq-7000 family, Artix-7 SB(G) and FB(G) package and Kintex-7 FB(G) package for Phase 3 release.

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