





### Example Base Address, High Address Specifications

The base address (C\_BASEADDR) and high address (C\_HIGHADDR) must specify a valid range for the BRAM that is attached to the BRAM Controller. The range (C\_HIGHADDR – C\_BASEADDR) specified by the high address and base address must be equal to  $2^n$  bytes, where  $n$  is a positive integer and  $2^n$  is a valid memory size as shown above. In addition, the  $n$  least significant bits of C\_BASEADDR must be equal to 0.:

Table 4: Example Address Range Specifications

| Memory Size (Bytes) | C_BASEADDR | C_HIGHADDR |
|---------------------|------------|------------|
| 2K                  | 0x10000000 | 0x100007FF |
| 4K                  | 0x50000000 | 0x50000FFF |
| 8K                  | 0x24000000 | 0x24001FFF |
| 16K                 | 0xE0000000 | 0xE0003FFF |
| 32K                 | 0x3FF00000 | 0x3FF07FFF |
| 64K                 | 0x82000000 | 0x8200FFFF |
| 128K                | 0xB0000000 | 0xB001FFFF |

Discontinued IP





















