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The SPI-4.2 Lite core is provided under the [SignOnce IP Site License](#) and can be generated using the Xilinx CORE Generator system v12.1 or higher. The CORE Generator system is shipped with Xilinx ISE Foundation™ Series Development software.

A simulation evaluation license for the core is shipped with the CORE Generator system. To access the full functionality of the core, including FPGA bitstream generation, a full license must be obtained from Xilinx. For more information, please visit the [SPI-4.2 Product Page](#).

Please contact your local Xilinx [sales representative](#) for pricing and availability of additional Xilinx cores and software. Information about additional Xilinx LogiCORE modules is available on the Xilinx [IP Center](#).

## Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
8/31/05	1.0	Initial Xilinx release.
1/18/06	1.1	Updated the release date, product version and Xilinx tools version.
7/13/06	2.0	Added support for Virtex-5 devices, updated product version to 4.1, release date, ISE to v8.2i
2/15/07	3.0	Added support for Spartan-3A/3AN devices, product version advanced to 4.2, ISE to v9.1i
4/07/07	3.1	Added support for Spartan-3A DSP devices.
6/27/08	4.0	Updated for ISE v10.1 release.
4/24/09	4.4	Updated core to v4.4 and ISE to v11.1.
6/24/09	4.5	Added support for Virtex-6 and Spartan-6 devices. Removed support for Virtex-II and Virtex-II Pro devices. Updated core to v5.1 and ISE to v11.2.
4/19/10	5.0	Updated core to v5.2 and ISE to v12.1.

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