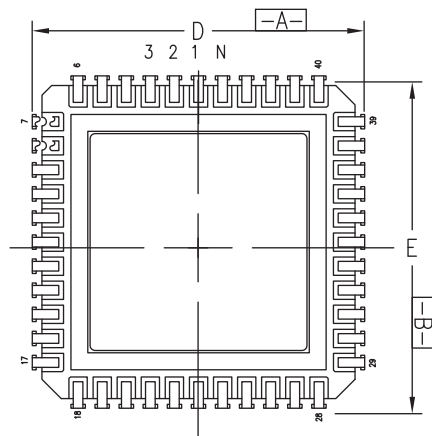
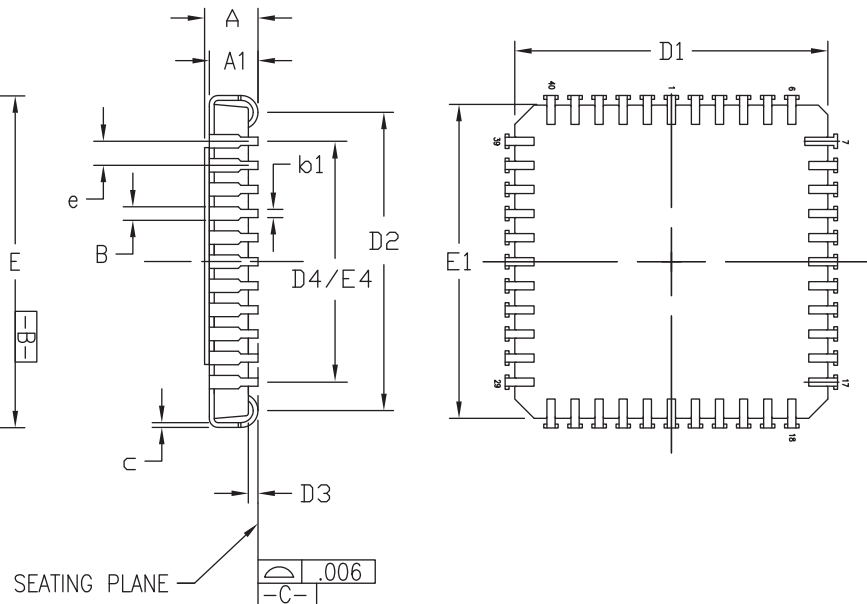


TOP VIEW



BOTTOM VIEW



SYMBOL	INCHES		
	MIN	NOM	MAX
A	.105	.123	.138
A1	.090	.113	.123
B	.026	.030	.034
b1	.014	.017	.021
c	.006	.008	.012
D/E	.679	.690	.695
D1/E1	.635	.650	.665
D2	.610	.630	.650
D3	.030	.040	.050
D4/E4	.500 REF.		
e	.050 BSC		
N	44		

NOTES:

1. ALL DIMENSIONS AND TOLERANCES CONFORM TO ANSI Y14.5M-1982.
2. LEAD WIDTH DIMENSIONS INCLUDE LEAD TRIM OFFSET and LEAD FINISH
LEAD FINISH: Gold (60 Micro Inches minimum) over Nickel
3. SYMBOL 'N' IS THE NUMBER OF TERMINALS.
4. THIS PACKAGE MEETS DIMENSIONAL REQUIREMENTS OF JEDEC MO-087-AB

Revision History

The following table shows the revision history for this document.

Date	Version	Description of Revisions
02/05/2018	1.0	Initial Xilinx release.

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