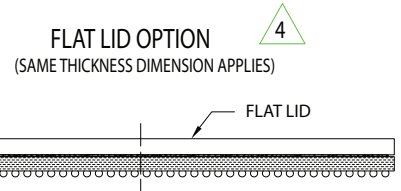


FF1760 - Sn/Pb SOLDER BALLS
FFG1760 - Sn/Ag/Cu SOLDER BALLS

SYMBOL	MILLIMETERS			NOTE
	MIN.	NOM.	MAX.	
A	2.80	<i>h</i>	3.50	2
A ₁	0.40	0.50	0.60	
A ₂	<i>h</i>	<i>h</i>	1.50	
D/E	42.50 BASIC			
D ₁ /E ₁	41.00 REF			
e	1.00 BASIC			
Øb	0.50	0.60	0.70	
aaa	<i>h</i>	<i>h</i>	0.20	
bbb	<i>h</i>	<i>h</i>	0.25	
ddd	<i>h</i>	<i>h</i>	0.25	
eee	<i>h</i>	<i>h</i>	0.10	
M	42			



NOTES:

1. ALL DIMENSIONS AND TOLERANCES CONFORM TO ASME Y14.5M-1994
2. SYMBOL "M" IS THE PIN MATRIX SIZE.
3. CONFORMS TO JEDEC MS-034-AAV-1 (DEPOPULATED)
- 4 FLAT LID WILL BE USED ONLY FOR VIRTEX-6 EXCEPT XC6VLX240T

Revision History

The following table shows the revision history for this document.

Date	Version	Description of Revisions
12/03/10	1.0	Initial Xilinx release.

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