

Libraries Guides

The various *Libraries Guides* contain information about the Xilinx Unified Libraries design elements, including macros and primitives. Each guide targets a specific device family and design entry method, and covers the following:

- Design entry methods
- Functional categories for design elements
- Design element information

Note HDL guides also contain instantiation code that you can copy and paste into your projects.

The following *Libraries Guides* are available:

- [CPLD Libraries Guide](#)
- [Spartan®-3 Libraries Guide for HDL Designs](#)
- [Spartan-3 Libraries Guide for Schematic Designs](#)
- [Spartan-3A and Spartan-3A DSP Libraries Guide for HDL Designs](#)
- [Spartan-3A and Spartan-3A DSP Libraries Guide for Schematic Designs](#)
- [Spartan-3E Libraries Guide for HDL Designs](#)
- [Spartan-3E Libraries Guide for Schematic designs](#)
- [Spartan-6 Libraries Guide for HDL Designs](#)
- [Spartan-6 Libraries Guide for Schematic Designs](#)
- [Virtex®-4 Libraries Guide for HDL Designs](#)
- [Virtex-4 Libraries Guide for Schematic Designs](#)
- [Virtex-5 Libraries Guide for HDL Designs](#)
- [Virtex-5 Libraries Guide for Schematic Designs](#)
- [Virtex-6 Libraries Guide for HDL Designs](#)
- [Virtex-6 Libraries Guide for Schematic Designs](#)

Note These guides are available from the ISE® [Software Manuals](#) collection, automatically installed with your ISE software. To open the Software Manuals collection, select **Help > Software Manuals**. The [Software Manuals](#) collection is also available from the Xilinx® website.