

Zynq-7000 All Programmable SoC

Embedded Design Tutorial

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This tutorial was validated with the 2017.2 tool release. Minor procedural differences might be required when using this tutorial with the 2017.1 version of the Xilinx tools.

For the 2017.2 version of this document, see www.xilinx.com/support/documentation/sw_manuals/xilinx2017_2/ug1165-zynq-embedded-design-tutorial.pdf.