

# Zynq-7000 All Programmable SoC

## *Embedded Design Tutorial*

**UG1165 (v2017.2) June 29, 2017**



This tutorial was validated with the 2017.3 tool release. Minor procedural differences might be required when using this tutorial with the 2017.2 version of the Xilinx tools.

You can find the most recent version of this tutorial at [www.xilinx.com/support/documentation/sw\\_manuals/xilinx2017\\_3/ug1165-zynq-embedded-design-tutorial.pdf](http://www.xilinx.com/support/documentation/sw_manuals/xilinx2017_3/ug1165-zynq-embedded-design-tutorial.pdf).