NOTE: REF_CLK In Mode (ETH_REFCLK = 50MHz)
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For more information on the parts used in this design, please refer to:

http://www/analog.com/ad127 (Precision, Micro-power LDO Voltage References in TSOT)
For more information on the parts used in this design, please refer to:
http://www.analog.com/adp2118 (3 A, 1.2 MHz, 600 kHz High Efficiency Synchronous Step-Down DC-to-DC Regulator)
http://www.analog.com/adm1086 (Voltage Sequencer with Active High, Pull-Pull Enable Output)
http://www.analog.com/adp2138 (Compact, 800 mA, 3 MHz, Step-Down DC-to-DC Converter)