

## **ARM - Xilinx SoC Lab-in-a-Box One day workshop**

### **Agenda**

| <b>Time</b>             |   |
|-------------------------|---|
| <b>7:30 – 8:00 AM</b>   | Registration  |
| <b>8:00 – 8:30 AM</b>   | AUP Introduction<br>XUP Introduction<br>Introducing Lab in a Box                              |
| <b>8:30 – 9:00 AM</b>   | Introduction to ARM <sup>®</sup> Cortex <sup>®</sup> -M0 processor & DesignStart <sup>™</sup> |
| <b>9:00 – 9:30 AM</b>   | Overview of AMBA <sup>®</sup> 3 AHB-Lite Protocol   |
| <b>9:30 – 10:00 AM</b>  | Artix-7 FPGA Architecture   |
| <b>10:00 – 10:30 AM</b> | Vivado Design Flow  |
| <b>10:30 – 11:30 PM</b> | Lab 1: Simple AHB Peripheral Design and Integration   |
| <b>11:30 – 12:30 PM</b> | Lunch   |
| <b>12:30 – 1:00 PM</b>  | Introduction to UART Peripheral   |
| <b>1:00 – 1:45 PM</b>   | Lab 2: Integrating UART Peripheral  |
| <b>1:45 – 2:15 PM</b>   | Introduction to Interrupts and CMSIS  |
| <b>2:15 – 3:30 PM</b>   | Lab 3: Integrating UART Peripheral and Interrupt. Use C program to develop interrupt handler  |
| <b>3:30 – 3:45 PM</b>   | Snake Game Application Demo   |
| <b>3:45 – 4:00 PM</b>   | Conclusion, Call for Action   |