

## **ARM - Xilinx SoC Lab-in-a-Box One day workshop**

### **Agenda**

<b>Time</b>	
<b>8:30 – 9:00 AM</b>	Registration
<b>9:00 – 9:30 AM</b>	AUP Introduction XUP Introduction Introducing Lab in a Box
<b>9:30 – 10:00 AM</b>	Introduction to ARM <sup>®</sup> Cortex <sup>®</sup> -M0 processor & DesignStart <sup>™</sup>
<b>10:00 – 10:30 AM</b>	Overview of AMBA <sup>®</sup> 3 AHB-Lite Protocol
<b>10:30 – 11:00 AM</b>	Artix-7 FPGA Architecture
<b>11:00 – 11:30 AM</b>	Vivado Design Flow & XSDK
<b>11:30 – 12:30 PM</b>	Lab 1: Simple AHB Peripheral Design and Integration
<b>12:30 – 1:30 PM</b>	Lunch
<b>1:30 – 2:45 PM</b>	Lab 2: Integrating UART Peripheral
<b>2:45 – 3:15 PM</b>	Introduction to CMSIS
<b>3:15 – 4:30 PM</b>	Lab 3: Integrating UART Peripheral and Interrupt. Use C program to develop interrupt handler
<b>4:30 – 4:45 PM</b>	Snake Game Application Demo
<b>4:45 – 5:00 PM</b>	Conclusion, Call for Action